



# 200929868 Errata Update EFM32JG1/PG1, EFR32BG1/BG1 CSP, EFR32FG1, EFR32MG1/MG1 SF, MG

**Bulletin Issue Date:** 9/29/2020

**Effective Date:** 9/29/2020

## Description of Change

Silicon Labs is pleased to announce the release of the following errata versions for EFM32JG1, EFM32PG1, EFR32BG1, EFR32BG1 CSP, EFR32FG1, EFR32MG1, EFR32MG1 SF and MGM111 devices.

Release of Version 1.7 of the EFM32JG1 Errata  
Release of Version 1.7 of the EFM32PG1 Errata  
Release of Version 1.8 of the EFR32BG1 Errata  
Release of Version 0.4 of the EFR32BG1 CSP Errata  
Release of Version 1.8 of the EFR32FG1 Errata  
Release of Version 1.8 of the EFR32MG1 Errata  
Release of Version 1.8 of the EFR32MG1 SF Errata  
Release of Version 0.4 of the MGM111 Errata

## Reason for Change

Errata item WDOG\_E201: If the device enters EM2, while the clear command is still being synchronized, the watchdog counter may not be cleared as expected. If the watchdog counter is not cleared as expected, the device can encounter a watchdog reset. Workaround: Wait for WDOG\_SYNCBUSY\_CMD to clear before entering EM2. Note that WDOG can be clocked from one of the low-frequency clock sources and will require additional time to enter EM2 when implementing this workaround. There is currently no resolution for this issue.

Other added errata whose details can be found in the new errata version are:

I2C\_E202  
I2C\_E203  
I2C\_E205  
I2C\_E206  
I2C\_E207  
TIMER\_E202  
USART\_E203  
USART\_E204  
USART\_E205  
USART\_E206

## Product Identification

Existing Part #  
EFM32JG1B100F128GM32-C0  
EFM32JG1B100F128GM32-C0R  
EFM32JG1B100F256GM32-C0  
EFM32JG1B100F256GM32-C0R  
EFM32JG1B100F256IM32-C0  
EFM32JG1B100F256IM32-C0R  
EFM32JG1B200F128GM32-C0  
EFM32JG1B200F128GM32-C0R  
EFM32JG1B200F128GM48-C0  
EFM32JG1B200F128GM48-C0R  
EFM32JG1B200F256GM32-C0  
EFM32JG1B200F256GM32-C0R  
EFM32JG1B200F256GM48-C0

EFM32JG1B200F256GM48-C0R  
EFM32JG1B200F256IM32-C0  
EFM32JG1B200F256IM32-C0R  
EFM32JG1B200F256IM48-C0  
EFM32JG1B200F256IM48-C0R  
EFM32JG1B100\*F128GM32-C  
EFM32JG1B100\*F128GM32-CR  
EFM32JG1B100\*GM32-C  
EFM32JG1B100\*GM32-CR  
EFM32PG1B100F128GM32-C0  
EFM32PG1B100F128GM32-C0R  
EFM32PG1B100F256GM32-C0  
EFM32PG1B100F256GM32-C0R  
EFM32PG1B100F256IM32-C0  
EFM32PG1B100F256IM32-C0R  
EFM32PG1B200F128GM32-C0  
EFM32PG1B200F128GM32-C0R  
EFM32PG1B200F128GM48-C0  
EFM32PG1B200F128GM48-C0R  
EFM32PG1B200F256GM32-C0  
EFM32PG1B200F256GM32-C0R  
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EFM32PG1B200F256GM48-C0R  
EFM32PG1B200F256IM32-C0  
EFM32PG1B200F256IM32-C0R  
EFM32PG1B200F256IM48-C0  
EFM32PG1B200F256IM48-C0R  
EFM32PG1B100\*GM32-C  
EFM32PG1B100\*GM32-CR  
EFR32BG1B132F256GD1-C0  
EFR32BG1B132F256GD1-C0R  
EFR32BG1B132F256GM32-C0  
EFR32BG1B132F256GM32-C0R  
EFR32BG1B132F256GM48-C0  
EFR32BG1B132F256GM48-C0R  
EFR32BG1B132F256IM32-C0  
EFR32BG1B132F256IM32-C0R  
EFR32BG1B232F256GM32-C0  
EFR32BG1B232F256GM32-C0R  
EFR32BG1B232F256GM48-C0  
EFR32BG1B232F256GM48-C0R  
EFR32BG1B232F256IM32-C0  
EFR32BG1B232F256IM32-C0R  
EFR32BG1P233F256GM48-C0  
EFR32BG1P233F256GM48-C0R  
EFR32BG1P232F256GM32-C0  
EFR32BG1P232F256GM32-C0R  
EFR32BG1P232F256GM48-C0  
EFR32BG1P232F256GM48-C0R  
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EFR32BG1P333F256GM48-C0  
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EFR32BG1B232F256GJ43-C0  
EFR32BG1B232F256GJ43-C0R  
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EFR32BG1P332F256GJ43-C0R  
EFR32FG1P133F64GM48-C0  
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MGM111A256V2  
MGM111A256V2R  
MGM111E256V2  
MGM111E256V2R

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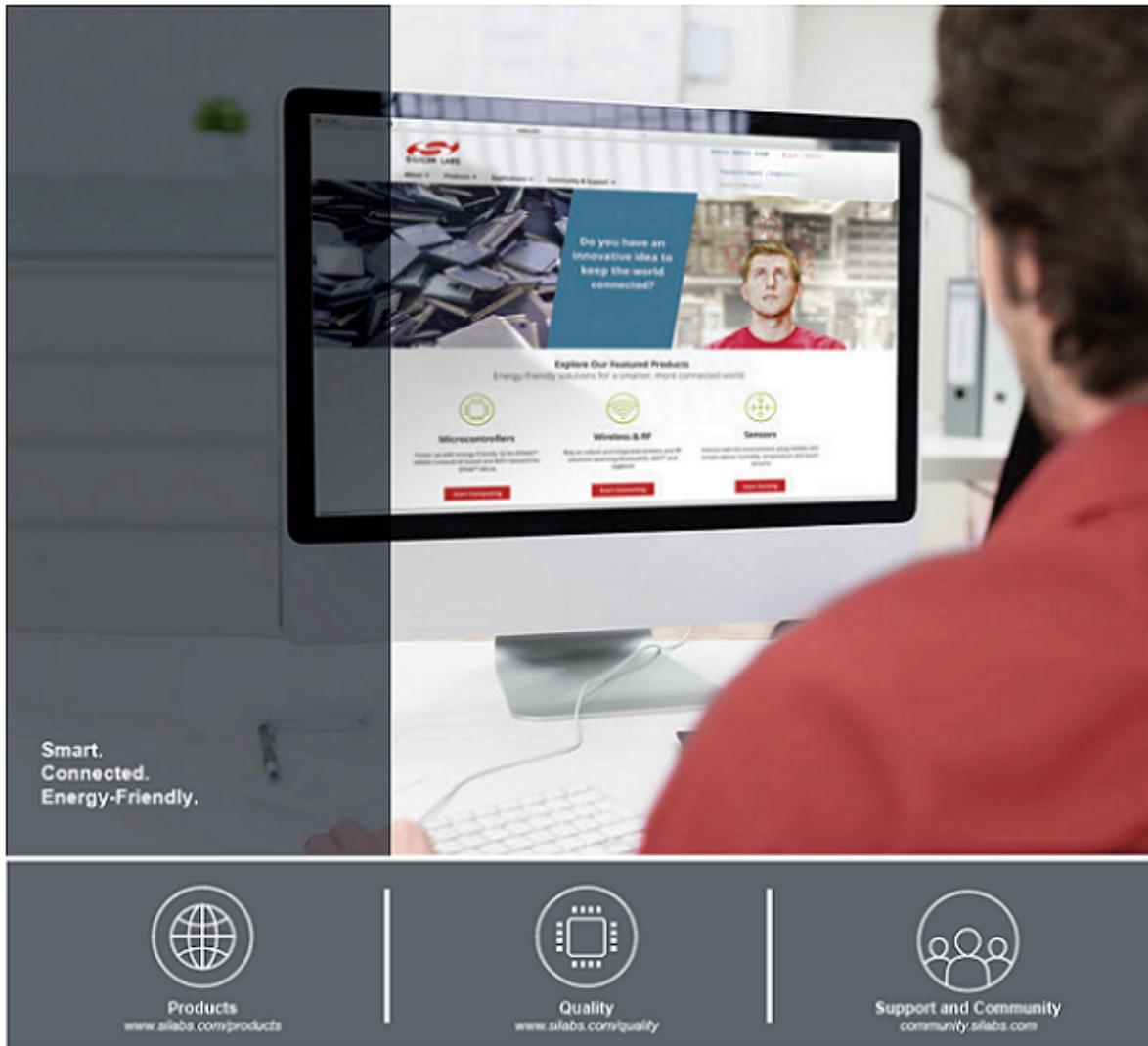
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