Customer Information Notification | 202010024l

Issue Date: 08-Nov-2020
Effective Date: 09-Nov-2020
Dear *Emma Tempest*,

Here's your personalized quality information concerning products Premier Farnell PLC purchased from NXP.
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Change Category

[ ] Wafer Fab Process	[ ] Assembly Process	[ ] Product Marking	[ ] Test Location	[ ] Design
[ ] Wafer Fab Materials	[ ] Assembly Materials	[ ] Mechanical Specification	[ ]Test Process	[X] Errata
[ ] Wafer Fab Location	[ ] Assembly Location	[ ] Packing/Shipping/Labeling	[ ] Test Equipment	[ ] Electrical spec./Test coverage
[ ] Firmware	[ ] Other			

i.MXRT1060/i.MXRT1064 Chip Errata Rev 1.2 Update for 0N00X and Release Errata Rev1.0 for 1N00X

Description

NXP Semiconductors announces chip errata update to revision 1.2 for i.MXRT1060 and i.MXRT1064 sillicon A and release revision 1.0 for i.MXRT1060 and i.MXRT1064 sillicon B. The revision history included in the updated documents provides a detailed description of the changes. Changes are summarized below.

For RT1060/RT1064 Silicon A Chip Errata:
Added following 3 errata:
* ERR050469: SEMC: 8/16bit write to 8bit PSRAM might cause data corruption
* ERR050164: BEE: Unaligned access may cause bus error
* ERR050538: SOC: Potential boot failure on system reset if SJC_DISABLE fuse is blown

Release separate Chip Errata Document for RT1060 Silicon B with removing following errata:

* ERR050101: USB: Endpoint conflict issue in device mode
* ERR006032: FlexCAN: A frame with wrong ID or payload is transmitted into the CAN bus when the Message Buffer under transmission is either aborted or deactivated while the CAN bus is in the Bus Idle state

Release separate Chip Errata Document for RT1064 Silicon B with removing following errata:

* ERR050101: USB: Endpoint conflict issue in device mode

The i.MXRT1060 errata is attached to this notice, and can be found at:
https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/i.mx-rt-series/i.mx-rt1060-crossover-processor-with-arm-cortex-m7-core:i.MX-RT1060?tab=Documentation_Tab&linkline=Errata

The i.MXRT1064 errata is attached to this notice, and can be found at:
https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/i.mx-rt-series/i.mx-rt1064-crossover-processor-with-arm-cortex-m7-core:i.MX-RT1064?tab=Documentation_Tab&linkline=Errata

Reason
The errata was added or removed for additional technical clarification on some device features.
Identification of Affected Products
Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality
No impact on form, fit, function, reliability or quality.

Additional information
Affected products and sales history information: see attached file
Additional documents: view online

Contact and Support
For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:
Name   technical support
e-mail address   tech.support@nxp.com
At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards.
Customer Focus, Passion to Win.

NXP Quality Management Team.

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