MicroZed SOM and Evaluation Kit
Product Change Notification PCN20005

Products Affected:
- AES-Z7MB-7Z010-G
- AES-Z7MB-7Z010-SOM-G
- AES-Z7MB-7Z010-SOM-I-G
- AES-Z7MB-7Z020-SOM-G
- AES-Z7MB-7Z020-SOM-I-G
- Any Semi-custom boards designed by Avnet based on MicroZed.

Subject:
Design update from Revision G to Revision H supporting
- lifecycle modifications
- circuit enhancements

Last Time Buy:
Lifetime buys of Revision G MicroZed SOMs will be accepted through 31 December 2020 with deliveries scheduled through 01 April 2021.

Changes:

Change Description	Reason for Change
1) Added PG_MODULE signal to control the PS_CLK oscillator U10 signal STBY_N.	Provide enhanced eFuse integrity support. See the following answer record: https://www.xilinx.com/support/answers/65240.html
2) Translate voltage level of PS_SRST# to QSPI device U7 RST# pin.	Proper voltage level selection of QSPI reset rather than relying on internal pull-up to perform the reset.
3) Added a capacitor to PS_SRST# push button SW2.	Provide capacitance for debouncing the push button reset switch.
4) Updated the AND gate device U20.	Updated device has better electrical characteristics.
5) Added secondary USB ESD protection devices F1/D13/VR1.	Existing USB ESD protection polyzen device D12 is nearing end of life and this provides path to replacement devices.
6) Added a voltage divider to the VBUS rail of USB UART device U2.	Existing USB UART device U2 is nearing end of life and this provides path to replacement device.

7)	Modified RC values for ETH RESET delay time constant.	Previous RC time constant on ethernet reset did not meet data sheet requirement for 10ms delay.
8)	Removed ETH RESET translator circuit components Q6 and R105.	New device U21 with enhanced electrical characteristics allowed for the elimination of the FET translator circuit.
9)	Updated the AND gate device U21.	Updated device has better electrical characteristics.
10)	Updated power supply on U21 from 3.3V to 1.8V.	New device U21 with enhanced electrical characteristics allowed for operation at 1.8V instead of 3.3V.
11)	Updated power supply ICs U14/U15/U16/U17 to TLV62130A from TLV62130.	Provide enhanced eFuse integrity support due to more robust POWER GOOD output in response to the following answer record: https://www.xilinx.com/support/answers/65240.html
12)	Added GATE U22 to PG_MODULE output.	Provide enhanced eFuse integrity support due to more robust POWER GOOD output in response to the following answer record: https://www.xilinx.com/support/answers/65240.html
13)	Updated several silkscreened logos.	Update PCB silkscreen logos to current standard.

Further Questions? Contact your local Avnet Sales office.
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