

#### 12V N-CHANNEL ENHANCEMENT MODE MOSFET

## **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
40)/	8mΩ @ V <sub>GS</sub> = 4.5V	12.2A
12V	12.5mΩ @ $V_{GS} = 2.5V$	10.4A

### **Description**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

## **Applications**

- Battery Management Application
- Power Management Functions
- DC-DC Converters

#### **Features**

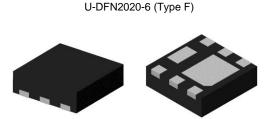
- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm<sup>2</sup>
- Low Gate Threshold Voltage
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part.
   A listing can be found at

https://www.diodes.com/products/automotive/automotive-products/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.
 <a href="https://www.diodes.com/quality/product-definitions/">https://www.diodes.com/quality/product-definitions/</a>

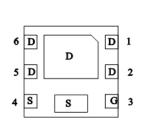
#### **Mechanical Data**

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.0065 grams (Approximate)

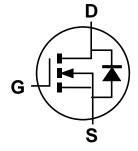


Top View

**Bottom View** 



Pin Out Bottom View



Internal Schematic

### Ordering Information (Note 4)

Part Number	Reel Size (inches)	Case	Quantity per Reel
DMN1008UFDF-7	7	U-DFN2020-6 (Type F)	3,000
DMN1008UFDF-13	13	U-DFN2020-6 (Type F)	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



# **Marking Information**

Site 1



8N = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Key

Date Code Ney												
Year	2015		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	С		Н	ı	J	K	L	М	N	0	Р	R
		1		1						I		
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Site 2



8N = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 0 = 2020) W = Week (ex: a = Week 27; z Represents Week 52 and 53) X = Internal Code (ex: U = Monday)

Date Code Key

Year	2015	 2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	5	 0	1	2	3	4	5	6	7	8	9

Week	1-26	27-52	53
Code	A-Z	a-z	z

Ī	Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Ī	Code	Т	U	V	W	X	Υ	Z



## **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	$V_{DSS}$	12	V		
Gate-Source Voltage	V <sub>GSS</sub>	±8	V		
Continuous Drain Current, V <sub>GS</sub> = 4.5V (Note 6)	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	12.2 9.8	А
Pulsed Drain Current (380µs Pulse, Duty Cycle = 1%	6)		I <sub>DM</sub>	60	Α
Continuous Source-Drain Diode Current (Note 6)		T <sub>A</sub> = +25°C	Is	1.8	Α
Avalanche Current, L = 0.1mH (Note 7)	I <sub>AS</sub>	16.4	Α		
Avalanche Energy, L = 0.1mH (Note 7)			E <sub>AS</sub>	13.5	mJ

# **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Total Dawar Discipation (Note 5)	T <sub>A</sub> = +25°C	D	0.7	W
Total Power Dissipation (Note 5)	$T_A = +70^{\circ}C$	$P_{D}$	0.4	VV
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R <sub>0JA</sub>	168	°C/W
Total Davier Discipation (Note C)	T <sub>A</sub> = +25°C		1.7	10/
Total Power Dissipation (Note 6)	T <sub>A</sub> = +70°C	P <sub>D</sub>	1.0	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	74	°C/W
Thermal Resistance, Junction to Case (Note 6)		R <sub>0JC</sub>	12	°C/W
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

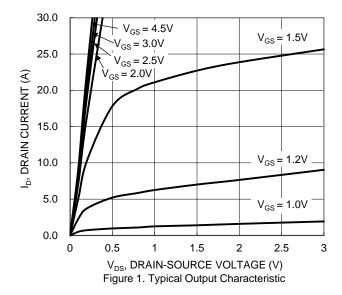
# **Electrical Characteristics** ( $@T_A = +25$ °C, unless otherwise specified.)

			•			
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	12		_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	_	-	1	μA	$V_{DS} = 9.6V, V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 8V$ , $V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.3	-	1.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
			6.6	8		$V_{GS} = 4.5V, I_D = 5A$
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	7.6	11	mΩ	$V_{GS} = 3.0V, I_D = 5A$
	, ,		8.5	12.5		$V_{GS} = 2.5V, I_D = 5A$
Diode Forward Voltage	V <sub>SD</sub>	_	0.7	1.2	V	$V_{GS} = 0V$ , $I_S = 5A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C <sub>iss</sub>	_	995	_		., ., ., .,
Output Capacitance	Coss	_	305	_	pF	$V_{DS} = 6V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	C <sub>rss</sub>	_	270	_		I = 1.0IVIHZ
Gate Resistance	$R_g$	_	1.5	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$
Total Gate Charge (V <sub>GS</sub> = 4.5V)	$Q_g$	_	13.6	_		
Total Gate Charge (V <sub>GS</sub> = 8V)	$Q_g$	_	23.4	_	nC	$V_{DS} = 6V, I_{D} = 5A$
Gate-Source Charge	$Q_{gs}$	_	1.3	_	IIC	$V_{DS} = 6V$ , $I_D = 5A$
Gate-Drain Charge	$Q_{gd}$	_	3.3	_		
Turn-On Delay Time	t <sub>D(ON)</sub>	_	3.5	_		
Turn-On Rise Time	t <sub>R</sub>	_	6.6	_	ns	$V_{DS} = 6V, V_{GS} = 4.5V,$
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	17.5	_	115	$R_G = 2\Omega$ , $I_D = 5A$
Turn-Off Fall Time	t <sub>F</sub>	_	7.5	_		
Reverse Recovery Time	t <sub>RR</sub>	_	15	_	ns	I <sub>F</sub> = 5A, di/dt = 200A/μs
Reverse Recovery Charge	$Q_{RR}$	_	4	_	nC	I <sub>F</sub> = 5A, di/dt = 200A/μs

Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 I<sub>AS</sub> and E<sub>AS</sub> ratings are based on low frequency and duty cycles to keep T<sub>J</sub> = +25°C.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.







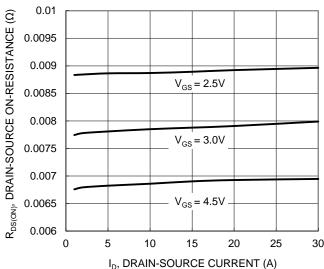


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

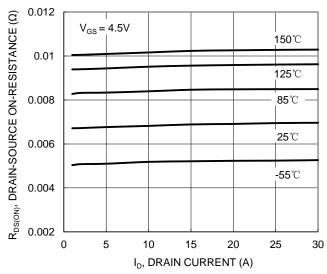


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

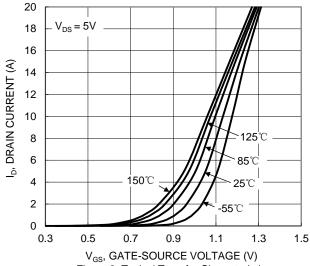


Figure 2. Typical Transfer Characteristic

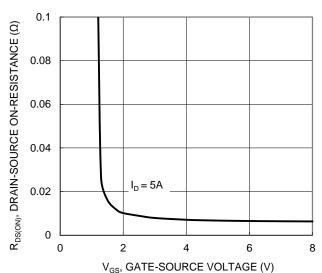


Figure 4. Typical Transfer Characteristic

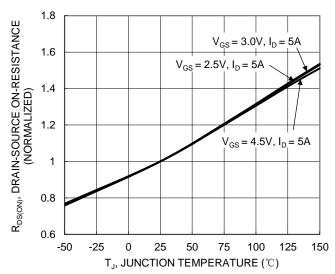


Figure 6. On-Resistance Variation with Junction Temperature





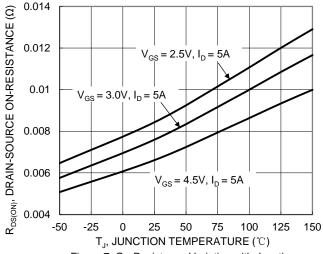


Figure 7. On-Resistance Variation with Junction Temperature

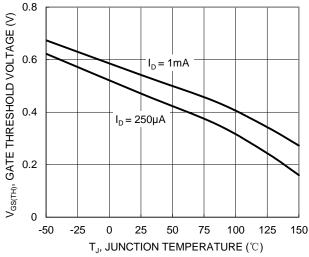
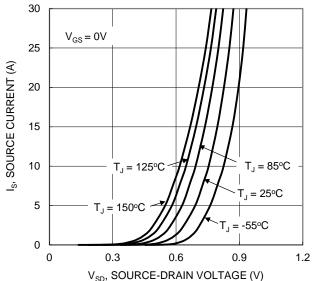
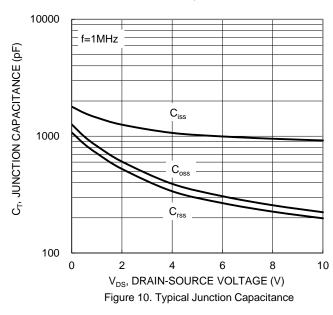


Figure 8. Gate Threshold Variation vs. Junction Temperature



V<sub>SD</sub>, SOURCE-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs. Current



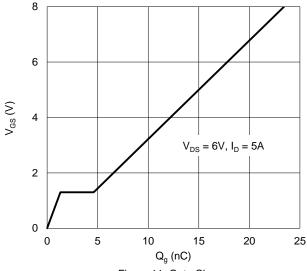
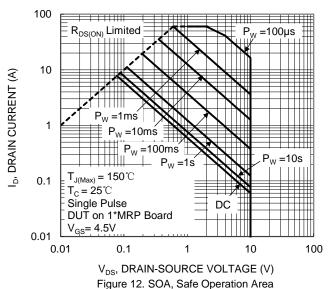


Figure 11. Gate Charge





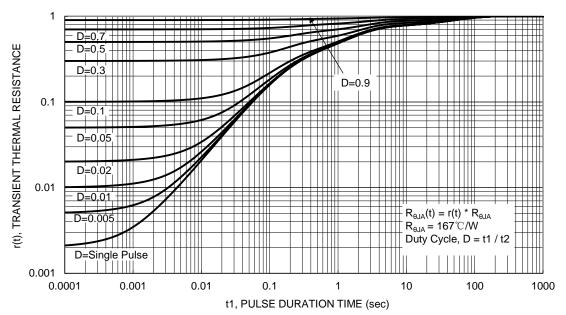


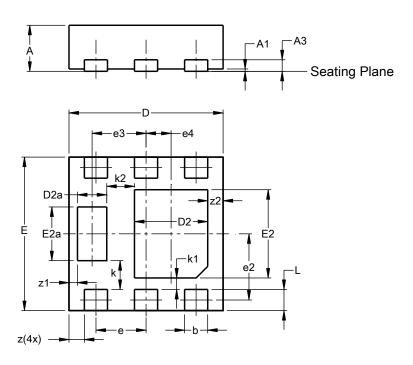
Figure 13. Transient Thermal Resistance



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### U-DFN2020-6 (Type F)

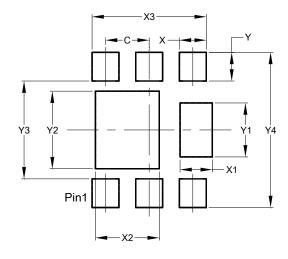


U-DFN2020-6						
		oe F)				
Dim	Min	Max	Тур			
Α	0.57	0.63	0.60			
A1	0.00	0.05	0.03			
A3	-	-	0.15			
b	0.25	0.35	0.30			
D	1.95	2.05	2.00			
D2	0.85	1.05	0.95			
D2a	0.33	0.43	0.38			
Е	1.95	2.05	2.00			
E2	1.05	1.25	1.15			
E2a	0.65	0.75	0.70			
е		0.65 BS	С			
e2	C	).863 BS	SC			
e3		0.70 BS	С			
e4	0	).325 BS	SC			
k		0.37 BS	С			
k1		0.15 BS	С			
k2		0.36 BS	С			
L	0.225	0.325	0.275			
Z		0.20 BS	С			
<b>z</b> 1		).110 BS	SC			
z2		0.20 BS	С			
All D	imens	ions in	mm			

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### U-DFN2020-6 (Type F)



Dimensions	Value (in mm)
С	0.650
Х	0.400
X1	0.480
X2	0.950
Х3	1.700
Υ	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300



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