

20V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	RDS(ON) max	I _{D max} T _A = +25°C
001/	9mΩ @ V _{GS} = 4.5V	15.2A
20V	15mΩ @ V _{GS} = 2.5V	13.8A

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- General Purpose Interfacing Switch
- Power Management Functions

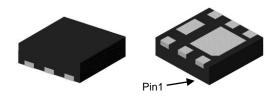
Features

- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm²
- Low Gate Threshold Voltage
- Low On-Resistance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

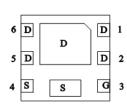
Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208(24)
- Weight: 0.007 grams (Approximate)

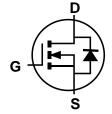
U-DFN2020-6 (Type F)







Pin Out Bottom View



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging		
DMN2015UFDF-7	U-DFN2020-6 (Type F)	3,000/Tape & Reel		
DMN2015UFDF-13	U-DFN2020-6 (Type F)	10,000/Tape & Reel		

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/



Marking Information

Site 1



5N = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Key

Year	2016		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	D		Η		J	K	L	М	N	0	Р	R
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Site 2



5N = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 0 = 2020) W = Week (ex: a = week 27; z represents week 52 and 53) X = Internal Code (ex: U = Monday)

Date Code Key

Date Code Key												
Year	2016		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	6		0	1	2	3	4	5	6	7	8	9
Week	Week 1-26					27	-52		53			
Code	A-Z a-z					Z	<u>'</u>					
Internal Code	Sun	1	Mon		Tue	W	ed	Thu		Fri		Sat
Code	Т		U		V	\	٧	Х		Υ		Z



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	VDSS	20	V		
Gate-Source Voltage	V_{GSS}	±12	V		
Continuous Drain Current (Note 6) V 4.5V	Steady State	T _A = +25°C T _A = +70°C	lo	11.6 9.3	А
Continuous Drain Current (Note 6) V _{GS} = 4.5V	lo	15.2 12.2	А		
Pulsed Drain Current (380µs Pulse, Duty Cycle = 1%)			I _{DM}	70	Α
Maximum Body Diode Continuous Current (Note 6)	Is	2.1	Α		
Avalanche Current (Note 7) L = 0.1mH	I _{AS}	23	Α		
Avalanche Energy (Note 7) L = 0.1mH			Eas	28	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit		
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	D-	0.8	W	
Total Power Dissipation (Note 5)	$T_A = +70^{\circ}C$	P _D	0.5	VV	
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	R⊕JA	159	°C/W	
Thermal Resistance, Junction to Ambient (Note 3)	t<10s	КөЈА	110	C/VV	
Total Power Dissipation (Note 6)	$T_A = +25$ °C	Pp	1.8	W	
Total Fower Dissipation (Note 6)	$T_A = +70^{\circ}C$	FD	1.2	VV	
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	Б	70		
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	RθJA	40	°C/W	
Thermal Resistance, Junction to Case (Note 6)	Rejc	14			
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C	

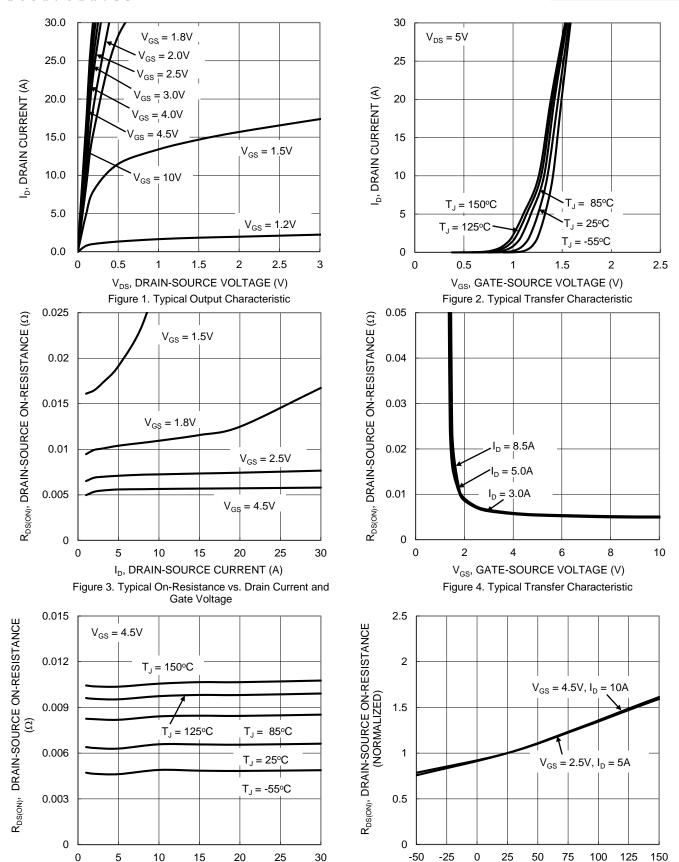
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	ı	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current T _J = +25°C	IDSS	_	-	1	μΑ	V _{DS} = 16V, V _{GS} = 0V
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 12V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)	•	•		•		
Gate Threshold Voltage	Vgs(TH)	0.4	1	1.2	٧	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
			6.8	9		$V_{GS} = 4.5V, I_D = 8.5A$
Static Drain-Source On-Resistance	Bro/our		7.6	15	mΩ	$V_{GS} = 2.5V, I_{D} = 8.5A$
Static Dialii-Source Off-Resistance	RDS(ON)	_	11	30	11177	$V_{GS} = 1.8V, I_D = 5A$
			18	50		$V_{GS} = 1.5V, I_D = 3A$
Diode Forward Voltage	VsD	_	0.75	1.2	V	V _G S = 0V, I _S = 8.5A
DYNAMIC CHARACTERISTICS (Note 9)	•	•		•		
Input Capacitance	Ciss	_	1439	_	pF	10)/)/
Output Capacitance	Coss	_	224	_	рF	$V_{DS} = 10V, V_{GS} = 0V,$ -f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	202	_	pF	T = 1.0WH IZ
Gate Resistance	Rg	_	1.3	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge (VGS = 4.5V)	Qg	_	19.3	_	nC	
Total Gate Charge (V _{GS} = 10V)	Qg	_	42.3	_	nC	\/ 40\/ I- 0.5A
Gate-Source Charge	Qgs	_	2.5	_	nC	$V_{DS} = 10V, I_{D} = 8.5A$
Gate-Drain Charge	Q _{gd}	_	4.5	_	nC	7
Turn-On Delay Time	t _D (ON)	_	4.7	_	ns	
Turn-On Rise Time	t _R	_	6.9	_	ns	$V_{DS} = 10V, I_{D} = 8.5A$
Turn-Off Delay Time	tD(OFF)	_	23	_	ns	$V_{GS} = 4.5V, R_{G} = 1.8\Omega$
Turn-Off Fall Time	tF	_	7.4	_	ns	7
Reverse Recovery Time	trr	_	11.6	_	ns	2.54 45/45 2404/55
Reverse Recovery Charge	Qrr	_	4.6	_	nC	I _F = 8.5A, di/dt = 210A/μs

5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.

- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
- I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.





I_D, DRAIN CURRENT (A)

Figure 5. Typical On-Resistance vs. Drain Current and

Junction Temperature

T_J, JUNCTION TEMPERATURE (°C)

Figure 6. On-Resistance Variation with Junction

Temperature



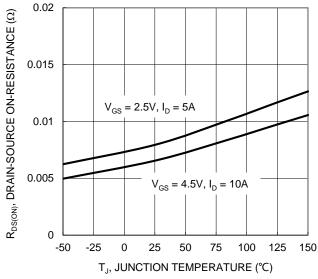
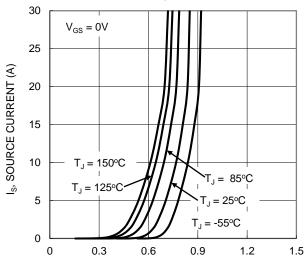


Figure 7. On-Resistance Variation with Junction Temperature



V_{SD}, SOURCE-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs Current

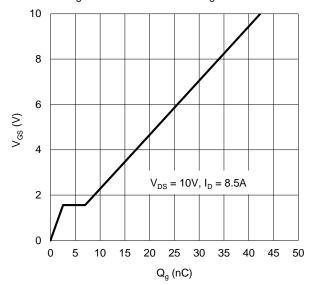


Figure 11. Gate Charge

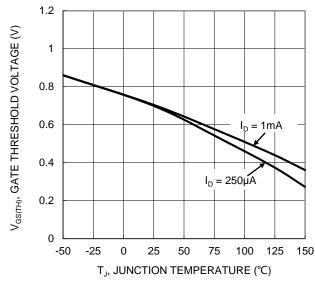


Figure 8. Gate Threshold Variation vs. Junction Temperature

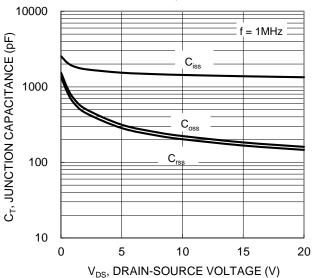


Figure 10. Typical Junction Capacitance

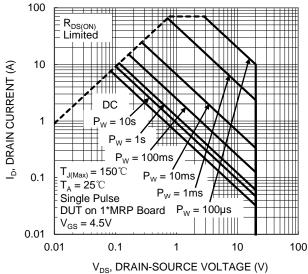


Figure 12. SOA, Safe Operation Area



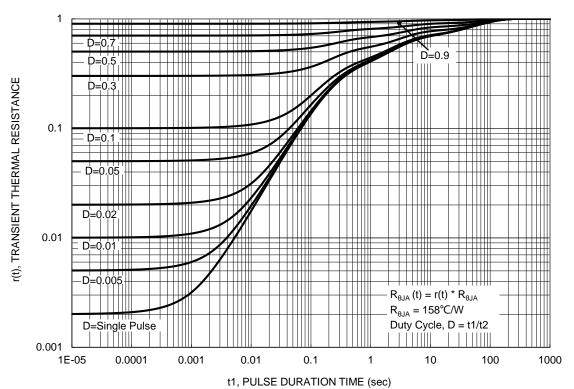


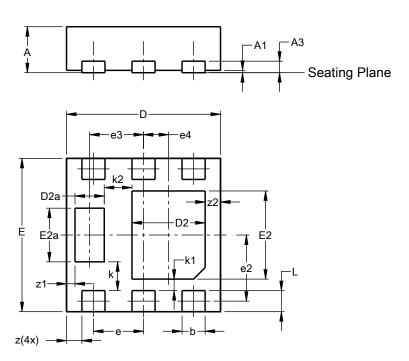
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)

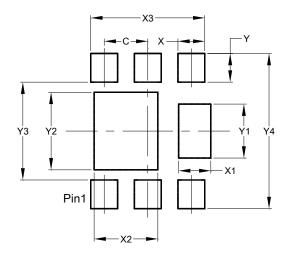


U-DFN2020-6							
		oe F)					
Dim	Min	Max	Тур				
Α	0.57	0.57 0.63 0.6					
A 1	0.00 0.05 0.03						
A3	-	-	0.15				
b	0.25	0.35	0.30				
D	1.95	2.05	2.00				
D2	0.85	1.05	0.95				
D2a	0.33	0.43	0.38				
Е	1.95	2.05	2.00				
E2	1.05	1.25	1.15				
E2a	0.65	0.75	0.70				
е		0.65 BS	С				
e2	C).863 BS	SC				
е3		0.70 BS	C				
e4	C).325 BS	SC				
k		0.37 BS	C				
k1	0.15 BSC						
k2		0.36 BS	C				
L	0.225 0.325 0.275						
Z	0.20 BSC						
z1	0).110 BS	SC				
z2		0.20 BS	С				
All C	imens	ions in	mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)



Dimensions	Value (in mm)
С	0.650
X	0.400
X1	0.480
X2	0.950
Х3	1.700
Y	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300



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