RGTH00TS65DGC13

650V 50A Field Stop Trench IGBT

Datasheet

V_{CES}	650V
I _{C(100°C)}	50A
V _{CE(sat) (Typ.)}	1.6V
P_D	277W

Features

- 1) Low Collector Emitter Saturation Voltage
- 2) High Speed Switching
- 3) Low Switching Loss & Soft Switching
- 4) Built in Very Fast & Soft Recovery FRD (RFN - Series)
- 5) Pb free Lead Plating; RoHS Compliant

Applications

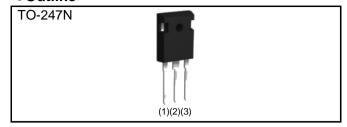
PFC

UPS

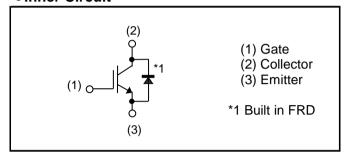
Power Conditioner

ΙH

Outline



●Inner Circuit



Packaging Specifications

	Packaging	Tube		
		Reel Size (mm)	-	
 		Tape Width (mm)	-	
Туре	р е	Basic Ordering Unit (pcs)	600	
		Packing code	C13	
		Marking	RGTH00TS65D	

● Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V _{CES}	650	V
Gate - Emitter Voltage		V_{GES}	±30	V
	T _C = 25°C	I _C	85	А
Collector Current	T _C = 100°C	I _C	50	А
Pulsed Collector Current	I _{CP} *1	200	А	
Diode Forward Current	T _C = 25°C	I _F	50	А
	T _C = 100°C	I _F	30	А
Diode Pulsed Forward Current		I _{FP} *1	200	А
Power Dissipation	T _C = 25°C	P _D	277	W
	T _C = 100°C	P_D	138	W
Operating Junction Temperature		T _j	-40 to +175	°C
Storage Temperature		T _{stg}	-55 to +175	°C

^{*1} Pulse width limited by T_{imax}.

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Datasheet

●Thermal Resistance

Parameter	Cymbol	Values			Linit
Farameter	Symbol	Min.	Тур.	Max.	Unit
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	0.54	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j-c)}$	-	-	1.42	°C/W

ullet **IGBT Electrical Characteristics** (at $T_j = 25$ °C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
r ai ai iletei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Collector - Emitter Breakdown Voltage	BV _{CES}	$I_C = 10 \mu A, V_{GE} = 0 V$	650	1	-	V
Collector Cut - off Current	I _{CES}	$V_{CE} = 650V, V_{GE} = 0V$	1	1	10	μΑ
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V, \ V_{CE} = 0V$	-	-	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5V, I_{C} = 34.7 \text{mA}$	4.5	5.5	6.5	V
Collector - Emitter Saturation Voltage	V _{CE(sat)}	$I_C = 50A$, $V_{GE} = 15V$ $T_j = 25$ °C $T_j = 175$ °C	-	1.6 2.1	2.1 -	V

ullet IGBT Electrical Characteristics (at $T_j = 25$ °C unless otherwise specified)

Daramatar	Symbol	Conditions		Unit		
Parameter			Min.	Тур.	Max.	Unit
Input Capacitance	C _{ies}	$V_{CE} = 30V$	-	2740	-	
Output Capacitance	C _{oes}	$V_{GE} = 0V$	-	106	-	pF
Reverse Transfer Capacitance	C _{res}	f = 1MHz	-	43	-	
Total Gate Charge	Q_g	V _{CE} = 300V	-	94	-	
Gate - Emitter Charge	Q_ge	I _C = 50A	-	22	-	nC
Gate - Collector Charge	Q_{gc}	V _{GE} = 15V	-	31	-	
Turn - on Delay Time	t _{d(on)}	$I_C = 50A, V_{CC} = 400V$	-	39	-	
Rise Time	t _r	$V_{GE} = 15V, R_G = 10\Omega$	-	63	-	
Turn - off Delay Time	t _{d(off)}	T _j = 25°C	-	143	-	ns
Fall Time	t _f	Inductive Load	-	50	-	
Turn - on Delay Time	t _{d(on)}	$I_C = 50A, V_{CC} = 400V$	-	39	-	
Rise Time	t _r	$V_{GE} = 15V, R_G = 10\Omega$	-	63	-	
Turn - off Delay Time	t _{d(off)}	T _j = 175°C	-	159	-	ns
Fall Time	t _f	Inductive Load	-	62	-	
-		I _C = 200A, V _{CC} = 520V				
Reverse Bias Safe Operating Area	RBSOA	$V_P = 650V, V_{GE} = 15V$	FULL SQUARE			-
		$R_G = 60\Omega, T_j = 175^{\circ}C$				

Datasheet

•IGBT Electrical Characteristics (at $T_j = 25$ °C unless otherwise specified)

Parameter	Symbol	Conditions	Values			l lmi4
			Min.	Тур.	Max.	Unit
		I _F = 30A				
Diode Forward Voltage	V_{F}	$T_j = 25^{\circ}C$	-	1.45	2.0	V
		T _j = 175°C	-	1.25	-	
Diode Reverse Recovery Time	t _{rr}	$I_F = 30A$ $V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 25^{\circ}C$	-	54	-	ns
Diode Peak Reverse Recovery Current	I _{rr}		-	7.4	-	А
Diode Reverse Recovery Charge	Q_{rr}		-	0.22	-	μC
Diode Reverse Recovery Time	t _{rr}	I _F = 30A	-	225	-	ns
Diode Peak Reverse Recovery Current	I _{rr}	$V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 175^{\circ}C$	-	12.8	-	Α
Diode Reverse Recovery Charge	Q_{rr}		-	1.60	-	μC

Fig.1 Power Dissipation vs. Case Temperature

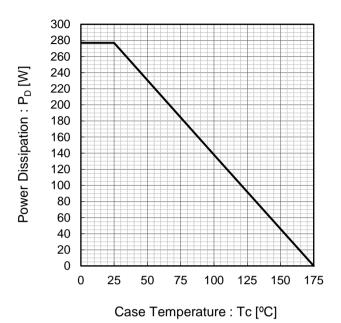


Fig.2 Collector Current vs. Case Temperature

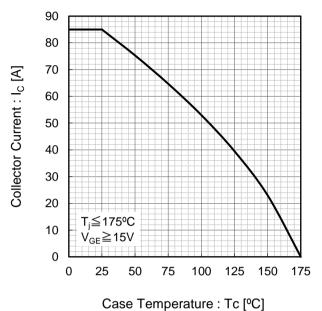


Fig.3 Forward Bias Safe Operating Area

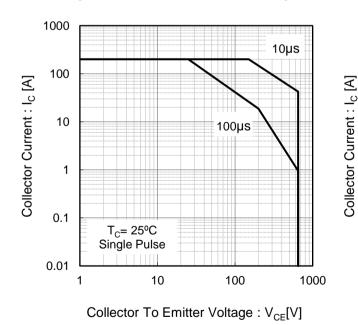
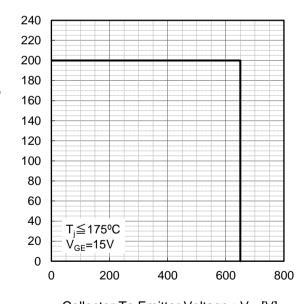


Fig.4 Reverse Bias Safe Operating Area



Collector To Emitter Voltage : $V_{CE}[V]$

Fig.5 Typical Output Characteristics

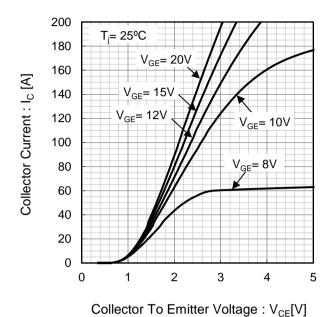
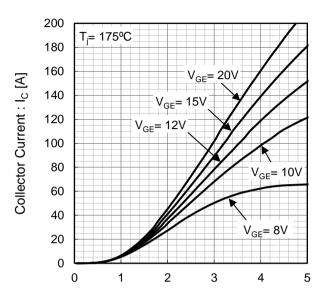


Fig.6 Typical Output Characteristics



Collector To Emitter Voltage : $V_{CE}[V]$

Fig.7 Typical Transfer Characteristics

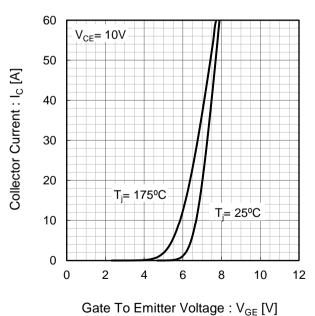
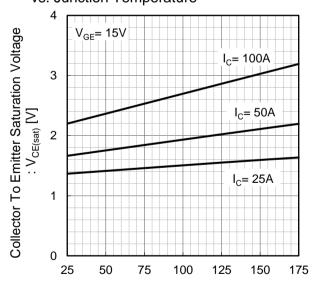
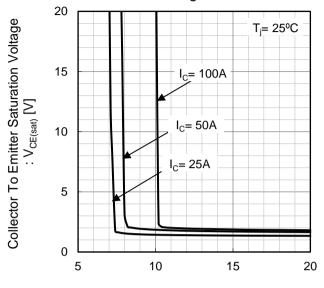


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature



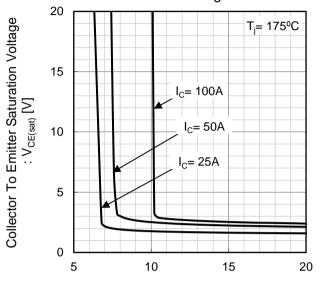
Junction Temperature : T_i [°C]

Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



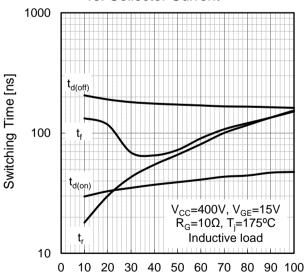
Gate To Emitter Voltage : V_{GE} [V]

Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



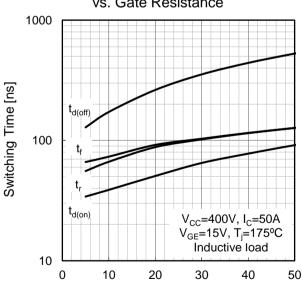
Gate To Emitter Voltage: V_{GE} [V]

Fig.11 Typical Switching Time vs. Collector Current



Collector Current : I_C [A]

Fig.12 Typical Switching Time vs. Gate Resistance



Gate Resistance : $R_G[\Omega]$

Fig.13 Typical Switching Energy Losses vs. Collector Current

10 E_{off} 0.1 E_{off} $V_{CC}=400V, V_{GE}=15V$ $R_{G}=10\Omega, T_{j}=175^{\circ}C$ Inductive load

0 10 20 30 40 50 60 70 80 90 100

Collector Current : I_{C} [A]

vs. Gate Resistance 10 Switching Energy Losses [mJ] $\mathsf{E}_{\mathsf{off}}$ 1 E_{on} 0.1 V_{CC}=400V, I_C=50A V_{GE}=15V, T_j=175°C Inductive load 0.01 0 10 20 30 40 50 Gate Resistance : $R_G[\Omega]$

Fig.14 Typical Switching Energy Losses

Fig.15 Typical Capacitance vs. Collector To Emitter Voltage 10000 Cies 1000 Capacitance [pF] Coes 100 Cres 10 f=1MHz V_{GE}=0V =25°C 0.01 0.1 1 10 100 Collector To Emitter Voltage : $V_{CE}[V]$

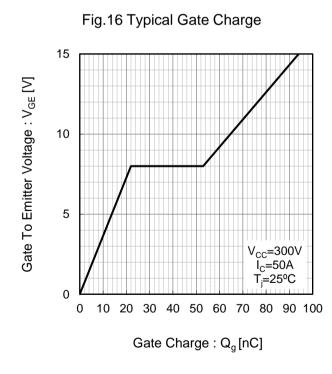


Fig.17 Typical Diode Forward Current vs. Forward Voltage 200 180 160 Forward Current: I_F [A] 140 120 100 80 60 T_i= 175°C 40 T = 25°C 20 0 0.5 1.5 2 2.5 3 3.5 0 Forward Voltage: V_F[V]

Fig.18 Typical Diode Reverse Recovery Time vs. Forward Current 400 V_{CC} =400V di_F/dt=200A/µs Reverse Recovery Time : t_{rr} [ns] Inductive load 300 T_i= 175°C 200 100 $T_{i} = 25^{\circ}C$ 0 10 20 30 50 0 40 Forward Current : I_F [A]

Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current

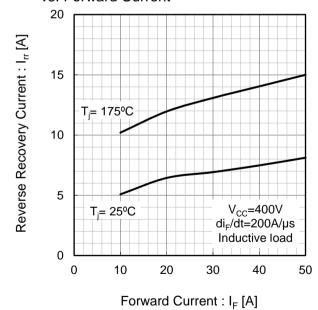
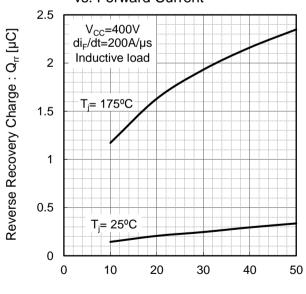
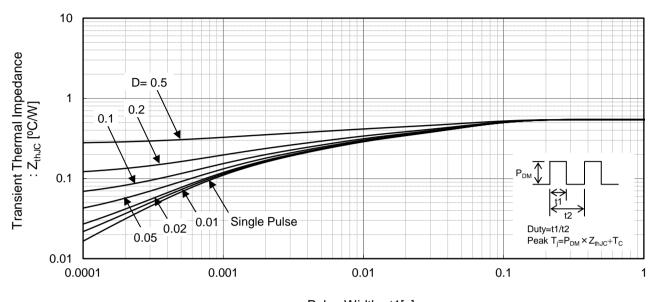


Fig.20 Typical Diode Reverse Recovery Charge vs. Forward Current



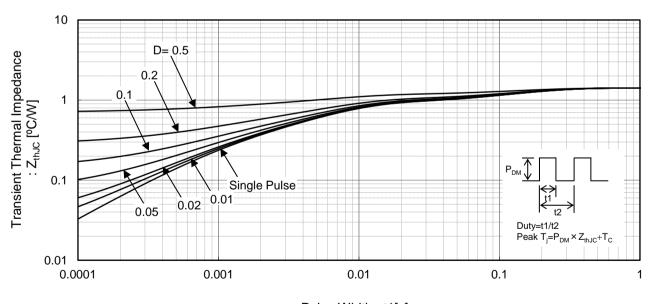
Forward Current : I_F [A]

Fig.21 IGBT Transient Thermal Impedance



Pulse Width: t1[s]

Fig.22 Diode Transient Thermal Impedance



Pulse Width: t1[s]

●Inductive Load Switching Circuit and Waveform

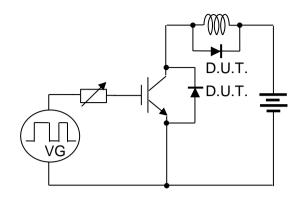


Fig.23 Inductive Load Circuit

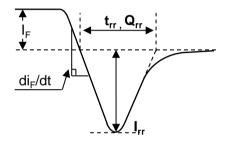


Fig.25 Diode Reverce Recovery Waveform

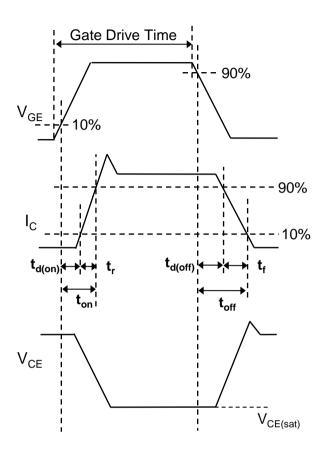


Fig.24 Inductive Load Waveform

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