RGW80TS65HR

650V 40A Field Stop Trench IGBT

Datasheet

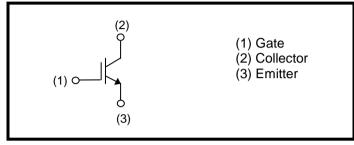
V _{CES}	650V
I _{C (100°C)}	40A
V _{CE(sat) (Typ.)}	1.5V
P_D	214W

Outline TO-247N (1) (2)(3)

Features

- 1) AEC-Q101 Qualified
- 2) Low Collector Emitter Saturation Voltage
- 3) Low Switching Loss & Soft Switching
- 4) Pb free Lead Plating; RoHS Compliant

●Inner Circuit



Application

Automotive

On & Off Board Chargers

DC-DC Converters

PFC

Industrial Inverter

Packaging Specifications

Packaging Specifications					
	Packaging	Tube			
	Reel Size (mm)	-			
Type	Tape Width (mm)	-			
Туре	Basic Ordering Unit (pcs)	450			
	Packing Code	C11			
	Marking	RGW80TS65			

● **Absolute Maximum Ratings** (at T_C = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage Gate - Emitter Voltage		V _{CES}	650	V
		V_{GES}	±30	V
Callagton Cumant	T _C = 25°C	I _C	80	Α
Collector Current	T _C = 100°C	I _C	48	Α
Pulsed Collector Current	Pulsed Collector Current		160	Α
Power Dissipation	T _C = 25°C	P _D	214	W
	T _C = 100°C	P _D	107	W
Operating Junction Temperature		T _j	-40 to +175	°C
Storage Temperature		T _{stg}	-55 to +175	°C

^{*1} Pulse width limited by T_{imax.}

●Thermal Resistance

Parameter	Symbol	Values			Unit
raidilletei	Symbol	Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	ı	0.70	°C/W

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
ratameter Symbol Conditions		Min.	Тур.	Max.		
Collector - Emitter Breakdown Voltage	BV _{CES}	$I_{C} = 10 \mu A, V_{GE} = 0 V$	650	ı	ı	V
Collector Cut - off Current	I _{CES}	$V_{CE} = 650V, V_{GE} = 0V$	-	-	10	μΑ
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V, V_{CE} = 0V$	-	-	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5V, I_{C} = 26.0 \text{mA}$	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage	V _{CE(sat)}	$I_{C} = 40A, V_{GE} = 15V,$ $T_{j} = 25^{\circ}C$ $T_{j} = 175^{\circ}C$	-	1.5 1.85	1.9 -	V

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Input Capacitance	C _{ies}	$V_{CE} = 30V$,	-	3320	-	
Output Capacitance	C _{oes}	$V_{GE} = 0V$,	-	83	-	pF
Reverse transfer Capacitance	C _{res}	f = 1MHz	-	60	-	
Total Gate Charge	Q_g	V _{CE} = 400V,	-	110	-	
Gate - Emitter Charge	Q_{ge}	$I_{\rm C} = 40A$,	-	23	-	nC
Gate - Collector Charge	Q_{gc}	V _{GE} = 15V	-	41	-	
Turn - on Delay Time	t _{d(on)}		-	42	-	
Rise Time	t _r	$I_C = 20A, V_{CC} = 400V,$ $V_{GE} = 15V, R_G = 10\Omega,$	-	11	-	ns
Turn - off Delay Time	t _{d(off)}	$T_i = 25^{\circ}C$	-	148	-	
Fall Time	t _f	Inductive Load	-	37	-	
Turn - on Switching Loss	E _{on}	*E _{on} include diode reverse recovery	-	0.24	-	
Turn - off Switching Loss	E _{off}	1000.00 1000.01,	-	0.33	-	mJ
Turn - on Delay Time	t _{d(on)}		-	39	-	
Rise Time	t _r	$I_C = 20A$, $V_{CC} = 400V$, $V_{GE} = 15V$, $R_G = 10\Omega$, $T_j = 175^{\circ}C$ Inductive Load *E _{on} include diode reverse recovery	-	12	-	
Turn - off Delay Time	t _{d(off)}		-	179	-	ns
Fall Time	t _f		-	75	-]
Turn - on Switching Loss	E _{on}		-	0.27	-	
Turn - off Switching Loss	E _{off}		-	0.51	-	mJ
Reverse Bias Safe Operating Area	RBSOA	$I_C = 160A$, $V_{CC} = 520V$, $V_P = 650V$, $V_{GE} = 15V$, $R_G = 100\Omega$, $T_j = 175^{\circ}C$	FU	LL SQUA	RE	-

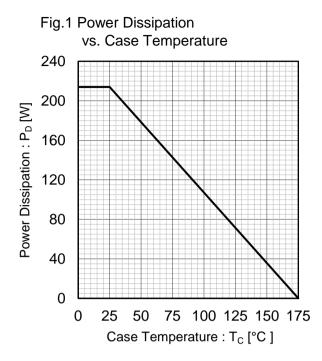


Fig.2 Collector Current vs. Case Temperature 90 80 70 Collector Current : Ic [A] 60 50 40 30 20 T_j ≤ 175°C V_{GE} ≥ 15V 10 0 25 50 75 100 125 150 175 Case Temperature : T_C [°C]

Fig.3 Forward Bias Safe Operating Area

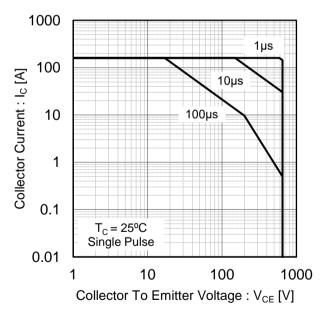
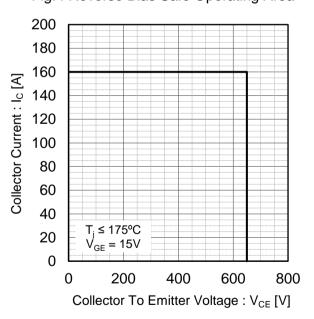


Fig.4 Reverse Bias Safe Operating Area



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Fig.5 Typical Output Characteristics

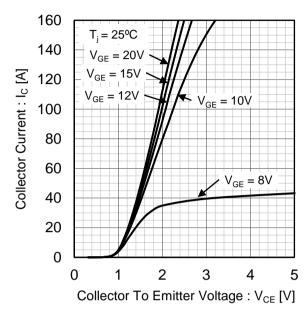


Fig.6 Typical Output Characteristics

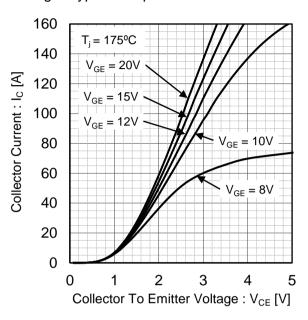


Fig.7 Typical Transfer Characteristics

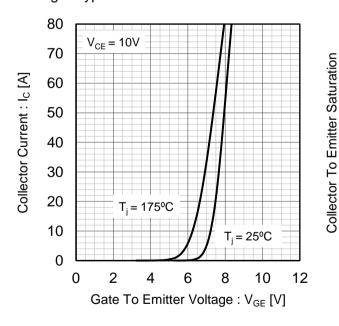
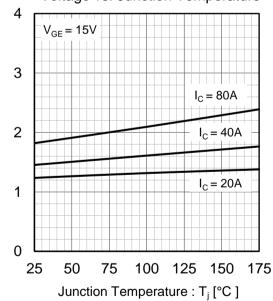


Fig.8 Typical Collector to Emitter Saturation Voltage vs. Junction Temperature



Voltage: V_{CE(sat)} [V]

Fig.9 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage 20 $T_i = 25^{\circ}C$ Collector To Emitter Saturation $I_{\rm C} = 80A$ 15 Voltage: V_{CE(sat)} [V] $I_C = 40A$ $I_{\rm C} = 20A$ 10 5 0 5 10 15 20 Gate To Emitter Voltage: V_{GE} [V]

Fig.10 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

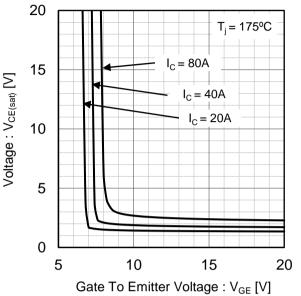
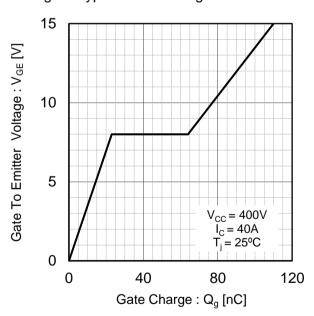


Fig.11 Typical Capacitance vs. Collector to Emitter Voltage 10000 C_{ies} 1000 Capacitance [pF] C_{oes} 100 $\mathsf{C}_{\mathsf{res}}$ 10 f = 1MHz $V_{GE} = 0V$ = 25°C 0.01 0.1 1 10 100 Collector To Emitter Voltage: V_{CE} [V]

Fig.12 Typical Gate Charge



Collector To Emitter Saturation

Fig.13 Typical Switching Time vs. Collector Current 1000 $t_{d(off)}$ Switching Time [ns] 100 $\mathbf{t}_{\mathsf{d}(\mathsf{on})}$ 10 V_{CC} = 400V, V_{GE} = 15V, R_G = 10 Ω , T_j = 25°C Inductive load 1 0 20 40 60 80 Collecter Current : I_C [A]

Fig.14 Typical Switching Time

Fig.15 Typical Switching Energy Losses vs. Collector Current

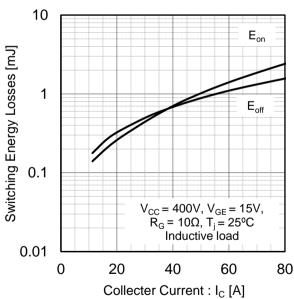


Fig.16 Typocal Switching Energy Losses vs. Gate Resistance

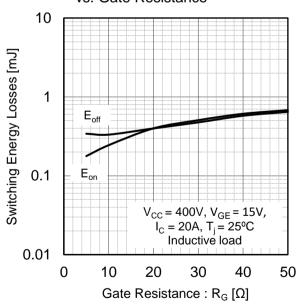


Fig.17 Typical Switching Time vs. Collector Current 1000 Switching Time [ns] 100 $\mathsf{t}_{\mathsf{d}(\mathsf{on})}$ 10 $V_{CC} = 400V, V_{GE} = 15V,$ $R_G = 10\Omega, T_j = 175^{\circ}C$ Inductive load 1 0 20 40 60 80 Collecter Current : I_C [A]

vs. Gate Resistance $\begin{array}{c} \text{1000} \\ \hline \begin{array}{c} \text{1000} \\ \hline \\ \text{0} \\ \text{0} \\ \text{0} \\ \text{100} \\ \end{array} \\ \begin{array}{c} \text{100} \\ \text{100$

Fig.18 Typical Switching Time

Fig.19 Typical Switching Energy Losses vs. Collector Current 10 Switching Energy Losses [mJ] 1 $\mathsf{E}_{\mathsf{off}}$ 0.1 E_{on} $V_{CC} = 400V, V_{GE} = 15V,$ $R_G = 10\Omega, T_j = 175^{\circ}C$ Inductive load 0.01 0 20 40 60 80 Collecter Current : I_C [A]

vs. Gate Resistance

10

See Scot 1

Eof V_{CC} = 400V, V_{GE} = 15V, I_C = 20A, T_j = 175°C Inductive load

0.01

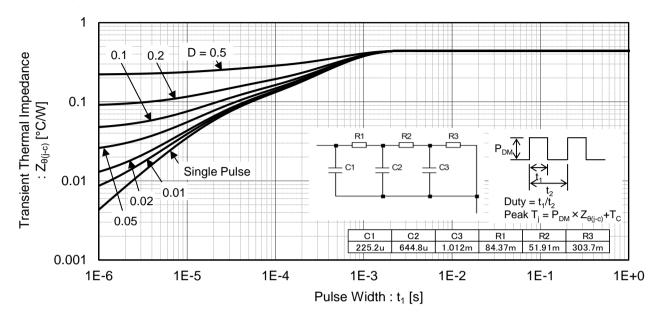
0 10 20 30 40 50

Gate Resistance : $R_G[\Omega]$

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Fig.20 Typocal Switching Energy Losses

Fig.21 Typical IGBT Transient Thermal Impedance



●Inductive Load Switching Circuit and Waveform

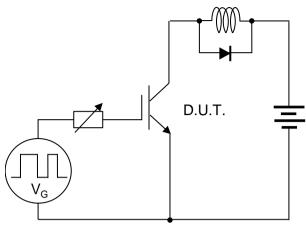


Fig.22 Inductive Load Circuit

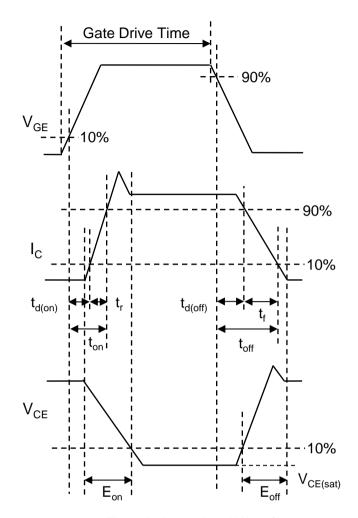


Fig.23 Inductive Load Waveform

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