



## Product Change Notification / JAON-04DXFD964

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### Date:

22-Mar-2021

### Product Category:

8-bit Microcontrollers, Analog to Digital Converters, Capacitive Touch Sensors, Charge Pump DC-to-DC Converters, Digital Potentiometers, Digital to Analog Converters, Instrumentation Amplifier, Interface-Controller Area Network (CAN), Interface- Infrared Products, Interface- LIN Transceiver, KEELOQ® Encoder Devices, Linear Comparators, Linear Op Amps, Linear Programmable Gain Amplifiers, Memory, Piezoelectric Horn Drivers and CO Detector, Power Management - PWM Controllers, Power MOSFET Drivers, Real-Time Clock/Calendar, RFID tag ICs - microID® ICs, Temperature Sensors

### PCN Type:

Manufacturing Change

### Notification Subject:

CCB 4541 Final Notice: Qualification of a new lead frame design for selected products available in 8L SOIC package using 8390A die attach and gold (Au) bond wire material assembled at MMT assembly site.

### Affected CPNs:

[JAON-04DXFD964\\_Affected\\_CPN\\_03222021.pdf](#)

[JAON-04DXFD964\\_Affected\\_CPN\\_03222021.csv](#)

### Notification Text:

**PCN Status:** Final notification

**PCN Type:** Manufacturing Change

**Microchip Parts Affected:** Please open one of the files found in the Affected CPNs section.

NOTE: For your convenience Microchip includes identical files in two formats (.pdf and .xls).

**Description of Change:** Qualification of a new lead frame design for selected products available in 8L SOIC package using 8390A die attach and gold (Au) bond wire material assembled at MMT assembly site.

**Pre Change:**

Using lead frame without lead lock

**Post Change:**Using lead frame with lead lock**Pre and Post Change Summary:**

	Pre Change	Post Change
<b>Assembly Site</b>	Microchip Technology Thailand (Branch) / MMT	Microchip Technology Thailand (Branch) / MMT
<b>Wire material</b>	Au	Au
<b>Die attach material</b>	8390A	8390A
<b>Molding compound material</b>	G600V	G600V
<b>Lead frame material</b>	CDA194	CDA194
<b>Lead Frame Lead Lock</b>	No	Yes
	See attached pre and post change comparison	

**Impacts to Data Sheet:** None**Change Impact:**None**Reason for Change:**To improve productivity by qualifying new lead frame design**Change Implementation Status:**In Progress**Estimated First Ship Date:**February 19, 2021 (date code: 2108)

NOTE: Please be advised that after the estimated first ship date customers may receive pre and post change parts.

**Time Table Summary:**

Workweek	February 2021				March 2021				
	06	07	08	09	10	11	12	13	14
Final PCN Issue Date		X							
Qual Report Availability								X	
Estimated First Ship Date			X						

**Method to Identify Change:** Traceability code**Qualification Report**Please open the attachments included with this PCN labeled as PCN\_#\_Qual\_Report

**Revision History:**February 9, 2021: Issued final notification. Provided estimated first ship date to be February 19, 2021.**March 22, 2021:** Re-issued final notification. Attached the qualification report.

The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable products.

## Attachments:

[PCN\\_JAON-04DXFD964\\_Qual Report.pdf](#)

[PCN\\_JAON-04DXFD964\\_Pre and Post Change\\_Summary.pdf](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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If you wish to change your PCN profile, including opt out, please go to the [PCN home page](#) select login and sign into your myMicrochip account. Select a profile option from the left navigation bar and make the applicable selections.

Affected Catalog Part Numbers (CPN)

PIC12F629T-I/SN220  
PIC12F629T-I/SN232  
PIC12F629T-I/SN233  
PIC12F629T-I/SN241  
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# CCB 4541

## Pre and Post Change Summary

### PCN # JAON-04DXFD964



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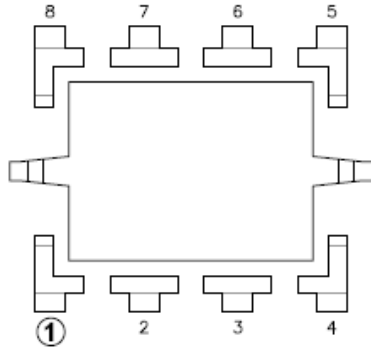
A Leading Provider of Smart, Connected and Secure Embedded Control Solutions



SMART | CONNECTED | SECURE

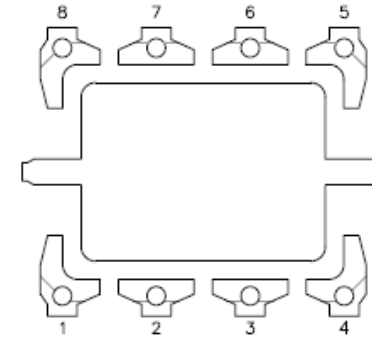
# Lead frame Comparison

## Pre Change

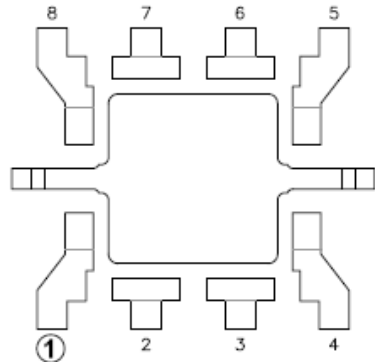


LF STRIP DENSITY: x140  
LF PAD SIZE: 95x130 mils  
LEAD LOCK: NO

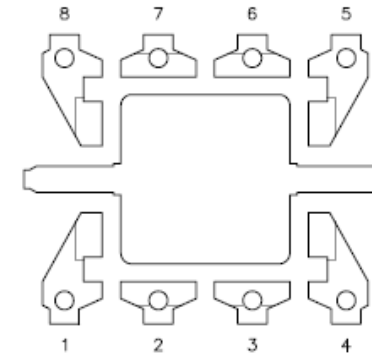
## Post Change



LF STRIP DENSITY: x320  
LF PAD SIZE: 95x130 mils  
LEAD LOCK: YES



LF STRIP DENSITY: x140  
LF PAD SIZE: 90x90 mils  
LEAD LOCK: NO



LF STRIP DENSITY: x320  
LF PAD SIZE: 90x90 mils  
LEAD LOCK: YES



**MICROCHIP**

**QUALIFICATION REPORT SUMMARY**  
RELIABILITY LABORATORY

**PCN #: JAON-04DXFD964**

**Date**

**March 08, 2021**

**Qualification of a new lead frame design for selected products available in 8L SOIC package using 8390A die attach and gold (Au) bond wire material assembled at MMT assembly site.**



## **MICROCHIP** **PACKAGE QUALIFICATION REPORT**

**Purpose** Qualification of a new lead frame design for selected products available in 8L SOIC package using 8390A die attach and gold (Au) bond wire material assembled at MMT assembly site.

**CN** ES352231

**QUAL ID** R2100093 Rev A

**MP CODE** MVAR14C2XFX3

**Part No.** PIC16F15313-E/SN

**Bonding No.** BDM-002812 Rev. A

**CCB#** 4541

**Package**

**Type** 8L SOIC

**Package size** 150 mils

**Lead Frame**

**Paddle size** 95 x 130 mils

**Material** CDA194

**Surface** Bare Cu

**Process** Stamped

**Lead Lock** Yes

**Part Number** 10100842

**Treatment** BOT

**Material**

**Epoxy** 8390A

**Wire** Au

**Mold Compound** G600V

**Plating Composition** Matte Sn



# MICROCHIP PACKAGE QUALIFICATION REPORT

## Manufacturing Information

Assembly Lot No.	Wafer Lot.	Date Code
MMT-214202310.000	TMPE221054345.600	2103V5R
MMT-214202312.000	TMPE221054345.600	210325E
MMT-214302391.000	TMPE221054345.600	2104260

### Result

Pass  Fail  \_\_\_\_\_

8L SOIC assembled by MMT pass reliability test per QCI-39000. This package was qualified the Moisture/Reflow Sensitivity Classification Level 1 at 260°C reflow temperature per IPC/JEDEC J-STD-020E standard.

# PACKAGE QUALIFICATION REPORT

Test Number (Reference)	Test Condition	Standard/ Method	Qty. (Acc.)	Def/SS.	Result	Remarks
<b>Precondition Prior Perform Reliability Tests (At MSL Level 1)</b>	<b>Electrical Test:</b> +25°C, 85°C and 125°C System: J750	JESD22- A113	693(0)	693		Good Devices
	Bake 150°C, 24 hrs System: CHINEE	JIP/ IPC/JEDEC C J-STD- 020E		693		
	85°C/85%RH Moisture Soak 168 hrs. System: TABAI ESPEC Model PR-3SPH			693		
	3x Convection-Reflow 265°C max System: Vitronics Soltec MR1243			693		
	<b>Electrical Test:</b> +25°C, 85°C and 125°C System: J750			0/693	Pass	
<b>Temp Cycle</b>	<b>Stress Condition:</b> -65°C to +150°C, 500 Cycles System: TABAI ESPEC TSA-70H	JESD22- A104		231		Parts had been pre-conditioned at 260°C
	<b>Electrical Test:</b> +25°C, 85°C and 125°C System: J750		231(0)	0/231	Pass	77 units / lot
	<b>Bond Strength:</b> Wire Pull (> 2.5 grams)		15 (0)	0/15	Pass	
	Bond Shear (>15.00 grams)		15 (0)	0/15	Pass	
<b>UNBIASED-HAST</b>	<b>Stress Condition:</b> +130°C/85%RH, 96 hrs. System: HAST 6000X	JESD22- A118		231		Parts had been pre-conditioned at 260°C
	<b>Electrical Test:</b> +25°C System: J750		231(0)	0/231	Pass	77 units / lot
<b>HAST</b>	<b>Stress Condition:</b> +130°C/85%RH, 96 hrs. <b>Bias Volt:</b> 5.5 Volts System: HAST 6000X	JESD22- A110		231		Parts had been pre-conditioned at 260°C
	<b>Electrical Test:</b> +25°C, 85°C and 125°C System: J750		231(0)	0/231	Pass	77 units / lot

# PACKAGE QUALIFICATION REPORT

Test Number (Reference)	Test Condition	Standard/ Method	Qty. (Acc.)	Def/SS.	Result	Remarks
<b>High Temperature Storage Life</b>	<b>Stress Condition:</b> Bake 175°C, 504 hrs System: SHEL LAB	JESD22- A103		45		45 units
	<b>Electrical Test:</b> +25°C, 85°C and 125°C System: J750		45(0)	0/45	Pass	
<b>Physical Dimensions</b>	Physical Dimension, 10 units from 1 lot	JESD22- B100/B108	30(0)	0/30	Pass	
<b>Bond Strength Data Assembly</b>	Wire Pull (> 2.50 grams)	Mil.Std. 883-2011	30 (0) Wires	0/30	Pass	
	Bond Shear (>15.00 grams)	CDF-AEC- Q100-001	30 (0) bonds	0/30	Pass	