Customer Information Notification

202103018I : MC56F82xxx Chip Errata Rev Dec 2020 Update

Note: This notice is NXP Company Proprietary.

Issue Date: Apr 08, 2021 **Effective date:Apr 09, 2021**

Dear Gordon Love,

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[ ]Wafer Fab Materials	[ ]Assembly Materials	[ ]Mechanical Specification	[ ]Test Equipment	[X]Errata
[ ]Wafer Fab Location	[ ]Assembly Location	[ ]Packing/Shipping/Labeling	[ ]Test Location	[ ]Electrical spec./Test coverage
[ ]Firmware	[ ]Other			

PCN Overview

Description

NXP Semiconductors announces chip errata update to revision Dec 2020 for MC56F82xxx. The revision history included in the updated document provides a detailed description of the changes. Changes are summarized below.

MC56F82xxx Chip Errata has the following changes:
• Workaround description updated in the following errata:
– e11484: POR: Residual voltage on VDD may cause POR unsuccessful

The MC56F82xxx errata is attached to this notice, and can be found at:
https://www.nxp.com/products/processors-and-microcontrollers/additional-mpu-mcus-architectures/digital-signal-controllers/32-bit-56f8xxxx-families/mc56f82xxx-mc56f826xx-and-mc56f827xx-digital-signal-controllers:MC56F827xx?tab=Documentation_Tab&linkline=Errata

Reason

The errata was modified to provide more accurate workaround to avoid the errata issue.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

Additional information

Additional documents: view online

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

Name	technical support
e-mail address	tech.support@nxp.com

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NXP Quality Management Team.

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