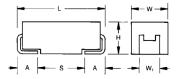
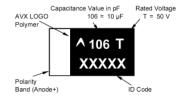
High Temperature Automotive Polymer Chip Capacitors







MARKING



FEATURES

- · Conductive Polymer Electrode
- · Benign Failure Mode Under Recommended Use Conditions
- Robust Design for Automotive Applications
- · Meets Requirements of AEC-Q200
- -55 to +150°C Operation Temperature
- Humidity 85°C/85%RH, Vr, 1000 Hours
- Basic Reliability 1%/1000hrs@85°C Vr with 60% Confidence Level
- DCL 0.1 CV
- · 3x reflow 260°C Compatible
- 100% Surge Current Tested





APPLICATIONS

DC/DC converters, Telecommunication (coupling/decoupling), Industrial & special, Automotive (body electronics, cabin controls, infotainment, comfort, after market etc)

Not recommended for use of conductive polymer parts in high power applications. For more information please see AVX automotive application guide at avx.com (see the link: http://www.avx.com/docs/techinfo/ApplicationGuides/Automotive-Application-Guide.pdf), or contact manufacturer.

AVX's qualification of TCO capacitors meets requirements of AEC-Q200. TCO series is manufactured in an IATF 16949 certified facility.

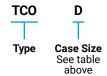
CASE DIMENSIONS:

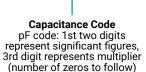
****	/• · ·
millimatore	Inchac
millimeters	HILLIES

Code	EIA Code	EIA Metric	L±0.20 (0.008)	W+0.20 (0.008) -0.10 (0.004)	H+0.20 (0.008) -0.10 (0.004)	W ₁ ±0.20 (0.008)	A+0.30 (0.012) -0.20 (0.008)	S Min.
D	2917	7343-31	7.30 (0.287)	4.30 (0.169)	2.90 (0.114)	2.40 (0.094)	1.30 (0.051)	4.40 (0.173)

 $\mbox{W}_{\mbox{\scriptsize 1}}$ dimension applies to the termination width for A dimensional area only.

HOW TO ORDER





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TECHNICAL SPECIFICATIONS

Technical Data:	All technical data relate to an ambient temperature of +25°C
Capacitance Range:	10 μF
Capacitance Tolerance:	±20%
Leakage Current DCL:	0.1CV
Temperature Range:	-55°C to +150°C
	Meets requirements of AEC-Q200

NOTE: Conductive Polymer Capacitors are designed to operate within the limits of the environmental conditions specified for each series. If operated continuously at their maximum temperature and / or humidity limit, or beyond these limits, capacitors may exhibit a parametric shift in capacitance and increases in ESR. These changes may occur earlier if the specified environmental conditions are exceeded. Similarly, their normal operational time period will be significantly extended if their general duty cycle includes operation below maximum temperature within humidity controlled environments. Careful attention should be paid to maximum temperature with associated high humidity environments as well as voltage derating, ripple current and current surges. Please reference the AVX Conductive Polymer Capacitor Guidelines for more information or contact factory for application assistance.





High Temperature Automotive Polymer Chip Capacitors

CAPACITANCE AND RATED VOLTAGE RANGE (LETTER DENOTES CASE SIZE)

Capac	itance	Rated Voltage DC (V _R) @ 105°C					
μF	Code	25V (E)	35V (V)	50V (T)			
10	106			D(150)			
15	156						
22	226						
33	336						

Released ratings, (ESR ratings in mOhms in parentheses)

Note: Voltage ratings are minimum values. AVX reserves the right to supply higher volage ratings in the same case size, to the same reliability standards.

RATINGS & PART NUMBER REFERENCE

	AVX Part No.	Case Capacitance (μF)	nce Rated	Rated Maximum Operating Temp.		DF Max			100kHz RMS Current (mA)					MSL	
			(μ F)	Voltage (V)	(°C)	Max (µA)	(%)	(mΩ)	45°C	85°C	105°C	125°C	150°C	85°C/85% RH, Vr (hrs)	IVISE
	50 Voit														
ſ	TCOD106M050#0150E	D	10	50	150	50	10	150	1225	857	551	306	184	1000	3

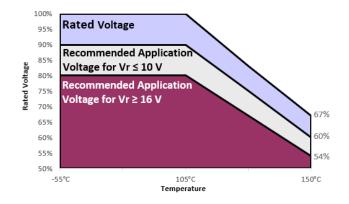
Moisture Sensitivity Level (MSL) is defined according to J-STD-020. All technical data relates to an ambient temperature of +25C.

Capacitance and DF are measured at 120Hz, 0.5RMS with DC bias of 2.2 volts. DCL is measured at rated voltage after 5 minutes. ESR allowed to move up to 1.25 times catalog limit post mounting. For typical weight and composition see page 276.

RECOMMENDED DERATING FACTOR

Voltage and temperature derating as percentage of Vr

Rated	Operating Temperature							
voltage	≤85°C 105°C 150°							
≤10V	90%	90%	60%					
≥16V	80%	80%	54%					







QUALIFICATION TABLE

TEOT	TCO series (Temperature range -55°C to 150°C)											
TEST		Condition			Cł	naracteri	stics					
				Visual examination	no visible	no visible damage						
		age (Ur) at 105°C fo		DCL	2 x initial	limit						
Endurance		e (Ur) at 150°C for 1 : impedance of ≤0.1		ΔC/C	within +1	within +10/-20% of initial value						
		re for 1-2 hours bef		DF	2 x initial	2 x initial limit						
	l com temperata		oro mododinig.	ESR	2 x initial	2 x initial limit						
				Visual examination	no visible	no visible damage						
	Store at 150°C. r	no voltage applied, f	or 1000 hours.	DCL	2x initial	2x initial limit						
Storage Life		n temperature for 1-		ΔC/C	within +1	0/-20% of	initial value	:				
	measuring.			DF	2 x initial	2 x initial limit						
				ESR	2 x initial	limit						
				Visual examination	no visibl	no visible damage						
		age (Ur) at 85°C, 85°		DCL	2 x initia	2 x initial limit						
Biased Humidity		0 hours. Stabilize a I humidity for 1-2 ho		ΔC/C	within +	within +35/-5% of initial value						
•	measuring.	i numicity for 1-2 nd	ours before	DF	1.5 x init	1.5 x initial limit						
	medoding.			ESR	2 x initia	2 x initial limit						
	Step	Temperature°C	Duration(min)		+20°C	-55°C	+20°C	+105°C	+150°C	+20°C		
	1	+20	15									
Temperature	3	-55 +20	15 15	DCL	IL*	n/a	IL*	10 x IL*	12.5 x IL*	IL*		
Stability	4	+105	15	ΔC/C	n/a	±20%	±5%	±20%	±30%	±5%		
	5	+150	15				11.4	45 "+	45 "+	11.4		
	6	+20	15	DF	IL*	IL*	IL*	1.5 x IL*	1.5 x IL*	IL*		
				Visual examination	no visible	no visible damage						
				DCL	initial lim	initial limit						
Surge Voltage		rated voltage (Ur) a arge / discharge res		ΔC/C		within +10/-20% of initial value for $Vr \le 10V$ within +20/-30% of initial value for $Vr \ge 16V$						
	1000 cycles, che	inge / discharge res	istance ooz.	DF		initial limit for Vr ≤ 10V 1.25x initial limit for Vr ≥ 16V						
				ESR	1.25 x ini	1.25 x initial limit						
				Visual examination	no visibl	no visible damage						
Mashaniaal				DCL	initial lin	initial limit						
Mechanical	MIL-STD-202, M	ethod 213, Conditio	n F	ΔC/C	within ±	within ±10% of initial value						
Shock				DF	initial lin	initial limit						
				ESR	1.25 x in	1.25 x initial limit						
				Visual examination	no visibl	e damage						
				DCL	initial lin	nit						
Vibration	MIL-STD-202, M	ethod 204, Conditio	n D	ΔC/C	within ±	within ±10% of initial value						
			DF	initial lin	initial limit							
			ESR	1.25 x in	1.25 x initial limit							

^{*}Initial Limit

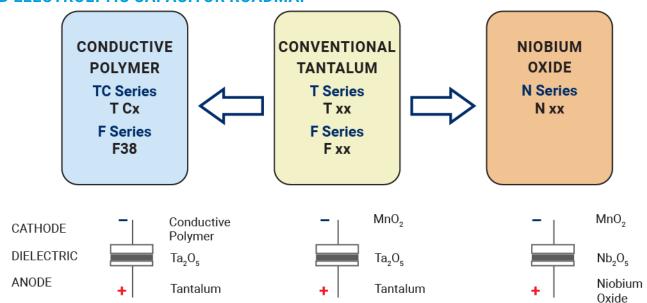
For use outside of recommended conditions and special request, please contact AVX.

Initial measurement max. 1hr after the removal from dry pack or after pretreatment at 85°C for 24 hours.

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SOLID ELECTROLYTIC CAPACITOR ROADMAP



FIVE CAPACITOR CONSTRUCTION STYLES



SERIES LINE UP: Conductive Polymer

