

BUK7S2R5-40H

N-channel 40 V, 2.5 mOhm standard level MOSFET in LFPAK88

6 April 2021

Product data sheet

1. General description

Automotive qualified N-channel MOSFET using the latest Trench 9 low ohmic superjunction technology, housed in a copper-clip LFPAK88 package. This product has been fully designed and qualified to meet beyond AEC-Q101 requirements delivering high performance and reliability.

2. Features and benefits

- · Fully automotive qualified to beyond AEC-Q101:
 - -55 °C to +175 °C rating suitable for thermally demanding environments
- LFPAK88 package
 - Designed for smaller footprint and improved power density over older wire bond packages such as D²PAK for today's space constrained high power automotive applications
 - Thin package and copper clip enables LFPAK88 to be highly efficient thermally
- LFPAK copper clip technology enabling improvements over wire bond packages by:
 - · Increased maximum current capability and excellent current spreading
 - Improved R_{DSon}
 - · Low source inductance
 - Low thermal resistance R_{th}
- LFPAK Gull Wing leads:
 - Flexible leads enabling high Board Level Reliability absorbing mechanical and thermal cycling stress, unlike traditional QFN packages
 - Visual (AOI) soldering inspection, no need for expensive x-ray equipment
 - · Easy solder wetting for good mechanical solder joint
- Unique 40 V Trench 9 superjunction technology:
 - Reduced cell pitch and superjunction platform enables lower R_{DSon} in the same footprint
 - Improved SOA and avalanche capability compared to standard TrenchMOS
 - Tight V_{GS(th)} limits enable easy paralleling of MOSFETs

3. Applications

- 12 V automotive systems
- 48 V DC/DC systems (on 12 V secondary side)
- Higher power motors, lamps and solenoid control
- · Reverse polarity protection
- · Ultra high performance power switching

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
V_{DS}	drain-source voltage	25 °C ≤ T _j ≤ 175 °C		-	-	40	V	
I _D	drain current	V _{GS} = 10 V; T _{mb} = 25 °C; <u>Fig. 2</u>	[1]	-	-	140	Α	
P _{tot}	total power dissipation	T _{mb} = 25 °C; <u>Fig. 1</u>		-	-	135	W	



Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Static chara	acteristics						
R _{DSon}	drain-source on-state resistance	V_{GS} = 10 V; I_{D} = 25 A; T_{j} = 25 °C; Fig. 11		1.51	2.16	2.51	mΩ
Dynamic cl	haracteristics						
Q_{GD}	gate-drain charge	I _D = 25 A; V _{DS} = 32 V; V _{GS} = 10 V; Fig. 13; Fig. 14		-	7	14	nC
Source-dra	in diode						
Q _r	recovered charge	$_{S}$ = 25 A; dI _S /dt = -100 A/ μ s; V _{GS} = 0 V; [2]		-	19.1	-	nC
S	softness factor	V _{DS} = 20 V; T _j = 25 °C; <u>Fig. 17</u>		-	0.89	-	

^{[1] 140}A continuous current has been successfully demonstrated during application. Practically the current will be limited by PCB, thermal design and operating temperature

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		D
2	S	Source		
3	S	Source		G—(H)
4	S	Source		mbb076 S
mb	D	mounting base; connected to drain	LFPAK88 (SOT1235)	

6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
BUK7S2R5-40H		plastic, single-ended surface-mounted package (LFPAK88); 4 leads; 2 mm pitch; 8 mm x 8 mm x 1.6 mm body	SOT1235			

7. Marking

Table 4. Marking codes

Type number	Marking code
BUK7S2R5-40H	7S2R540H

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage	25 °C ≤ T _j ≤ 175 °C	-	40	V

^[2] includes capacitive recovery

Symbol	Parameter	Conditions		Min	Max	Unit
V _{GS}	gate-source voltage	DC; T _j = 175 °C		-10	20	V
P _{tot}	total power dissipation	T _{mb} = 25 °C; <u>Fig. 1</u>		-	135	W
I _D	drain current	V _{GS} = 10 V; T _{mb} = 25 °C; <u>Fig. 2</u>	[1]	-	140	Α
I _{DM}	peak drain current	pulsed; $t_p \le 10 \mu s$; $T_{mb} = 25 °C$; Fig. 3		-	629	Α
T _{stg}	storage temperature			-55	175	°C
T _j	junction temperature			-55	175	°C
Source-drain d	iode		'			·
Is	source current	T _{mb} = 25 °C	[2]	-	135	Α
I _{SM}	peak source current	pulsed; t _p ≤ 10 μs; T _{mb} = 25 °C		-	629	Α
Avalanche rugo	gedness					
E _{DS(AL)S}	non-repetitive drain- source avalanche energy	I_D = 96 A; $V_{sup} \le 40$ V; R_{GS} = 50 Ω; V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; unclamped; Fig. 4	[3] [4]	-	80	mJ
I _{AS}	non-repetitive avalanche current	V_{sup} = 40 V; V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; R_{GS} = 50 Ω	[5]	-	96	A

- [1] 140A continuous current has been successfully demonstrated during application. Practically the current will be limited by PCB, thermal design and operating temperature
- [2] 135Å continuous current has been successfully demonstrated during application. Practically the current will be limited by PCB, thermal design and operating temperature
- [3] Single-pulse avalanche rating limited by maximum junction temperature of 175 °C.
- [4] Refer to application note AN10273 for further information.
- [5] Protected by 100% test.

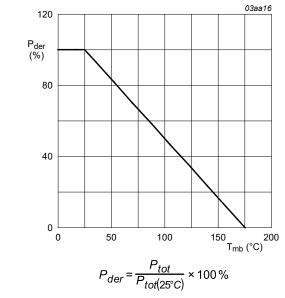
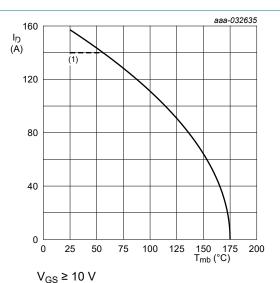


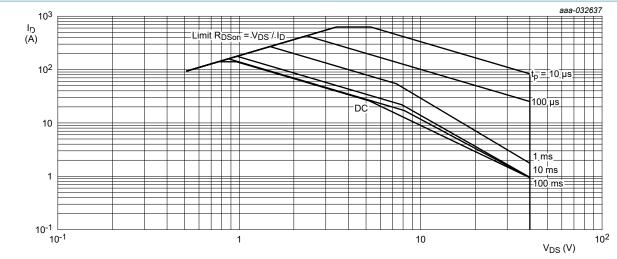
Fig. 1. Normalized total power dissipation as a function of mounting base temperature



(1) 140A continuous current has been successfully demonstrated during application tests. Practically the current will be limited by PCB, thermal design and operating temperature.

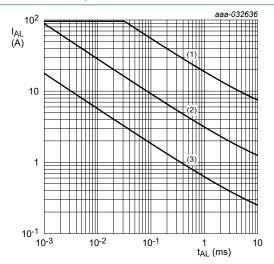
Fig. 2. Continuous drain current as a function of mounting base temperature

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T_{mb} = 25 °C; I_{DM} is a single pulse

Fig. 3. Safe operating area; continuous and peak drain currents as a function of drain-source voltage



(1) $T_{j \text{ (init)}}$ = 25 °C; (2) $T_{j \text{ (init)}}$ = 150 °C; (3) Repetitive Avalanche

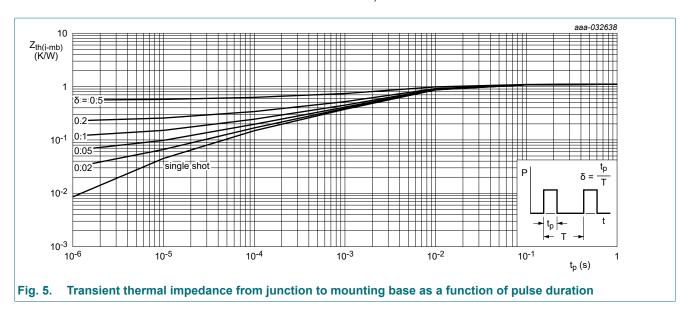
Fig. 4. Avalanche rating; avalanche current as a function of avalanche time

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base	Fig. 5	-	0.97	1.11	K/W

N-channel 40 V, 2.5 mOhm standard level MOSFET in LFPAK88



10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
V _{(BR)DSS}	drain-source	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	40	43	-	V
	breakdown voltage	I _D = 250 μA; V _{GS} = 0 V; T _j = -40 °C	-	40.5	-	V
		I _D = 250 μA; V _{GS} = 0 V; T _j = -55 °C	36	40	-	V
V _{GS(th)}	gate-source threshold voltage	$I_D = 1 \text{ mA}; V_{DS}=V_{GS}; T_j = 25 \text{ °C}; Fig. 9;$ Fig. 10	2.4	3	3.6	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = -55 \text{ °C}; Fig. 10$	-	-	4.3	V
		$I_D = 1 \text{ mA}; V_{DS}=V_{GS}; T_j = 175 \text{ °C};$ Fig. 10	1	-	-	V
I _{DSS}	drain leakage current	V _{DS} = 40 V; V _{GS} = 0 V; T _j = 25 °C	-	0.027	1	μA
		V _{DS} = 16 V; V _{GS} = 0 V; T _j = 125 °C	-	0.7	10	μA
		V _{DS} = 40 V; V _{GS} = 0 V; T _j = 175 °C	-	82	500	μA
I _{GSS}	gate leakage current	V _{GS} = 20 V; V _{DS} = 0 V; T _j = 25 °C	-	2	100	nA
		V _{GS} = -10 V; V _{DS} = 0 V; T _j = 25 °C	-	2	100	nA
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 25 A; T _j = 25 °C; Fig. 11	1.51	2.16	2.51	mΩ
		V _{GS} = 10 V; I _D = 25 A; T _j = 105 °C; Fig. 12	2.14	3.32	3.99	mΩ
		V _{GS} = 10 V; I _D = 25 A; T _j = 125 °C; Fig. 12	2.37	3.64	4.39	mΩ
		V _{GS} = 10 V; I _D = 25 A; T _j = 175 °C; Fig. 12	2.97	4.52	5.47	mΩ
R _G	gate resistance	f = 1 MHz; T _j = 25 °C	0.31	0.76	1.91	Ω
Dynamic ch	aracteristics		'	<u> </u>	,	'
Q _{G(tot)}	total gate charge	I _D = 25 A; V _{DS} = 32 V; V _{GS} = 10 V;	-	38	54	nC
Q _{GS}	gate-source charge	Fig. 13; Fig. 14	-	11	16	nC
Q_{GD}	gate-drain charge		-	7	14	nC

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Symbol	Parameter	Conditions		Min	Тур	Max	Unit
C _{iss}	input capacitance	V _{DS} = 25 V; V _{GS} = 0 V; f = 1 MHz;		-	2709	3793	pF
C _{oss}	output capacitance	T _j = 25 °C; <u>Fig. 15</u>		-	729	1021	pF
C _{rss}	reverse transfer capacitance			-	127	279	pF
t _{d(on)}	turn-on delay time	$V_{DS} = 30 \text{ V}; R_L = 1.2 \Omega; V_{GS} = 10 \text{ V};$		-	10	-	ns
t _r	rise time	$R_{G(ext)} = 5 \Omega$		-	8.5	-	ns
t _{d(off)}	turn-off delay time			-	22	-	ns
t _f	fall time	1		-	11	-	ns
Source-dra	ain diode				'	'	'
V _{SD}	source-drain voltage	$I_S = 25 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}; Fig. 16$		-	0.79	1	V
t _{rr}	reverse recovery time	$I_S = 25 \text{ A}; dI_S/dt = -100 \text{ A/}\mu\text{s}; V_{GS} = 0 \text{ V};$		-	30.1	-	ns
Q _r	recovered charge	$V_{DS} = 20 \text{ V}; T_j = 25 \text{ °C}; Fig. 17$	[1]	-	19.1	-	nC
S	softness factor			-	0.89	-	
		I_S = 25 A; dI_S/dt = -500 A/ μ s; V_{GS} = 0 V; V_{DS} = 20 V; T_j = 25 °C; Fig. 17		-	0.67	-	

[1] includes capacitive recovery

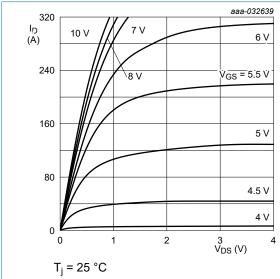


Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

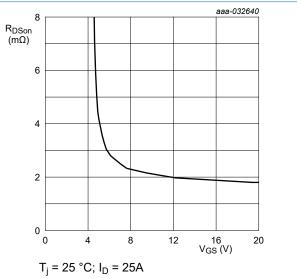


Fig. 7. Drain-source on-state resistance as a function of gate-source voltage; typical values

N-channel 40 V, 2.5 mOhm standard level MOSFET in LFPAK88

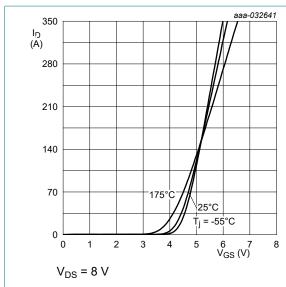


Fig. 8. Transfer characteristics; drain current as a function of gate-source voltage; typical values

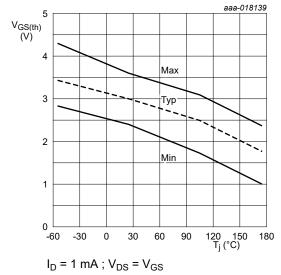


Fig. 10. Gate-source threshold voltage as a function of junction temperature

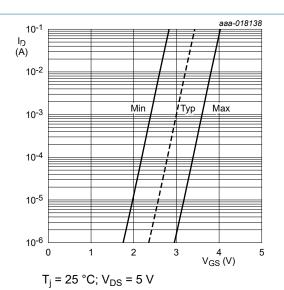


Fig. 9. Sub-threshold drain current as a function of gate-source voltage

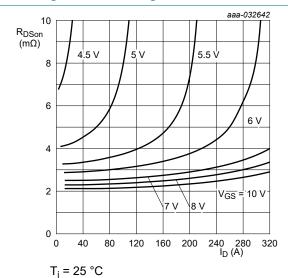


Fig. 11. Drain-source on-state resistance as a function of drain current; typical values

N-channel 40 V, 2.5 mOhm standard level MOSFET in LFPAK88

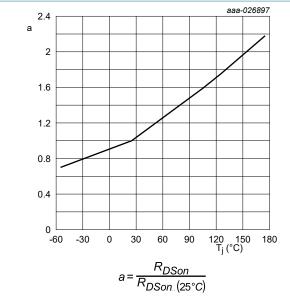


Fig. 12. Normalized drain-source on-state resistance factor as a function of junction temperature

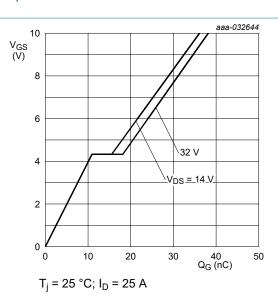


Fig. 13. Gate-source voltage as a function of gate charge; typical values

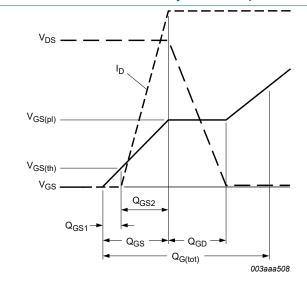


Fig. 14. Gate charge waveform definitions

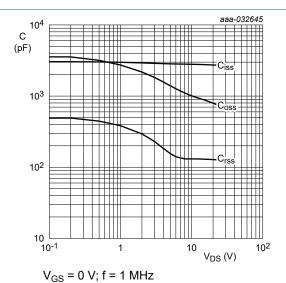


Fig. 15. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

N-channel 40 V, 2.5 mOhm standard level MOSFET in LFPAK88

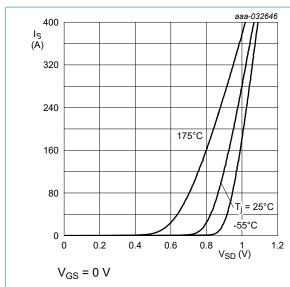


Fig. 16. Source-drain (diode forward) current as a function of source-drain (diode forward) voltage; typical values

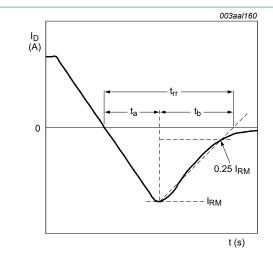


Fig. 17. Reverse recovery timing definition

11. Package outline

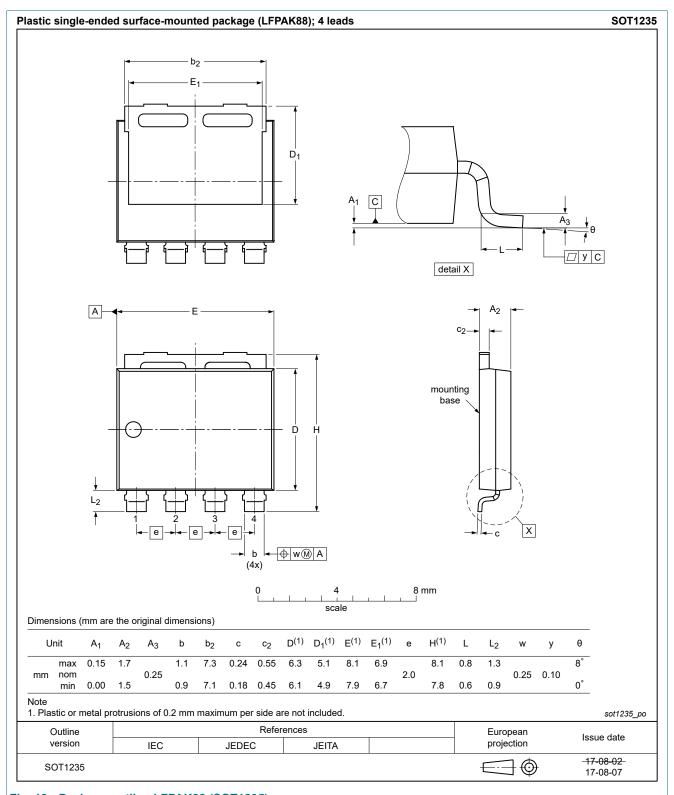
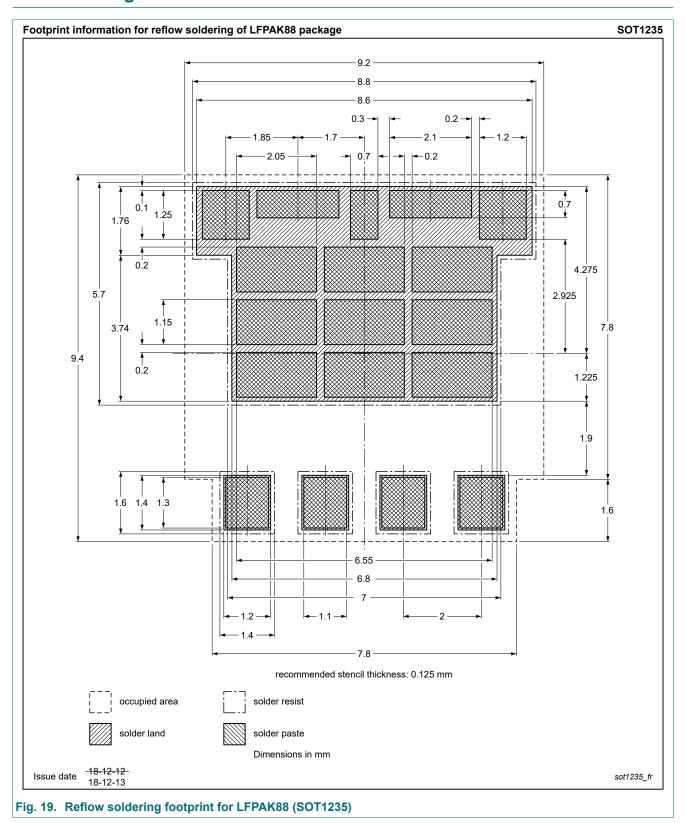


Fig. 18. Package outline LFPAK88 (SOT1235)

12. Soldering



13. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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