

65 V, 100 mA NPN general-purpose transistor Rev. 2 — 12 May 2021

Product data sheet

1. General description

NPN general-purpose transistor in an ultra small DFN1412D-3 (SOT8009) leadless Surface-Mounted Device (SMD) plastic package with side-wettable flanks.

Table 1. Product overview

Type number	Package		Package		PNP complement:
	Nexperia	JEDEC			
BC846AQC-Q	SOT8009 MO-340CA	MO-340CA	BC856AQC-Q		
BC846BQC-Q			BC856BQC-Q		

2. Features and benefits

- High power dissipation capability
- High voltage (max. 65 V)
- Suitable for Automatic Optical Inspection (AOI) of solder joint
- Smaller footprint compared to conventional leaded SMD packages
- . Low package height of 0.5 mm
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- General-purpose switching and amplification •
- Space restricted applications •

4. Quick reference data

Table 2. Quick reference data

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	65	V
I _C	collector current		-	-	100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	200	mA
h _{FE}	DC current gain					
	BC846AQC-Q	V _{CE} = 5 V; I _C = 2 mA	110	-	220	
	BC846BQC-Q		200	-	450	

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5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base		С
2	E	emitter		
3	С	collector		B-f
			3	E
				sym021
			Bottom view	

6. Ordering information

Table 4. Ordering information

Type number	Package	ackage					
	Name	Description	Version				
BC846AQC-Q	DFN1412D-3	plastic, leadless ultra small outline package with	SOT8009				
BC846BQC-Q		sidewettable flanks (SWF); 3 terminals; 0.8 mm pitch; 1.4 mm x 1.2 mm x 0.48 mm body					

7. Marking

Table 5. Marking	
Type number	Marking code
BC846AQC-Q	9R
BC846BQC-Q	9S

8. Limiting values

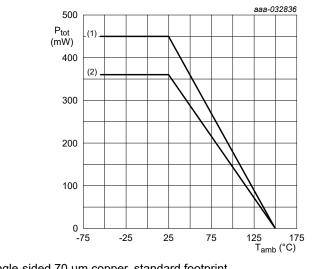
Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	80	V
V _{CEO}	collector-emitter voltage	open base		-	65	V
V _{EBO}	emitter-base voltage	open collector		-	6	V
I _C	collector current			-	100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	200	mA
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	360	mW
			[2]	-	450	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided 35 µm copper; tin-plated and standard footprint.
 Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided 70 µm copper; tin-plated and standard footprint.



(1) FR4 PCB; single-sided 70 µm copper, standard footprint (2) FR4 PCB; single-sided 35 µm copper, standard footprint

Fig. 1. Power derating curves DFN1412D-3 (SOT8009)

9. Thermal characteristics

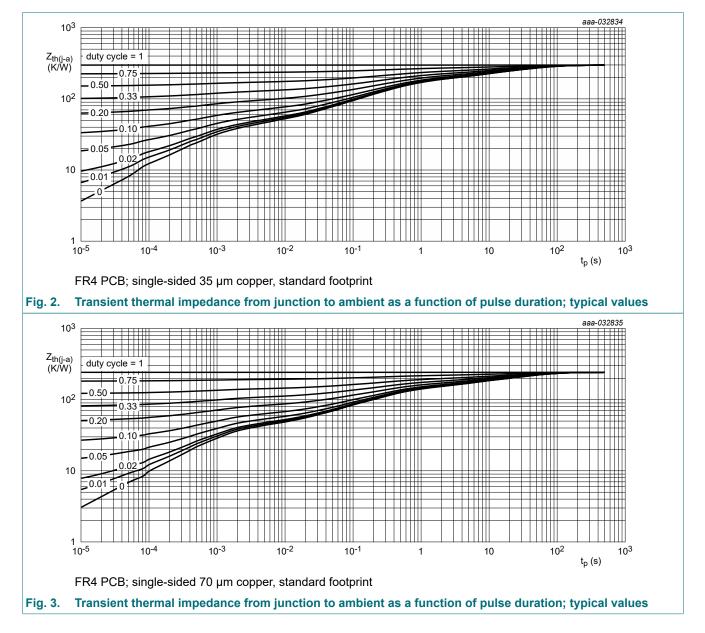
Table 7. Thermal characteristics

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	348	K/W
			[2]	-	-	278	K/W

[1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided 35 µm copper; tin-plated and standard footprint.

[2] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided 70 µm copper; tin-plated and standard footprint.



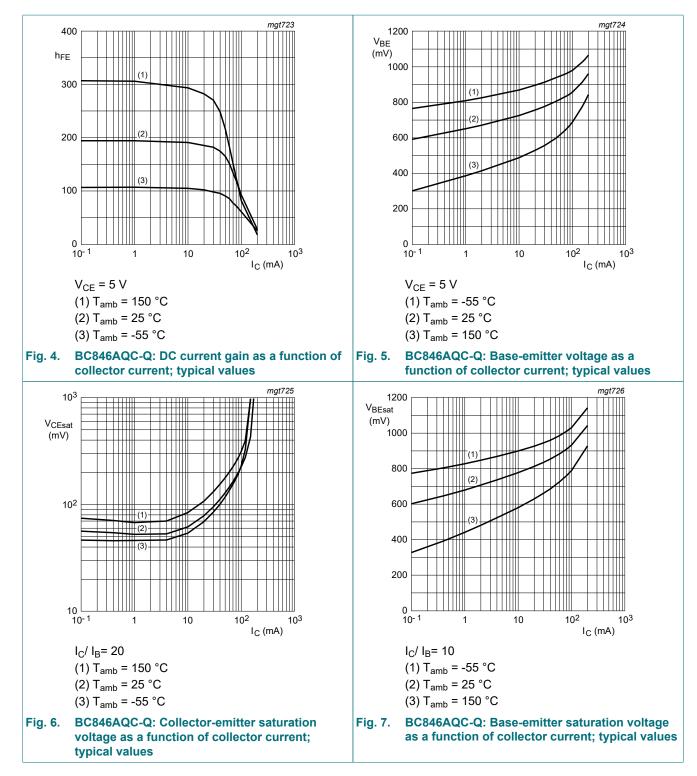
10. Characteristics

Table 8. Characteristics

 T_{amb} = 25 °C unless otherwise specified.

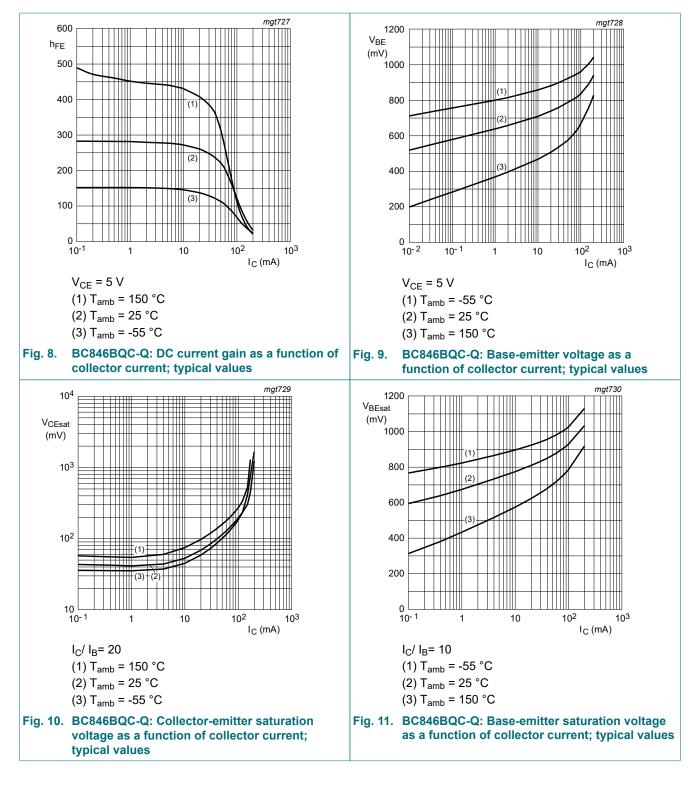
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 μA; I _E = 0 A		80	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = 2 mA; I _B = 0 A		65	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	I _E = 100 μA; I _C = 0 A		6	-	-	V
I _{CBO}	collector-base cut-off	V _{CB} = 30 V; I _E = 0 A		-	-	15	nA
	current	V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C		-	-	5	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A		-	-	100	nA
h _{FE}	DC current gain	1					
	BC846AQC-Q	V _{CE} = 5 V; I _C = 2 mA		110	-	220	
	BC846BQC-Q			200	-	450	
V _{CEsat} collector-emitter saturation voltage		I _C = 10 mA; I _B = 0.5 mA		-	-	200	mV
	saturation voltage	I _C = 100 mA; I _B = 5 mA	[1]	-	-	400	mV
V _{BE}	base-emitter voltage	V _{CE} = 5 V ; I _C = 2 mA	[2]	580	-	700	mV
		V _{CE} = 5 V ; I _C = 10 mA	[2]	-	-	770	mV
V _{BEsat}	base-emitter saturation	I _C = 10 mA ; I _B = 0.5 mA		-	760	-	mV
	voltage	I _C = 100 mA ; I _B = 5 mA	[1]	-	900	-	mV
f _T	transition frequency	V _{CE} = 5 V; I _C = 10 mA; f = 100 MHz		100	-	-	MHz
C _c	collector capacitance	V _{CB} = 10 V; I _E = i _e = 0 A; f = 1 MHz		-	-	3	pF
C _e	emitter capacitance	V _{EB} = 0.5 V; I _E = i _e = 0 A; f = 1 MHz		-	11	-	pF
NF	noise figure	V_{CE} = 5 V; I _C = 200μA; R _S = 2 kΩ; f = 1 kHz; B = 200 Hz		-	-	10	dB

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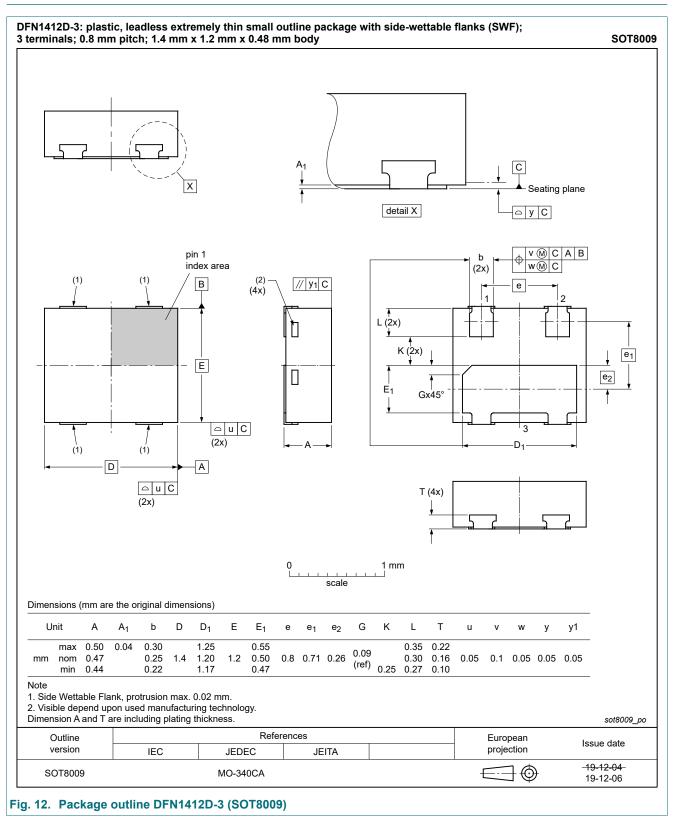


11. Test information

Quality information

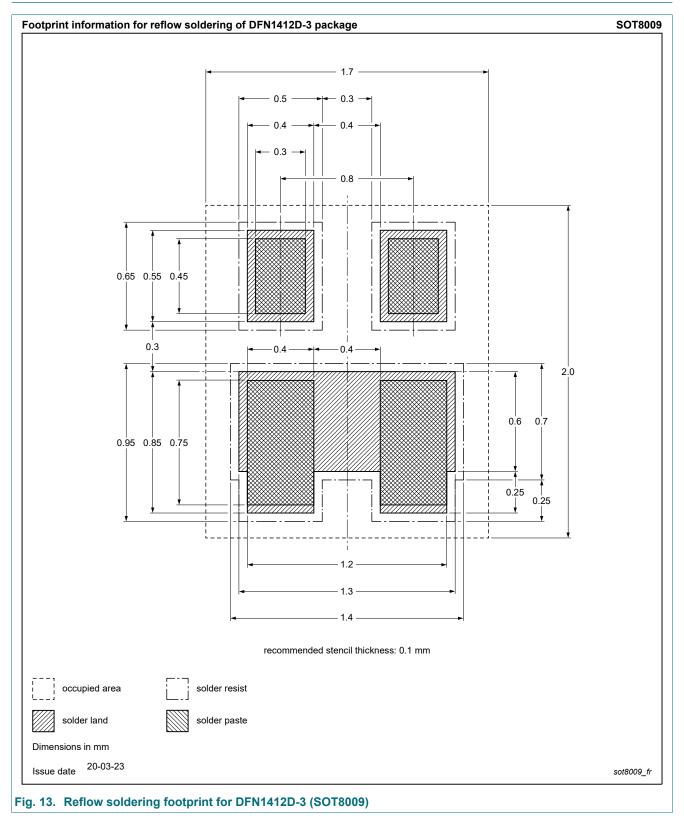
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline



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13. Soldering



14. Revision history

Table 9. Revision history							
Document ID	Release date	Data sheet status	Change notice	Supersedes			
BC846xQC-Q_SER v.2	20210512	Product data sheet	-	BC846xQC-Q_SER v.1			
Modifications:	Features and be	 Features and benefits: added recommendation for automotive applications 					
BC846xQC-Q_SER v.1	20210304	Product data sheet	-	-			

BC846XQC-Q_SER

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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