DOSEMI

IGBT

DG100X07T2

650V/100A IGBT with Diode

General Description

DOSEMI IGBT Power Discrete provides ultra low conduction loss as well as low switching loss. They are designed for the applications such as general inverters and UPS.

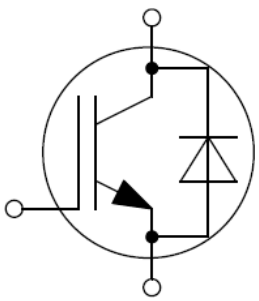
Features

- Low $V_{CE(sat)}$ Trench IGBT technology
- Low switching loss
- Maximum junction temperature 175°C
- $V_{CE(sat)}$ with positive temperature coefficient
- Fast & soft reverse recovery anti-parallel FWD
- Lead free package

Typical Applications

- Inverter for motor drive
- AC and DC servo drive amplifier
- Uninterruptible power supply

Equivalent Circuit Schematic



Absolute Maximum Ratings T_C=25°C unless otherwise noted

IGBT

Symbol	Description	Value	Unit
V_{CES}	Collector-Emitter Voltage	650	V
V_{GES}	Gate-Emitter Voltage	±20	V
I_C	Collector Current @ T_C=25°C @ T_C=130°C	200 100	A
I_{CM}	Pulsed Collector Current t_p limited by T_{jmax}	400	A
P_D	Maximum Power Dissipation @ T_j=175°C	1071	W

Diode

Symbol	Description	Value	Unit
V_{RRM}	Repetitive Peak Reverse Voltage	650	V
I_F	Diode Continuous Forward Current @ T_C=25°C @ T_C=120°C	200 100	A
I_{FM}	Diode Maximum Forward Current t_p limited by T_{jmax}	400	A

Discrete

Symbol	Description	Values	Unit
T_{jop}	Operating Junction Temperature	-40 to +175	°C
T_{STG}	Storage Temperature Range	-55 to +150	°C
T_S	Soldering Temperature,1.6mm from case for 10s	260	°C

IGBT Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{CE(sat)}$	Collector to Emitter Saturation Voltage	I_C=100A,V_{GE}=15V, T_j=25°C		1.45	1.90	V
		I_C=100A,V_{GE}=15V, T_j=125°C		1.60		
		I_C=100A,V_{GE}=15V, T_j=150°C		1.70		
$V_{GE(th)}$	Gate-Emitter Threshold Voltage	I_C=1.60mA,V_{CE}=V_{GE}, T_j=25°C	5.1	5.8	6.5	V
I_{CES}	Collector Cut-Off Current	V_{CE}=V_{CES},V_{GE}=0V, T_j=25°C			1.0	mA
I_{GES}	Gate-Emitter Leakage Current	V_{GE}=V_{GES},V_{CE}=0V, T_j=25°C			400	nA
R_{Gint}	Internal Gate Resistance			2.0		Ω
C_{ies}	Input Capacitance	V_{CE}=25V,f=1MHz, V_{GE}=0V		11.6		nF
C_{res}	Reverse Transfer Capacitance			0.23		nF
Q_G	Gate Charge	V_{GE}=-15…+15V		0.69		μC
$t_{d(on)}$	Turn-On Delay Time	V_{CC}=300V,I_C=100A, R_G=3.3Ω,V_{GE}=±15V, T_j=25°C		40		ns
t_r	Rise Time			20		ns
$t_{d(off)}$	Turn-Off Delay Time			192		ns
t_f	Fall Time			40		ns
E_{on}	Turn-On Switching Loss			0.44		mJ
E_{off}	Turn-Off Switching Loss			2.00		mJ
$t_{d(on)}$	Turn-On Delay Time	V_{CC}=300V,I_C=100A, R_G=3.3Ω,V_{GE}=±15V, T_j=125°C		48		ns
t_r	Rise Time			24		ns
$t_{d(off)}$	Turn-Off Delay Time			208		ns
t_f	Fall Time			52		ns
E_{on}	Turn-On Switching Loss			0.68		mJ
E_{off}	Turn-Off Switching Loss			2.68		mJ
$t_{d(on)}$	Turn-On Delay Time	V_{CC}=300V,I_C=100A, R_G=3.3Ω,V_{GE}=±15V, T_j=150°C		52		ns
t_r	Rise Time			24		ns
$t_{d(off)}$	Turn-Off Delay Time			216		ns
t_f	Fall Time			60		ns
E_{on}	Turn-On Switching Loss			0.78		mJ
E_{off}	Turn-Off Switching Loss			2.80		mJ
I_{SC}	SC Data	t_P≤6μs,V_{GE}=15V, T_j=150°C,V_{CC}=360V, V_{CEM}≤650V		500		A

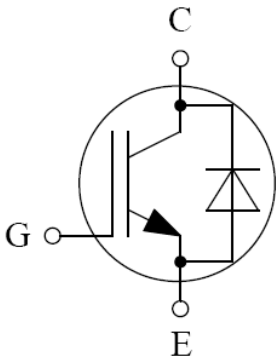
Diode Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_F	Diode Forward Voltage	I_F=100A,V_{GE}=0V,T_i=25°C		1.55	2.00	V
		I_F=100A,V_{GE}=0V,T_j=125°C		1.50		
		I_F=100A,V_{GE}=0V,T_j=150°C		1.45		
Q_r	Recovered Charge	V_R=300V,I_F=100A, -di/dt=3520A/μs,V_{GE}=-15V T_j=25°C		4.6		μC
I_{RM}	Peak Reverse Recovery Current			99		A
E_{rec}	Reverse Recovery Energy			1.32		mJ
Q_r	Recovered Charge	V_R=300V,I_F=100A, -di/dt=3520A/μs,V_{GE}=-15V T_j=125°C		8.6		μC
I_{RM}	Peak Reverse Recovery Current			121		A
E_{rec}	Reverse Recovery Energy			2.37		mJ
Q_r	Recovered Charge	V_R=300V,I_F=100A, -di/dt=3520A/μs,V_{GE}=-15V T_j=150°C		9.9		μC
I_{RM}	Peak Reverse Recovery Current			127		A
E_{rec}	Reverse Recovery Energy			2.64		mJ

Discrete Characteristics T_C=25°C unless otherwise noted

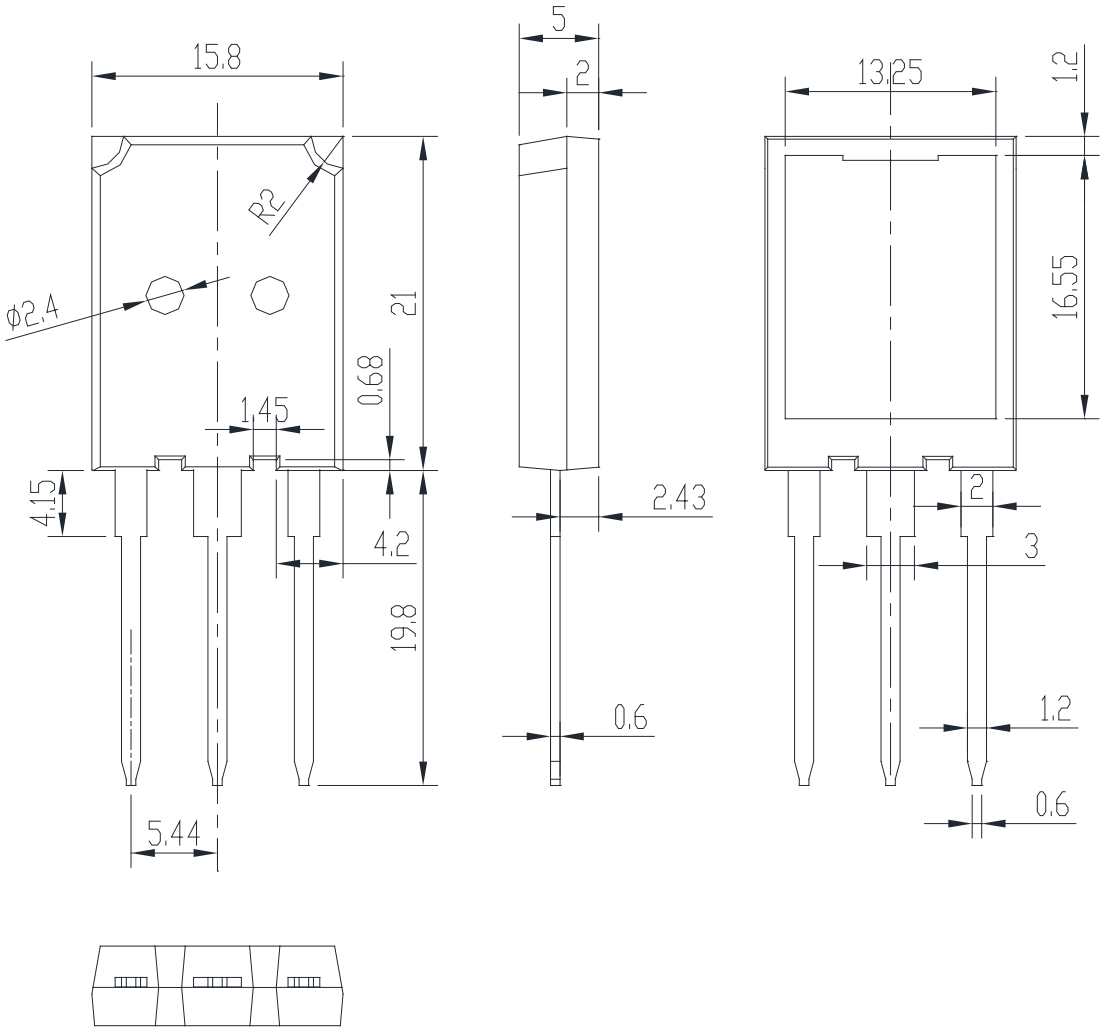
Symbol	Parameter	Min.	Typ.	Max.	Unit
R_{thJC}	Junction-to-Case (per IGBT) Junction-to-Case (per Diode)			0.140 0.262	K/W
R_{thJA}	Junction-to-Ambient		40		K/W

Circuit Schematic



Package Dimensions

Dimensions in Millimeters



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