



Final Product Change Notification

202107010F01 : i.MX RT1020/ i.MX RT1015 New Silicon Revision With New Part Number and Errata Update

Note: This notice is NXP Company Proprietary.

Issue Date: Aug 15, 2021 **Effective date:** Nov 13, 2021

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Change Category

<input type="checkbox"/> Wafer Fab Process	<input type="checkbox"/> Assembly Process	<input checked="" type="checkbox"/> Product Marking	<input type="checkbox"/> Test Process	<input checked="" type="checkbox"/> Design
<input type="checkbox"/> Wafer Fab Materials	<input type="checkbox"/> Assembly Materials	<input type="checkbox"/> Mechanical Specification	<input type="checkbox"/> Test Equipment	<input checked="" type="checkbox"/> Errata
<input type="checkbox"/> Wafer Fab Location	<input type="checkbox"/> Assembly Location	<input type="checkbox"/> Packing/Shipping/Labeling	<input type="checkbox"/> Test Location	<input type="checkbox"/> Electrical spec./Test coverage
<input type="checkbox"/> Firmware	<input type="checkbox"/> Other			

PCN Overview

Description

NXP Semiconductors is announcing i.MX RT1020 and i.MX RT1015 maskset revision 1N87V (from 0N87V) which has been qualified with new part numbers MIMXRT1021xxxxB / MIMXRT1015xxxxB.

The new mask revision has the below improvement:

Fixed ERR050101 USB: Endpoint conflict issue in device mode. An endpoint conflict occurs when the USB is working in device mode during an isochronous communication.

Due to the unprecedented supply constraints within the Semiconductor Industry and existing backlog profile, new orders must be placed on the new replacement parts immediately. There are cases that existing backlog will need to be migrated to the replacement parts. NXP will work with each customer to manage those transitions.
Please contact your NXP sales or marketing representative for additional information.

i.MX RT1015/RT1020 data sheets were updated to revision 3 with adding new part numbers ordering information.

i.MX RT1015/RT1020 Errata were updated to revision 3:

Add 3 additional errata:

ERR050606: LPSPI: TCR value does not get resampled when polling the register

ERR050607: LPSPI: TCR[FRAMESZ] can be ignored when TCR[TXMSK] = 1b1

ERR050538: SOC: Potential boot failure on system reset if SJC_DISABLE fuse is blown

Update ERR050101, Fix in A1 revision.

The updated documents can be found at:

https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/i-mx-rt-crossover-mcus/i-mx-rt1020-crossover-mcu-with-arm-cortex-m7-core:i.MX-RT1020?tab=Documentation_Tab

https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/i-mx-rt-crossover-mcus/i-mx-rt1015-crossover-mcu-with-arm-cortex-m7-core:i.MX-RT1015?tab=Documentation_Tab

Reason

The release of i.MX RT1020 / i.MX RT1015 new maskset 1N87V fixed ERR050101 USB: Endpoint conflict issue in device mode.

Errata have been updated to correct errors and / or provide additional technical clarification on some device features.

Identification of Affected Products

Replacement part type created, see Parts Affected list

Product Availability

Sample Information

Samples availability differs by product-see attached sample plan

Production

Planned first shipment Jan 31, 2022

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

Data Sheet Revision

A new datasheet will be issued

Disposition of Old Products

Existing inventory will be shipped until depleted

Additional information

Self qualification: [view online](#)

Additional documents: [view online](#)

Timing and Logistics

In compliance with JEDEC J-STD-046, your acknowledgement of this change is expected by Sep 14, 2021.

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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NXP Quality Management Team.

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Changed Orderable Part#	12NC	Product Type	Product Description	Package Outline	Package Description	Product Status	Customer Specific Indicator	New Orderable Part#	12NC New	Product Type New	Product Description New	Product Line
MIMXRT1015CAF4A	93538089 2557	MIMXRT1015C AF4A	i.MXRT1015, 100LQFP	(L)QFP100	SOT407-3	RFS	No	MIMXRT1015CA F4B	93539679 4557	MIMXRT1015C AF4B	i.MXRT1015 100LQFP USB	BLM1
MIMXRT1015DAF5A	93538089 3557	MIMXRT1015D AF5A	i.MXRT1015, 100LQFP	(L)QFP100	SOT407-3	RFS	No	MIMXRT1015DA F5B	93539679 5557	MIMXRT1015D AF5B	i.MXRT1015 100LQFP USB	BLM1
MIMXRT1021CAF4A	93536031 7557	MIMXRT1021C AF4A	i.MXRT1020 100LQFP	(L)QFP100	SOT407-3	RFS	No	MIMXRT1021CA F4B	93539679 6557	MIMXRT1021C AF4B	i.MXRT1020 100LQFP USB	BLM1
MIMXRT1021DAF5A	93536031 9557	MIMXRT1021D AF5A	i.MXRT1020 100LQFP	(L)QFP100	SOT407-3	RFS	No	MIMXRT1021DA F5B	93539679 9557	MIMXRT1021D AF5B	i.MXRT1020 100LQFP USB	BLM1
MIMXRT1021CAG4A	93536031 8557	MIMXRT1021C AG4A	i.MXRT1020 144LQFP	(L)QFP144	SOT486-2	RFS	No	MIMXRT1021CA G4B	93539679 8557	MIMXRT1021C AG4B	i.MXRT1020 144LQFP USB	BLM1

MIMXRT1021DAG5A	935360321557	MIMXRT1021DAG5A	i.MXRT1020144LQFP	(L)QFP144	SOT486-2	RFS	No	MIMXRT1021DAG5B	935396801557	MIMXRT1021DAG5B	i.MXRT1020144LQFP USB	BLM1
MIMXRT1021CAG4AR	935360318528	MIMXRT1021CAG4AR	i.MXRT1020144LQFP	(L)QFP144	SOT486-2	RFS	No	MIMXRT1021CAG4BR	935396798528	MIMXRT1021CAG4BR	i.MXRT1020144LQFP USB	BLM1
MIMXRT1015CAF4AR	935380892528	MIMXRT1015CAF4AR	i.MXRT1015,100LQFP	(L)QFP100	SOT407-3	RFS	No	MIMXRT1015CAF4BR	935396794528	MIMXRT1015CAF4BR	i.MXRT1015100LQFP USB	BLM1