TOSHIBA High Efficiency Rectifier Silicon Epitaxial Type

CRH01

Switching Mode Power Supply Applications

Unit: mm

Repetitive peak reverse voltage : V_{RRM} = 200 V
 Average forward current : IF (AV) = 1 A
 Peak forward voltage : V_{EM} = 0.98 V (M)

Peak forward voltage : V_{FM} = 0.98 V (Max.)
 Very Fast Reverse-Recovery Time : t_{rr} = 35 ns (Max.)

• Suitable for compact assembly due to small surface-mount package "S-FLATTM" (Toshiba package name)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Repetitive peak reverse voltage	VRRM	200	V
Average forward current	IF(AV)	1	Α
Non-repetitive peak forward surge current	IFSM	15 (50 Hz)	Α
Junction temperature	Tj	-40 to 150	°C
Storage temperature	T _{stg}	-40 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e.

JEDEC —

JEITA —

TOSHIBA 3-2A1A

Weight: 0.013 g (typ.)

operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

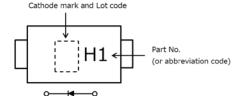
Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
	V _{FM (1)}	I _{FM} = 0.1 A (pulse test)	_	0.71	_		
Peak forward voltage	VFM (2)	I _{FM} = 0.7 A (pulse test)	_	0.86	_	V	
	VFM (3)	I _{FM} = 1 A (pulse test)		0.90	0.98		
Repetitive peak reverse current	I _{RRM}	V _{RRM} = 200 V (pulse test)	_	_	10	μА	
Reverse recovery time	t _{rr}	I _F = 1 A, di/dt = -30 A/μs		_	35	ns	
Forward recovery time	t _{fr}	I _F = 1 A	_	_	100	ns	
Thermal resistance		Device mounted on a ceramic board board size : 50 mm × 50 mm soldering land size : 2 mm × 2 mm board thickness : 0.64 mm	_	_	65	°C/W	
(junction to ambient)	Rth (j-a)	Device mounted on a glass-epoxy board board size : 50 mm × 50 mm soldering land size : 6 mm × 6 mm board thickness : 1.6 mm	_	_	130	C/VV	

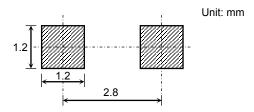
Start of commercial production 1999-07

Marking

Abbreviation Code	Part No.		
H1	CRH01		

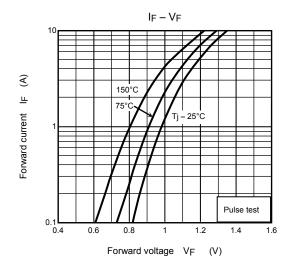


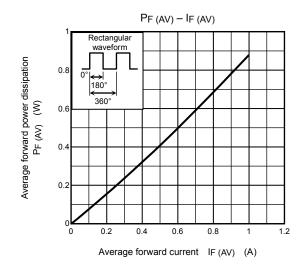
Land pattern dimensions for reference only

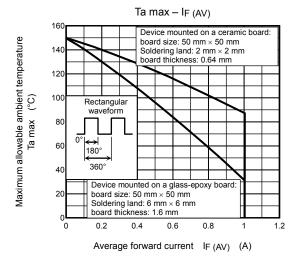


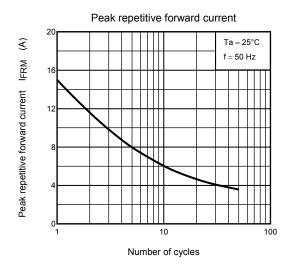
Handling Precaution

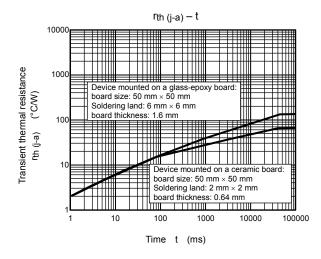
- The absolute maximum ratings are rated values that must not be exceeded during operation, even for an instant. The following are the recommended general derating methods for designing a circuit board using this device.
 - VRRM: We recommend that the worst case voltage, including surge voltage, be no greater than 80% of the absolute maximum rating of VRRM for a DC circuit and be no greater than 50% of that of VRRM for an AC circuit. VRRM has a temperature coefficient of 0.1%/°C. Take this temperature coefficient into account designing a device at low temperature.
 - IF (AV) :We recommend that the worst case current be no greater than 80% of the absolute maximum rating of IF (AV) and Tj be below 120°C. When using this device, take the margin into consideration by using an allowable Ta max-IF (AV) curve.
 - IFSM :This rating specifies peak non-repetitive forward surge current. This only applies to an abnormal operation, which seldom occurs during the lifespan of a device.
 - Tj :Derate device parameters in proportion to this rating in order to ensure high reliability. We recommend that the junction temperature (Tj) of a device be kept below 120°C.
- 2) Thermal resistance (junction-to-ambient) varies with the mounting conditions of a device on a circuit board. An appropriate thermal resistance value should be used, considering the circuit board design and land pattern dimensions (provided for reference only).
- 3) For other design considerations, see the Rectifiers databook or the Toshiba website.

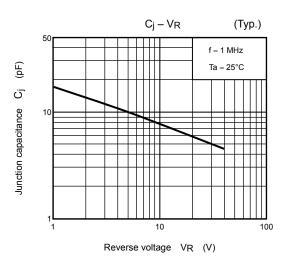












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