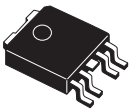
LD29150XXxx
LD29150XX

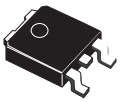
1.5 A, very low drop voltage regulators

Features

- Very low dropout voltage (typ. 0.4 at 1.5 A)
- Guaranteed output current up to 1.5 A
- Fixed and adjustable output voltage (± 1 % at 25 °C)
- Internal current and thermal limit
- Logic controlled electronic shutdown available in PPAK and P^2PAK



PPAK



DPAK

Description

The LD29150 is a high current, high accuracy, low-dropout voltage regulator series. These regulators feature 400 mV dropout voltage and very low ground current. Designed for high current loads, these devices are also used in lower current, extremely low dropout-critical systems, where their tiny dropout voltage and ground current values are important attributes.

Typical applications are in Power supply switching post regulation, Series power supply for monitors, Series power supply for VCRs and TVs, Computer Systems and Battery powered systems.

Contents

1 Diagram

Figure 1. Schematic diagram for adjustable version

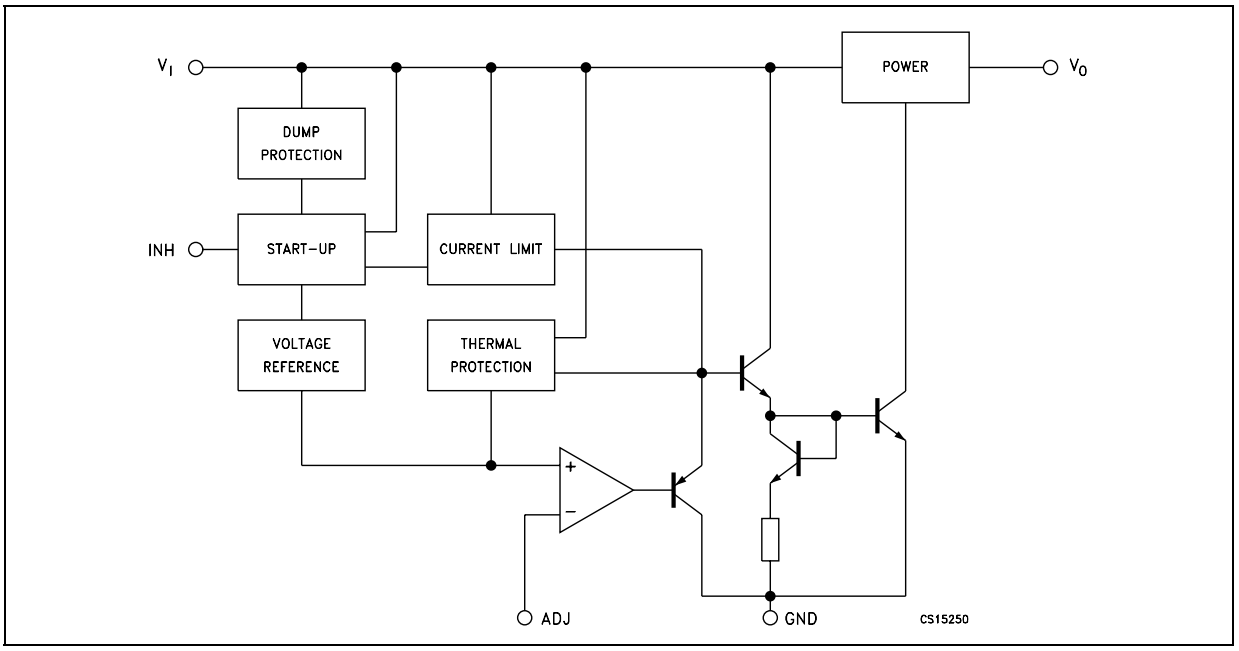
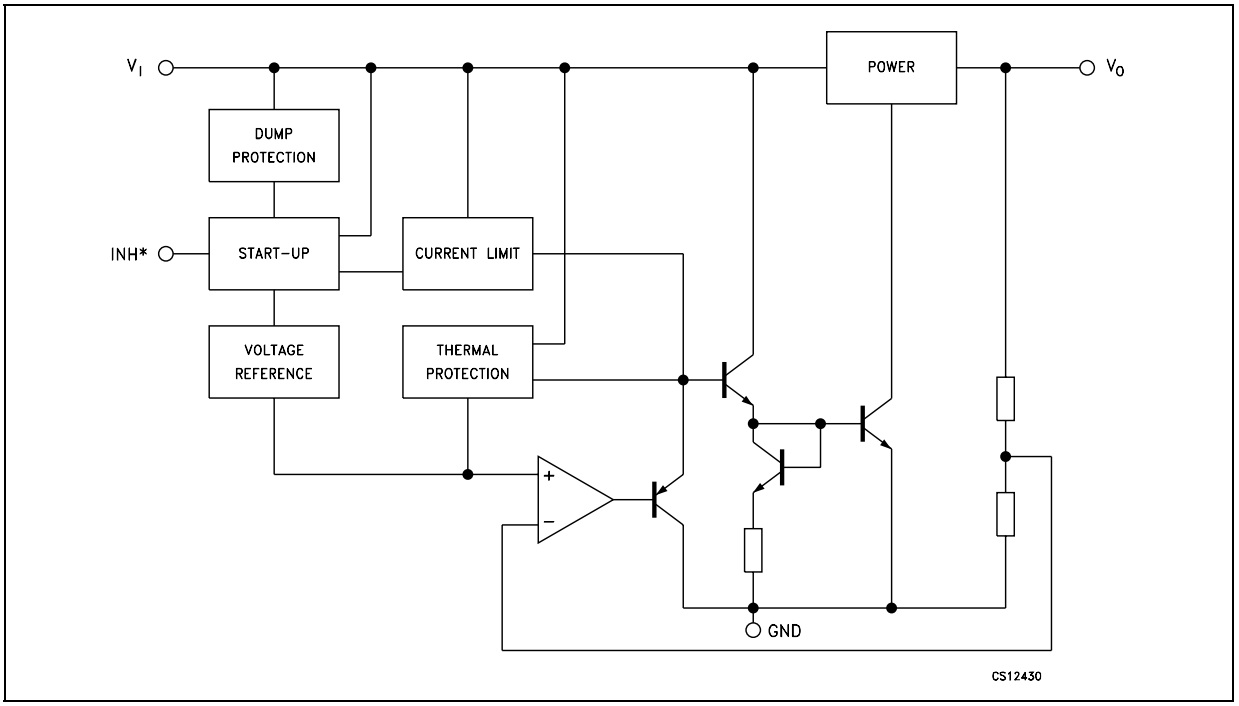


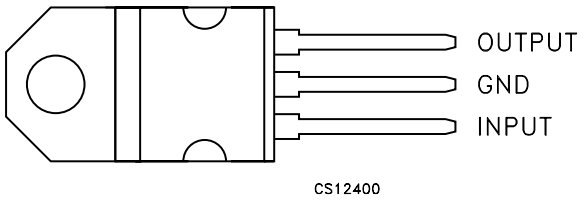
Figure 2. Schematic diagram for fixed version



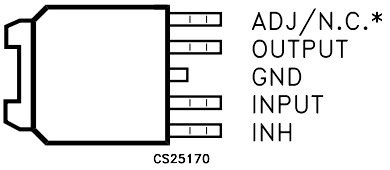
* Only for version with inhibit function.

2 Pin configuration

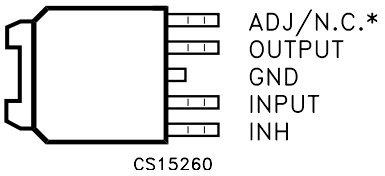
Figure 3. Pin connections (top view)



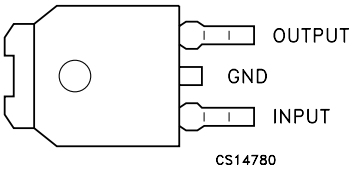
TO-220



P^2PAK



PPAK

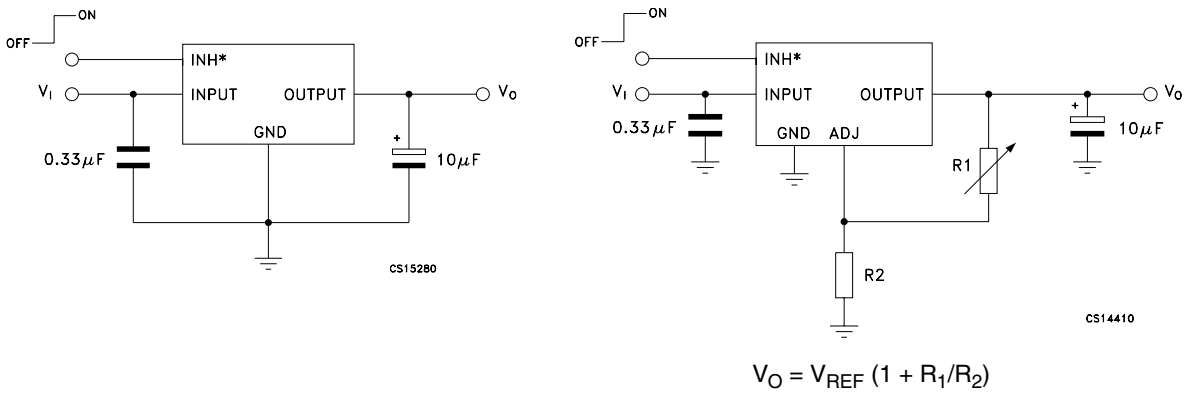


DPAK

* Not connected for fixed version.

3 Typical application

Figure 4. Application circuit



* Only for version with inhibit function.

4 Maximum ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_I	DC input voltage	30 (1)	V
V_O	DC output voltage	-0.3 to 20	V
V_{INH}	Inhibit input voltage	-0.3 to 20	V
I_O	Output current	Internally limited	mA
P_D	Power dissipation	Internally limited	mW
T_{STG}	Storage temperature range	-55 to 150	°C
T_{OP}	Operating junction temperature range	-40 to 125	°C

1. Above 14 V the device is automatically in shut-down.

Note: Absolute maximum ratings are those values beyond which damage to the device may occur. Functional operation under these conditions is not implied.

Table 3. Thermal data

Symbol	Parameter	DPAK	PPAK	P^2PAK	TO-220	Unit
R_{thJA}	Thermal resistance junction-ambient	100	100	60	50	°C/W
R_{thJC}	Thermal resistance junction-case	8	8	3	3	°C/W

5 Electrical characteristics

Table 4. Electrical characteristics of LD29150#15
(I_O = 10 mA, T_J = 25 °C, V_I = 3.5 V, V_{INH} = 2 V (*Note 2*), C_I = 330 nF, C_O = 10 µF, unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_I	Minimum operating input voltage	I_O = 10mA to 1.5A, T_J = -40 to 125°C	2.5			V
V_O	Output voltage	I_O = 10mA to 1.5A, V_I = 3 to 7V T_J = -40 to 125°C	1.485	1.5	1.515	V
			1.47		1.53	
ΔV_O	Load regulation	I_O = 10mA to 1.5A		0.2	1.0	%
ΔV_O	Line regulation	V_I = 3 to 13V		0.06	0.5	%
SVR	Supply voltage rejection	f = 120 Hz, V_I = 3.5 ± 1V, I_O = 0.75A (*Note 1*)	65	75		dB
I_q	Quiescent current	I_O = 0.75A, T_J = -40 to 125°C		15	40	mA
		I_O = 1.5A, T_J = -40 to 125°C		30	80	
		V_I = 13V, V_{INH} = GND, T_J = -40 to 125°C		130	180	µA
I_{sc}	Short circuit current	V_I - V_O = 5.5V		2.2		A
V_{IL}	Control input logic low	OFF MODE, (*Note 2*), T_J = -40 to 125°C			0.8	V
V_{IH}	Control input logic high	ON MODE, (*Note 2*), T_J = -40 to 125°C	2			V
I_{INH}	Control input current	T_J = -40 to 125°C, V_{INH} = 13V		5	10	µA
eN	Output noise voltage	B_P = 10Hz to 100kHz, I_O = 100mA		60		μV_{RMS}
T_{SHDN}	Thermal shutdown			150		°C

Note: 1 Guaranteed by design.
2 Only for version with Inhibit function.

Table 5. Electrical characteristics of LD29150#18
(I_O = 10 mA, T_J = 25 °C, V_I = 3.8 V, V_{INH} = 2 V (*Note 3*), C_I = 330 nF, C_O = 10 µF, unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_O	Output voltage	I_O = 10mA to 1.5A, V_I = 3 to 7.3V T_J = -40 to 125°C	1.782	1.8	1.818	V
			1.764		1.836	
ΔV_O	Load regulation	I_O = 10mA to 1.5A		0.2	1.0	%
ΔV_O	Line regulation	V_I = 3 to 13V		0.06	0.5	%
SVR	Supply voltage rejection	f = 120 Hz, V_I = 3.8 ± 1V, I_O = 0.75A (*Note 1*)	62	72		dB
V_{DROP}	Dropout voltage	I_O = 250mA, T_J = -40 to 125°C (*Note 2*)		0.1		V
		I_O = 0.75A, T_J = -40 to 125°C (*Note 2*)		0.2		
		I_O = 1.5A, T_J = -40 to 125°C (*Note 2*)		0.4	0.7	
I_q	Quiescent current	I_O = 0.75A, T_J = -40 to 125°C		15	40	mA
		I_O = 1.5A, T_J = -40 to 125°C		30	80	
		V_I = 13V, V_{INH} = GND, T_J = -40 to 125°C		130	180	µA
I_{sc}	Short circuit current	V_I - V_O = 5.5V		2.2		A
V_{IL}	Control input logic low	OFF MODE, (*Note 3*), T_J = -40 to 125°C			0.8	V
V_{IH}	Control input logic high	ON MODE, (*Note 3*), T_J = -40 to 125°C	2			V
I_{INH}	Control input current	T_J = -40 to 125°C, V_{INH} = 13V		5	10	µA
eN	Output noise voltage	B_P = 10Hz to 100kHz, I_O = 100mA		72		μV_{RMS}
T_{SHDN}	Thermal shutdown			150		°C

Note: 1 Guaranteed by design.

2 Dropout voltage is defined as the input-to-output differential when the output voltage drops to 99 % of its nominal value with V_O + 1 V applied to V_I.

3 Only for version with Inhibit function.

Table 6. Electrical characteristics of LD29150#25
(I_O = 10 mA, T_J = 25 °C, V_I = 4.5 V, V_{INH} = 2 V (*Note 3*), C_I = 330 nF, C_O = 10 µF, unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_O	Output voltage	I_O = 10mA to 1.5A, V_I = 3.5 to 8V T_J = -40 to 125°C	2.475 2.45	2.5	2.525 2.55	V
ΔV_O	Load regulation	I_O = 10mA to 1.5A		0.2	1.0	%
ΔV_O	Line regulation	V_I = 3.5 to 13V		0.06	0.5	%
SVR	Supply voltage rejection	f = 120 Hz, V_I = 4.5 ±1V, I_O = 0.75A (*Note 1*)	55	70		dB
V_{DROP}	Dropout voltage	I_O = 250mA, T_J = -40 to 125°C (*Note 2*)		0.1		V
		I_O = 0.75A, T_J = -40 to 125°C (*Note 2*)		0.2		
		I_O = 1.5A, T_J = -40 to 125°C (*Note 2*)		0.4	0.7	
I_q	Quiescent current	I_O = 0.75A, T_J = -40 to 125°C		15	40	mA
		I_O = 1.5A, T_J = -40 to 125°C		30	80	
		V_I = 13V, V_{INH} = GND, T_J = -40 to 125°C		130	180	µA
I_{sc}	Short circuit current	V_I - V_O = 5.5V		2.2		A
V_{IL}	Control input logic low	OFF MODE, (*Note 3*), T_J = -40 to 125°C			0.8	V
V_{IH}	Control input logic high	ON MODE, (*Note 3*), T_J = -40 to 125°C	2			V
I_{INH}	Control input current	T_J = -40 to 125°C, V_{INH} = 13V		5	10	µA
eN	Output noise voltage	B_P = 10Hz to 100kHz, I_O = 100mA		100		μV_{RMS}
T_{SHDN}	Thermal shutdown			150		°C

Note: 1 Guaranteed by design.

2 Dropout voltage is defined as the input-to-output differential when the output voltage drops to 99 % of its nominal value with V_O + 1 V applied to V_I.

3 Only for version with Inhibit function.

Table 7. Electrical characteristics of LD29150#33
(I_O = 10 mA, T_J = 25 °C, V_I = 5.3 V, V_{INH} = 2 V (*Note 3*), C_I = 330 nF, C_O = 10 µF, unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_O	Output voltage	I_O = 10mA to 1.5A, V_I = 4.3 to 8.8V T_J = -40 to 125°C	3.267	3.3	3.333	V
			3.234		3.366	
ΔV_O	Load regulation	I_O = 10mA to 1.5A		0.2	1.0	%
ΔV_O	Line regulation	V_I = 4.3 to 13V		0.06	0.5	%
SVR	Supply voltage rejection	f = 120 Hz, V_I = 5.3 ±1V, I_O = 0.75A (*Note 1*)	52	67		dB
V_{DROP}	Dropout voltage	I_O = 250mA, T_J = -40 to 125°C (*Note 2*)		0.1		V
		I_O = 0.75A, T_J = -40 to 125°C (*Note 2*)		0.2		
		I_O = 1.5A, T_J = -40 to 125°C (*Note 2*)		0.4	0.7	
I_q	Quiescent current	I_O = 0.75A, T_J = -40 to 125°C		15	40	mA
		I_O = 1.5A, T_J = -40 to 125°C		30	80	
		V_I = 13V, V_{INH} = GND, T_J = -40 to 125°C		130	180	µA
I_{sc}	Short circuit current	V_I - V_O = 5.5V		2.2		A
V_{IL}	Control input logic low	OFF MODE, (*Note 3*), T_J = -40 to 125°C			0.8	V
V_{IH}	Control input logic high	ON MODE, (*Note 3*), T_J = -40 to 125°C	2			V
I_{INH}	Control input current	T_J = -40 to 125°C, V_{INH} = 13V		5	10	µA
eN	Output noise voltage	B_P = 10Hz to 100kHz, I_O = 100mA		132		μV_{RMS}
T_{SHDN}	Thermal shutdown			150		°C

Note: 1 Guaranteed by design.

2 Dropout voltage is defined as the input-to-output differential when the output voltage drops to 99 % of its nominal value with V_O + 1 V applied to V_I.

3 Only for version with Inhibit function.

Table 8. Electrical characteristics of LD29150#50
(I_O = 10 mA, T_J = 25 °C, V_I = 7 V, V_{INH} = 2 V (*Note 3*), C_I = 330 nF, C_O = 10 µF, unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_O	Output voltage	I_O = 10mA to 1.5A, V_I = 6 to 10.5V T_J = -40 to 125°C	4.95	5	5.05	V
			4.9		5.1	
ΔV_O	Load regulation	I_O = 10mA to 1.5A		0.2	1.0	%
ΔV_O	Line regulation	V_I = 6 to 13V		0.06	0.5	%
SVR	Supply voltage rejection	f = 120 Hz, V_I = 7 ±1V, I_O = 0.75A (*Note 1*)	49	64		dB
V_{DROP}	Dropout voltage	I_O = 250mA, T_J = -40 to 125°C (*Note 2*)		0.1		V
		I_O = 0.75A, T_J = -40 to 125°C (*Note 2*)		0.2		
		I_O = 1.5A, T_J = -40 to 125°C (*Note 2*)		0.4	0.7	
I_q	Quiescent current	I_O = 0.75A, T_J = -40 to 125°C		15	40	mA
		I_O = 1.5A, T_J = -40 to 125°C		30	80	
		V_I = 13V, V_{INH} = GND, T_J = -40 to 125°C		130	180	µA
I_{sc}	Short circuit current	V_I - V_O = 5.5V		2.2		A
V_{IL}	Control input logic low	OFF MODE, (*Note 3*), T_J = -40 to 125°C			0.8	V
V_{IH}	Control input logic high	ON MODE, (*Note 3*), T_J = -40 to 125°C	2			V
I_{INH}	Control input current	T_J = -40 to 125°C, V_{INH} = 13V		5	10	µA
eN	Output noise voltage	B_P = 10Hz to 100kHz, I_O = 100mA		200		μV_{RMS}
T_{SHDN}	Thermal shutdown			150		°C

Note: 1 Guaranteed by design.

2 Dropout voltage is defined as the input-to-output differential when the output voltage drops to 99 % of its nominal value with V_O + 1 V applied to V_I.

3 Only for version with Inhibit function.

Table 9. Electrical characteristics of LD29150#ADJ
(I_O = 10 mA, T_J = 25 °C, V_I = 3.23 V, V_{INH} = 2 V (*Note 3*), C_I = 330 nF, C_O = 10 µF adjust pin tied to output pin)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_I	Minimum operating input voltage	I_O = 10mA to 1.5A, T_J = -40 to 125°C	2.5			V
ΔV_O	Load regulation	I_O = 10mA to 1.5A		0.2	1.0	%
ΔV_O	Line regulation	V_I = 2.5 V to 13V, I_O = 10mA		0.06	0.5	%
V_{REF}	Reference voltage	I_O = 10mA to 1.5A, V_I = 2.5 to 4.5V T_J = -40 to 125°C (*Note 2*)	-1%	1.23	+1%	V
			-2%		+2%	
SVR	Supply voltage rejection	f = 120 Hz, V_I = 3.23 ±1V, I_O = 0.75A (*Note 1*)	45	75		dB
I_q	Quiescent current	I_O = 0.75A, T_J = -40 to 125°C		15	40	mA
		I_O = 1.5A, T_J = -40 to 125°C		30	80	
		V_I = 13V, V_{INH} = GND, T_J = -40 to 125°C		130	180	µA
I_{ADJ}	Adjust pin current	T_J = -40 to 125°C (*Note 1*)			1	µA
I_{sc}	Shoe.523i V	$_I$ - V_O = 5.5V		2.2		A
V_{IL}	Control input logic low	OFF MODE, (*Note 3*),T_J = -40 to 125°C			0.8	V
V_{IH}	Control input logic high	ON MODE, (*Note 3*), T_J = -40 to 125°C	2			V
I_{INH}	Control input current	T_J = -40 to 125°C, V_{INH} = 13V		5	10	µA
eN	Output noise voltage	B_P = 10Hz to 100kHz, I_O = 100mA		50		μV_{RMS}
T_{SHDN}	Thermal shutdown			150		°C

Note: 1 Guaranteed by design.

2 Reference voltage is measured between output and GND pin, with ADJ PIN tied to V_{OUT}.

3 Only for version with Inhibit function.

6 Typical characteristics

Figure 5. Output voltage vs temperature

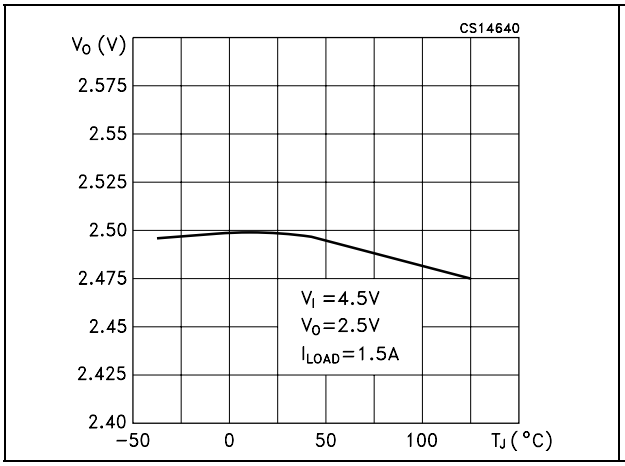


Figure 6. Reference voltage vs temperature

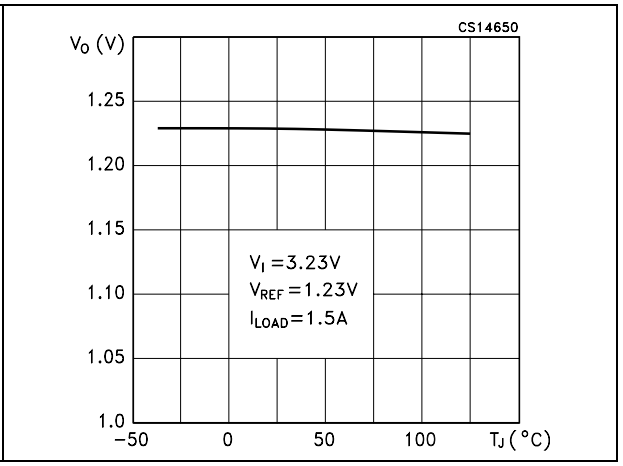


Figure 7. Dropout voltage vs temperature

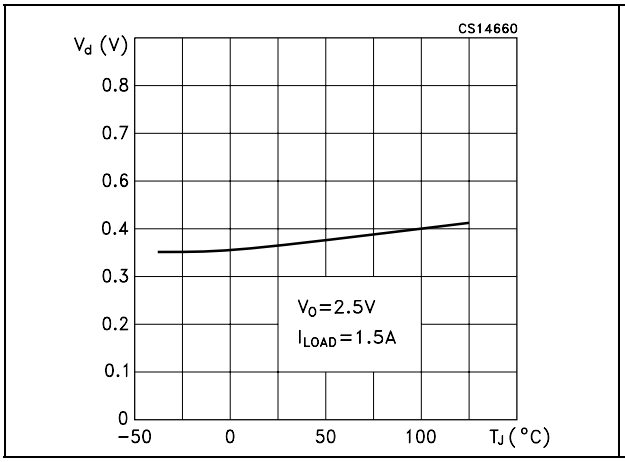


Figure 8. Dropout voltage vs output current

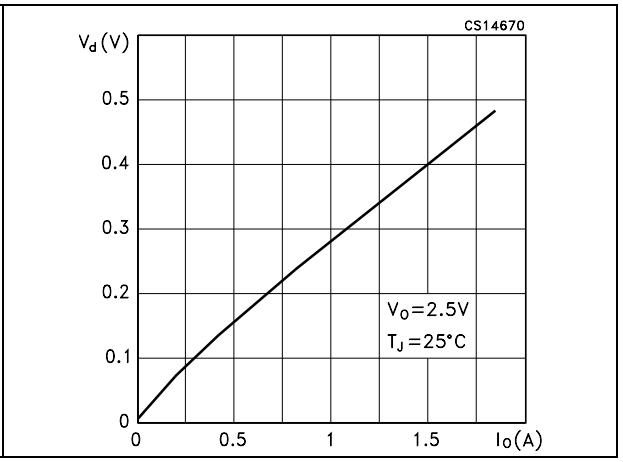


Figure 9. Quiescent current vs output current

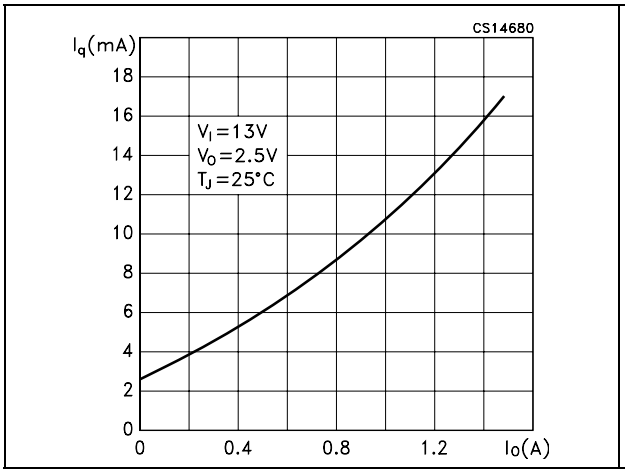


Figure 10. Quiescent current vs output current

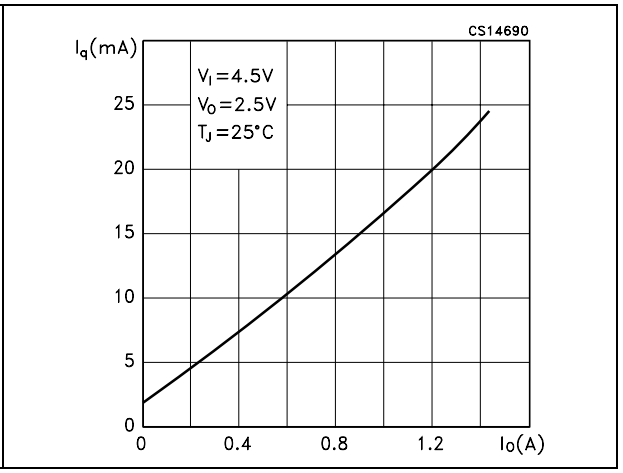


Figure 11. Quiescent current vs supply voltage

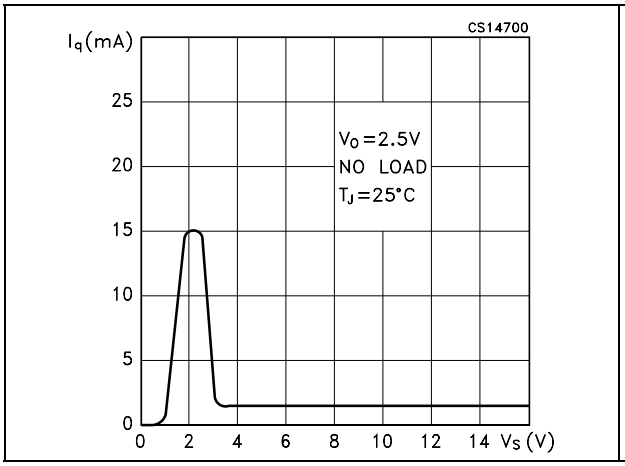


Figure 12. vs temperature

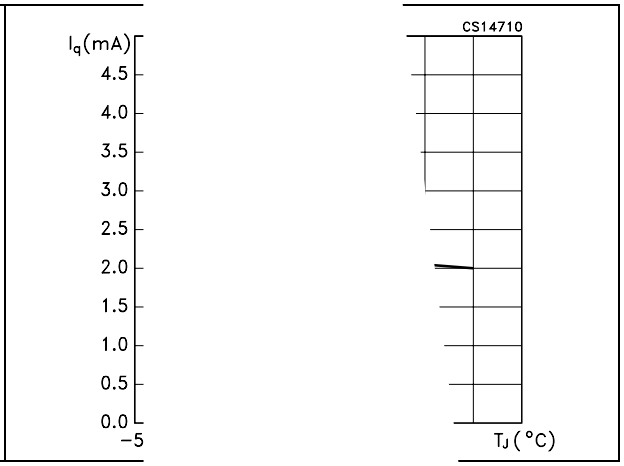


Figure 13. Quiescent current vs temperature

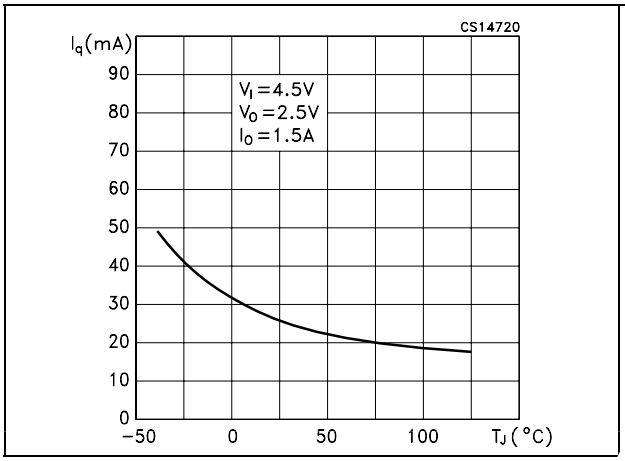


Figure 14. temperature

Figure 15. Adjust pin current vs temperature

gure Supply voltage reje /s temperature

Figure 17. Output voltage vs input voltage

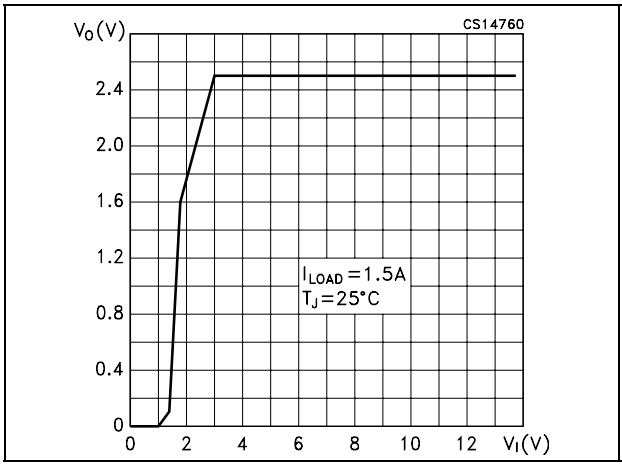


Figure 18. Stability vs C_O

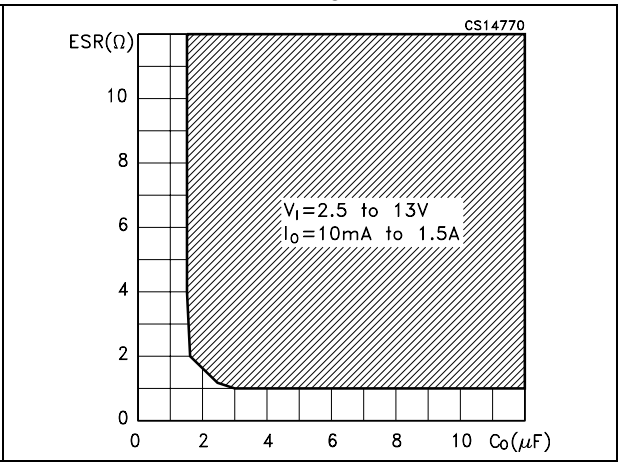
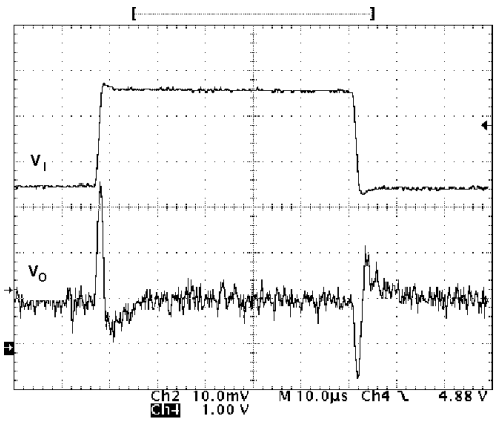
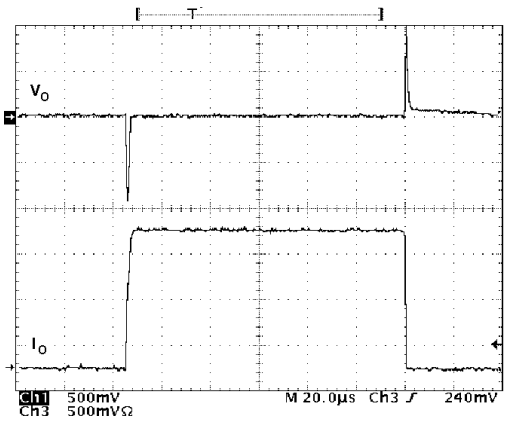


Figure 19. Line transient



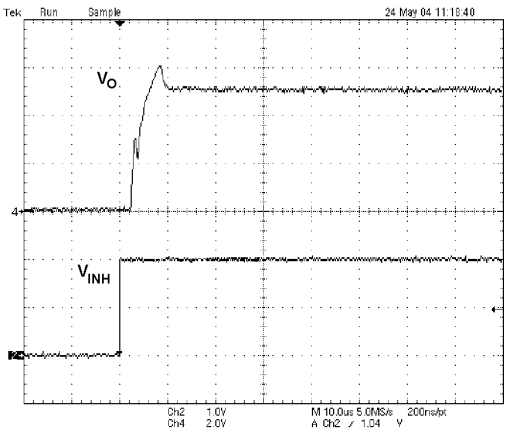
V_I=3.5 to 5.5V, I_O=10mA, V_O=2.5V, C_O=10µF

Figure 20. Load transient



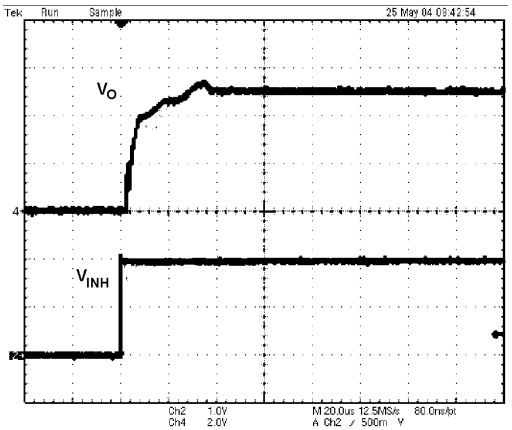
V_I=5V, I_O=10mA to 1.5A, V_O=2.5V, C_O=10µF

Figure 21. Start-up time 10mA



C_O=10µF, I_O=10mA, V_{INH}=2V, V_O=5V, V_I=7V

Figure 22. Start-up time 1.5A



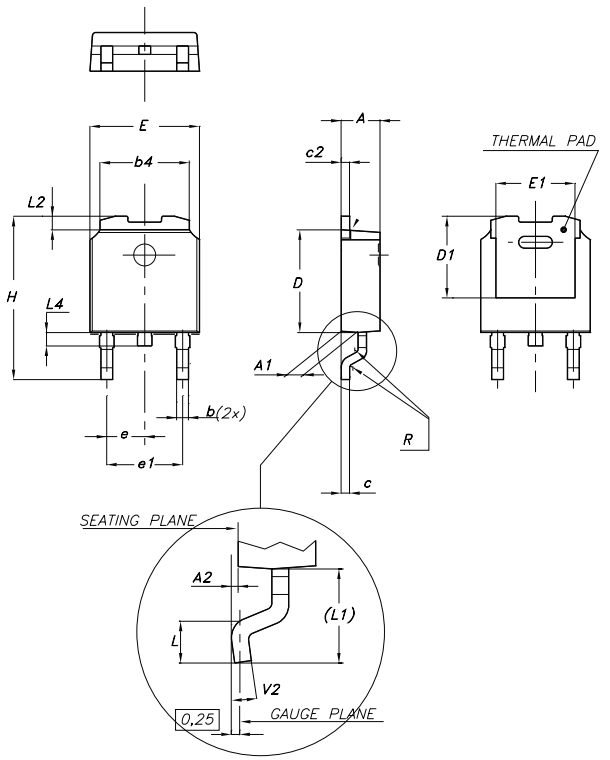
C_O=10µF, I_O=1.5A, V_{INH}=2V, V_O=5V, V_I=7V

7 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second Level Interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

DPAK mechanical data

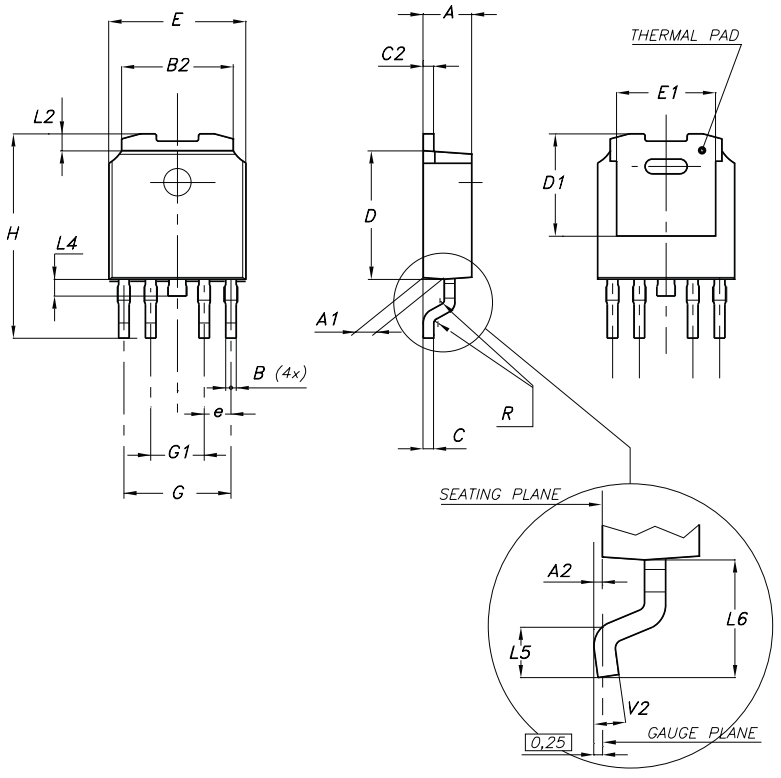
Dim.	mm.			inch.		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.2		2.4	0.086		0.094
A1	0.9		1.1	0.035		0.043
A2	0.03		0.23	0.001		0.009
B	0.64		0.9	0.025		0.035
b4	5.2		5.4	0.204		0.212
C	0.45		0.6	0.017		0.023
C2	0.48		0.6	0.019		0.023
D	6		6.2	0.236		0.244
D1		5.1			0.200	
E	6.4		6.6	0.252		0.260
E1		4.7			0.185	
e		2.28			0.090	
e1	4.4		4.6	0.173		0.181
H	9.35		10.1	0.368		0.397
L	1			0.039		
(L1)		2.8			0.110	
L2		0.8			0.031	
L4	0.6		1	0.023		0.039
R		0.2			0.008	
V2	0°		8°	0°		8°



0068772-F

PPAK mechanical data

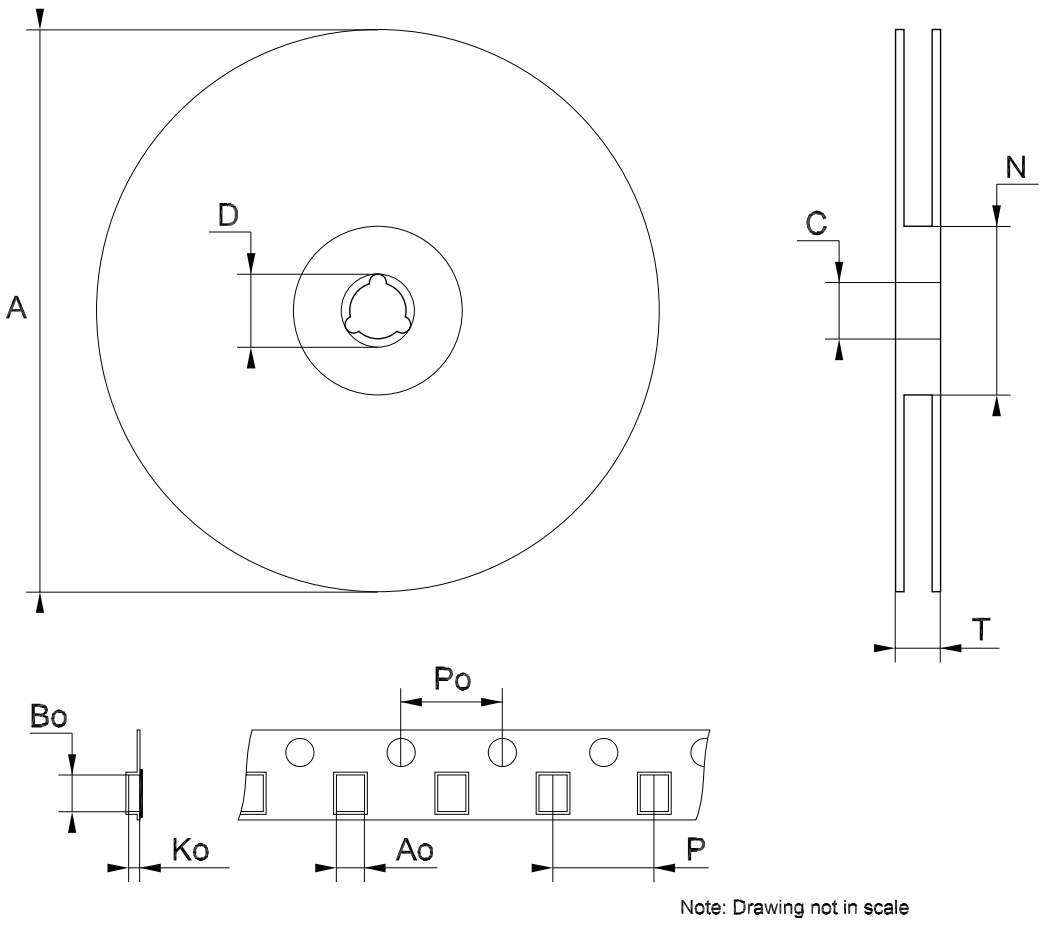
Dim.	mm.			inch.		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.2		2.4	0.086		0.094
A1	0.9		1.1	0.035		0.043
A2	0.03		0.23	0.001		0.009
B	0.4		0.6	0.015		0.023
B2	5.2		5.4	0.204		0.212
C	0.45		0.6	0.017		0.023
C2	0.48		0.6	0.019		0.023
D	6		6.2	0.236		0.244
D1		5.1			0.201	
E	6.4		6.6	0.252		0.260
E1		4.7			0.185	
e		1.27			0.050	
G	4.9		5.25	0.193		0.206
G1	2.38		2.7	0.093		0.106
H	9.35		10.1	0.368		0.397
L2		0.8	1		0.031	0.039
L4	0.6		1	0.023		0.039
L5	1			0.039		
L6		2.8			0.110	



0078180-E

Tape & reel DPAK-PPAK mechanical data

Dim.	mm.			inch.		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			330			12.992
C	12.8	13.0	13.2	0.504	0.512	0.519
D	20.2			0.795		
N	60			2.362		
T			22.4			0.882
Ao	6.80	6.90	7.00	0.268	0.272	0.2.76
Bo	10.40	10.50	10.60	0.409	0.413	0.417
Ko	2.55	2.65	2.75	0.100	0.104	0.105
Po	3.9	4.0	4.1	0.153	0.157	0.161
P	7.9	8.0	8.1	0.311	0.315	0.319



Note: Drawing not in scale

8 Revision history

Table 10. Document revision history

Date	Revision	Changes
17-Jun-2004	5	Add figures 20 and 21, PPAK, TO-220 and TO-220FP mechanical data updated.
19-Jul-2004	6	Remove Package TO-220FP4.
08-Nov-2004	7	Mistake Figure 7.
21-Mar-2005	8	Add V_O and V_{INH} on Table 2.
21-Oct-2005	9	Order Codes Has Been Updated.
17-Oct-2006	10	Add new package P^2PAK.
13-Nov-2006	11	Add row T_{SHDN} on tables of the electrical characteristics.
11-May-2007	12	Order codes updated.
15-Feb-2008	13	Added: *Table 1 on page 1*.

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