



Mikroe NXP Cup Mainboard

RDDRONE-CUPK64

Mikroe NXP Cup mainboard V 20 is based on the NXP Semiconductors FRDM-K64 board with an Arm® Cortex®-M4. The board uses the Kinetis® K series MCU family built on the Arm Cortex-M4 processor and is intended for the NXP CUP challenge. The processing unit connects with sensing systems (e.g. camera) and actuators (e.g. servo or brushed motors). The mainboard has been designed by Haute École Arc ingénierie, Switzerland.

On-Board Devices

Processors and Microcontrollers

K64_120 : Kinetis® K64-120 MHz, 256 KB SRAM Microcontrollers (MCUs) Based on Arm® Cortex®-M4 Core

Features

General Features

- Arm Cortex-M4
- Kinetis K6x MCU

Applications

- Mobile Robotics Ecosystem