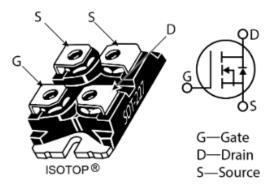


MSC025SMA120J Silicon Carbide N-Channel Power MOSFET

1 Product Overview

This section shows the product overview for the MSC025SMA120J device.



1.1 Features

The following are key features of the MSC025SMA120J device:

- Low capacitances and low gate charge
- Fast switching speed due to low internal gate resistance (ESR)
- Stable operation at high junction temperature, T_{J(max)} = 175 °C
- Fast and reliable body diode
- Superior avalanche ruggedness
- RoHS compliant
- Isolated voltage to 2500 V

1.2 Benefits

The following are benefits of the MSC025SMA120J device:

- High efficiency to enable lighter, more compact system
- Simple to drive and easy to parallel
- Improved thermal capabilities and lower switching losses
- Eliminates the need for external freewheeling diode
- Lower system cost of ownership

1.3 Applications

The MSC025SMA120J device is designed for the following applications:

- PV inverter, converter, and industrial motor drives
- Smart grid transmission and distribution
- Induction heating and welding
- H/EV powertrain and EV charger
- Power supply and distribution



2 Device Specifications

This section shows the specifications for the MSC025SMA120J device.

2.1 Absolute Maximum Ratings

The following table shows the absolute maximum ratings for the MSC025SMA120J device.

Table 1 • Absolute Maximum Ratings

Symbol	Parameter	Ratings	Unit	
VDSS	Drain source voltage	1200	V	
lo	Continuous drain current at Tc = 25 °C	77		
	Continuous drain current at Tc = 100 °C	54	_	
Ірм	Pulsed drain current ¹	275	_	
V _G S	Gate-source voltage	25 to -10	V	
PD	Total power dissipation at Tc = 25 °C	278	W	
	Linear derating factor	1.67	W/°C	

Note:

1. Repetitive rating: pulse width and case temperature limited by maximum junction temperature.

The following table shows the thermal and mechanical characteristics for the MSC025SMA120J device.

Table 2 • Thermal and Mechanical Characteristics

Symbol	Characteristic	Min	Тур	Max	Unit
Reлc	Junction-to-case thermal resistance		0.36	0.54	°C/W
Tı	Operating junction temperature	-55		175	°C
Тѕтб	Storage temperature	-55		150	_
Visolation	RMS voltage (50 Hz–60 Hz sinusoidal waveform from terminals to mounting base for 1 minute)	2500			V
	Mounting torque, M4 screw			10	lbf-in
				1.1	N-m
Wt	Package weight		1.03		OZ
			29.2		g



2.2 Electrical Performance

The following table shows the static characteristics for the MSC025SMA120J device. $T_J = 25$ °C unless otherwise specified.

Table 3 • Static Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
V(BR)DSS	Drain-source breakdown voltage	V_{GS} = 0 V, I_D = 100 μA	1200			V
R _{DS(on)}	Drain-source on resistance 1	V _{GS} = 20 V, I _D = 40 A		25	31	mΩ
V _{GS(th)}	Gate-source threshold voltage	V _{GS} = V _{DS} , I _D = 1 mA	1.8	2.8		V
$\Delta V_{GS(th)}/\Delta T_J$	Threshold voltage coefficient	V _{GS} = V _{DS} , I _D = 1 mA		-3.5		mV/°C
Ibss	Zero gate voltage drain current	V _{DS} = 1200 V, V _{GS} = 0 V			100	μΑ
		V _{DS} = 1200 V, V _{GS} = 0 V T _J = 125 °C			500	_
Igss	Gate-source leakage current	V _{GS} = 20 V/–10 V			±100	nA

Note:

1. Pulse test: pulse width $< 380 \mu s$, duty cycle < 2%.

The following table shows the dynamic characteristics for the MSC025SMA120J device. $T_J = 25$ °C unless otherwise specified.

Table 4 • Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit	
Ciss	Input capacitance	V _{GS} = 0 V		3020		pF	
Crss	Reverse transfer	- V _{DD} = 1000 V V _{AC} = 25 mV		25		=	
	capacitance	_ f = 1 MHz				_	
Coss	Output capacitance	,		270			
Qg	Total gate charge	V _{GS} = -5 V/20 V		232		nC	
Qgs	Gate-source charge	V_{DD} = 800 VI_D = 40 A		41 50		-	
Q _{gd}	Gate-drain charge	_ ID = 40 A					
td(on)	Turn-on delay time	$V_{DD} = 800 \text{ V}$ $V_{GS} = 0 \text{ V}/20 \text{ V}$ $I_D = 40 \text{ A}$ $R_{G(ext)} = 3.3 \Omega^1$ Freewheeling diode = MSC020SDA120B		14		ns	
tr	Current rise time		11		_		
t _{d(off)}	Turn-off delay time			69		-	
t f	Current fall time			33		_	
E _{on2}	Turn-on switching energy ²			1040		μЈ	
Eoff	Turn-off switching energy	-		670		_	
t _{d(on)}	Turn-on delay time	V _{DD} = 800 V		12		ns	
tr	Current rise time	$- V_{GS} = 0 V/20 V$ $- I_{D} = 40 A$ $R_{G(ext)} = 3.3 \Omega^{1}$ $- T_{C} = 150 ^{\circ}C$ $- Freewheeling diode =$ $- MSC020SDA120B$		11		_	
td(off)	Turn-off delay time			69		_	
t f	Current fall time			33		_	
E _{on2}	Turn-on switching energy ²			975		μJ	
Eoff	Turn-off switching energy			950		=	
ESR	Equivalent series resistance	f = 1 MHz, 25 mV, drain short		0.88		Ω	
SCWT	Short circuit withstand time	V _{DS} = 960 V, V _{GS} = 20 V		3		μs	



Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
Eas	Avalanche energy, single pulse	V _{DS} = 150 V, V _{GS} = 20 V, I _D = 40 A		3500		mJ

Notes:

- 1. R_G is the total effective external gate resistance.
- 2. E_{on2} includes energy of MSC020SDA120B freewheeling diode.

The following table shows the body diode characteristics for the MSC025SMA120J device. T_J = 25 °C unless otherwise specified.

Table 5 • Body Diode Characteristics

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
V _{SD}	Diode forward voltage	I _{SD} = 40 A, V _{GS} = 0 V		4.0		V
		I _{SD} = 40 A, V _{GS} = -5 V		4.2		V
trr	Reverse recovery time	$I_{SD} = 40 \text{ A, } V_{GS} = -5 \text{ V}$ $V_{DD} = 800 \text{ V}$ $dI/dt = -1000 \text{ A/}\mu\text{s}$		90		ns
Qrr	Reverse recovery charge			550		nC
IRRM	Reverse recovery current			13.5		Α

2.3 Typical Performance Curves

This section shows the typical performance curves for the MSC025SMA120J device.

Figure 1 • Drain Current vs. Drain-to-Source Voltage

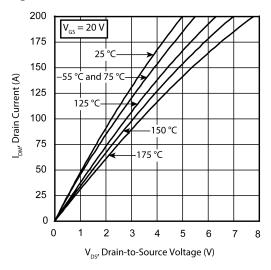


Figure 2 • Drain Current vs. Drain-to-Source Voltage

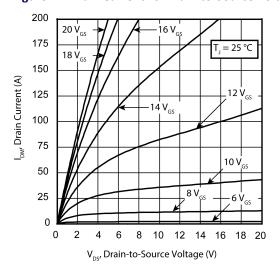




Figure 3 • Drain Current vs. Drain-to-Source Voltage

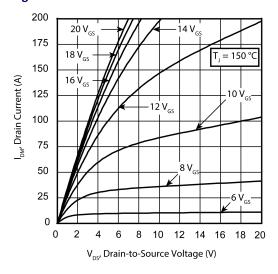


Figure 5 • RDS(on) vs. Junction Temperature

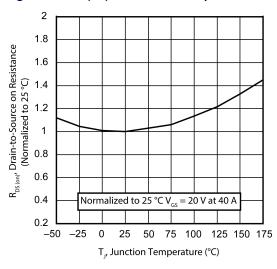


Figure 7 • Capacitance vs. Drain-to-Source Voltage

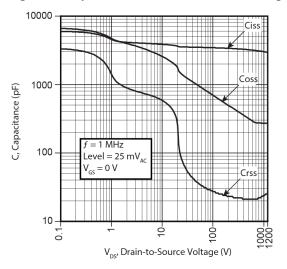


Figure 4 • Drain Current vs. Drain-to-Source Voltage

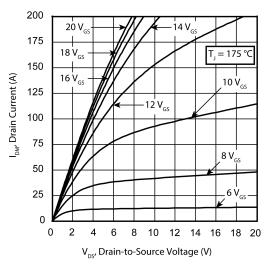


Figure 6 • Gate Charge Characteristics

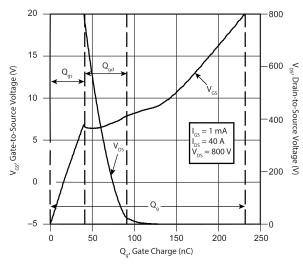


Figure 8 • IDM vs. Gate-to-Source Voltage

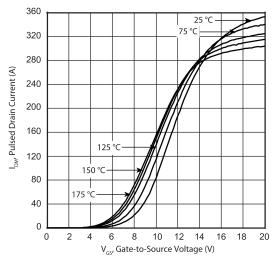




Figure 9 • IDM vs. VDS Third Quadrant Conduction

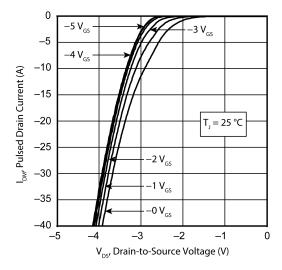


Figure 11 • VGS(th) vs. Junction Temperature

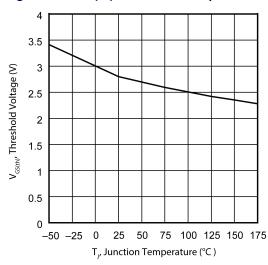


Figure 10 • IDM vs. VDS Third Quadrant Conduction

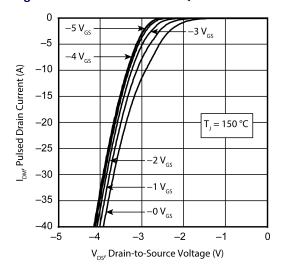


Figure 12 • Forward Safe Operating Area

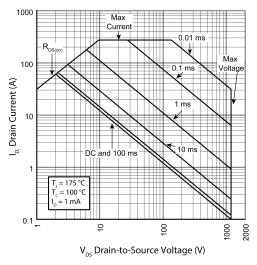
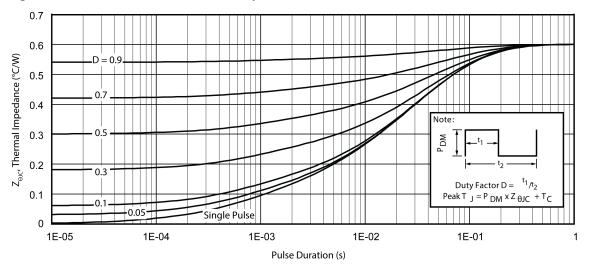


Figure 13 • Maximum Transient Thermal Impedance





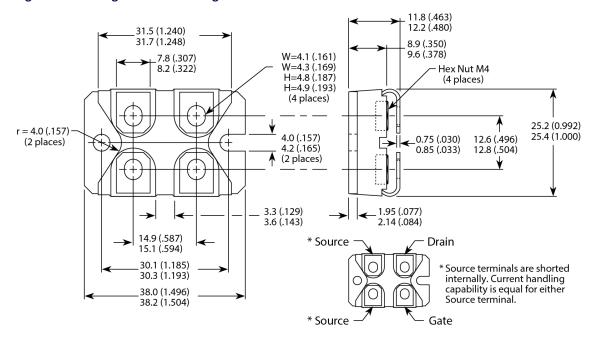
3 Package Specification

This section shows the package specification for the MSC025SMA120J device.

3.1 Package Outline Drawing

This section shows the SOT-227 package drawing for the MSC025SMA120J device. The dimensions in the figure below are in millimeters and (inches).

Figure 14 • Package Outline Drawing







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050-7741 | April 2019 | Preliminary