

Vishay High Power Products

Ultrafast Rectifier, 2 x 3 A FRED PtTM



Anode

Common cathode

Anode

PRODUCT SUMMARY				
t _{rr}	25 ns			
I _{F(AV)}	2 x 3 A			
V_{R}	200 V			

FEATURES

- · Ultrafast recovery time
- · Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- Lead (Pb)-free ("PbF" suffix)
- · Designed and qualified for Q101 level



Vishay HPP's 200 V series are the state of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Peak repetitive reverse voltage	V_{RRM}		200	V	
Average rectified forward current per device	I _{F(AV)}	Total device, rated V _R , T _C = 159 °C	6		
Non-repetitive peak surge current	I _{FSM}		50	Α	
Peak repetitive forward current per diode	I _{FM}	Rated V_R , square wave, 20 kHz, T_C = 159 °C	6		
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	200	-	-		
Forward voltage		I _F = 3 A	-	-	1	V	
	V	I _F = 3 A, T _J = 125 °C	-	-	0.9		
	V _F	I _F = 6 A	-	-	1.2		
		I _F = 6 A, T _J = 125 °C	-	-	1.08		
Reverse leakage current I _R	V _R = V _R rated	-	-	5	4		
	I _R	T _J = 125 °C, V _R = V _R rated	-	-	100	μΑ	
Junction capacitance	C _T	V _R = 200 V	-	12	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body -		8.0	-	nH	

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6CWH02FNPbF

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CON	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	-	35	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	19	-	ns
		T _J = 125 °C		-	26	-	
Peak recovery current		T _J = 25 °C	$I_F = 3 \text{ A}$ $V_R = 160 \text{ V}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$	-	3.1	-	A
	I _{RRM}	T _J = 125 °C		-	4.6	-	
Reverse recovery charge		T _J = 25 °C		-	30	-	0
	Q _{rr}	T _J = 125 °C		-	60	-	nC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}	- 65	-	175	°C	
Thermal resistance, junction to case per leg	R _{thJC}	-	-	5		
Thermal resistance, junction to ambient per leg	R _{thJA}	-	-	80	°C/W	
Thermal resistance, case to heatsink	R _{thCS}	-	-	-		
Weight		-	0.3	-	g	
		-	0.01	-	OZ.	
Mounting torque		6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)	
Marking device		Case style D-PAK		6CWH	l02FN	

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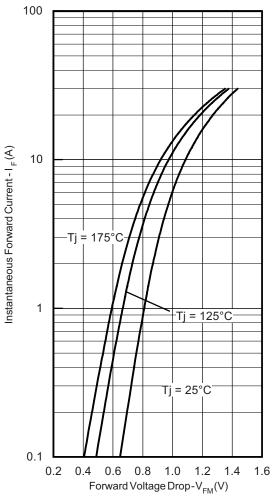


Fig. 1 - Maximum Forward Voltage Drop Characteristics

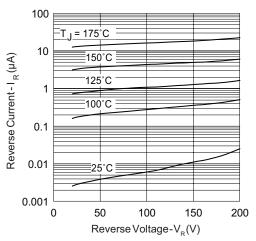


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

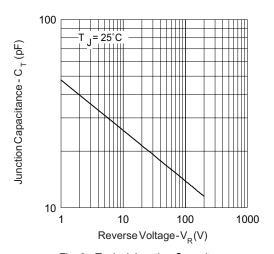


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

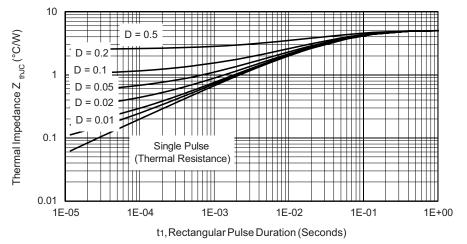


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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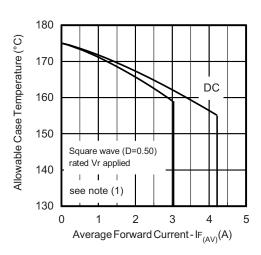


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

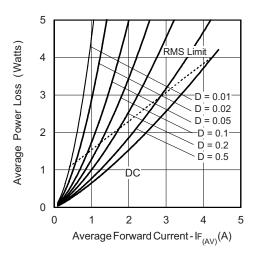


Fig. 6 - Forward Power Loss Characteristics

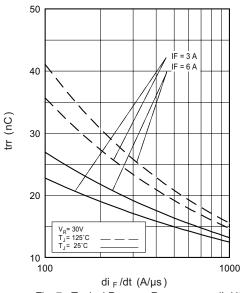


Fig. 7 - Typical Reverse Recovery vs. dI_F/dt

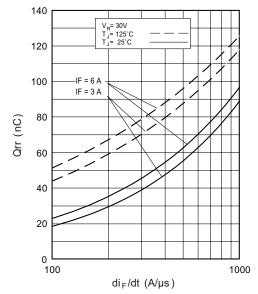


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{l} \text{(1)} \ \ \text{Formula used:} \ T_C = T_J - (Pd + Pd_{REV}) \ x \ R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \ x \ V_{FM} \ \text{at} \ (I_{F(AV)}/D) \ (\text{see fig. 6}); \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \ x \ I_R \ (1 - D); \ I_R \ \text{at} \ V_{R1} = \text{Rated} \ V_R \\ \end{array}$



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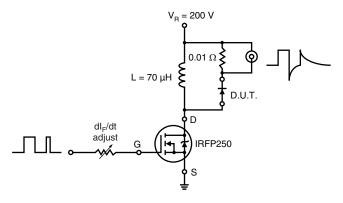
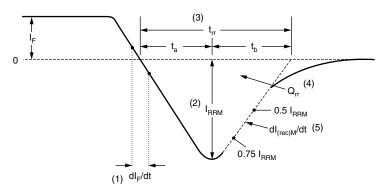


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dI_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

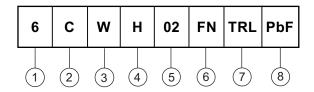
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ORDERING INFORMATION TABLE

Device code



- 1 Current rating (6 = 6 A)
- C = Center tap configuration
- Package identifier:

W = D-PAK

- 4 H = Hyperfast recovery
- 5 Voltage rating (02 = 200 V)
- 6 FN = TO-252AA
- 7 • None = Tube (50 pieces)
 - TRL = Tape and reel (left oriented)
 - TRR = Tape and reel (right oriented)
- None = Standard production
 - PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95016				
Part marking information	http://www.vishay.com/doc?95059			
Packaging information	http://www.vishay.com/doc?95033			

For technical questions, contact: diodes-tech@vishay.com

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