V20PL63

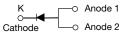
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Vishay General Semiconductor

High Current Density Surface-Mount TMBS[®] (Trench MOS Barrier Schottky) Rectifier

Ultra Low $V_F = 0.31$ V at $I_F = 5$ A

eSMP[®] Series K SMPC (TO-277A)



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	20 A			
V _{RRM}	60 V			
I _{FSM}	240 A			
V_{F} at I_{F} = 20 A (T_{J} = 125 °C)	0.53 V			
T _J max.	150 °C			
Package	SMPC (TO-277A)			
Circuit configuration	Single			

FEATURES

- Very low profile typical height of 1.1 mm
- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
 Automotive ordering code; base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in low voltage high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V20PL63	UNIT	
Device marking code		20L63		
Maximum repetitive peak reverse voltage	V _{RRM}	60	V	
Maximum average forward rectified current (fig. 1)	I _{F(AV)} ⁽¹⁾	20		
Maximum average forward rectilied current (lig. 1)	I _{F(AV)} ⁽²⁾	5	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	240	А	
Operating junction temperature range	T _J ⁽³⁾	T _J ⁽³⁾ -40 to +150		
Storage temperature range	T _{STG}	-55 to +150	°C	

Notes

⁽¹⁾ Mounted on 30 mm x 30 mm pad areas aluminum PCB

⁽²⁾ Free air, mounted on recommended copper pad area

 $^{(3)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: dP_D/dT_J <1/ R_{0JA}

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RoHS

COMPLIANT

HALOGEN

FREE

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V20PL63

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ELECTRICAL CHARACTERISTICS ($T_J = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage	I _F = 5.0 A	T _J = 25 °C	– V _F ⁽¹⁾	0.42	-	V	
	I _F = 10 A			0.48	-		
	I _F = 20 A			0.57	0.64		
	I _F = 5.0 A	T _J = 125 °C		0.31	-		
	I _F = 10 A			0.40	-		
	I _F = 20 A			0.53	0.61		
Reverse current	V - 60 V	T _J = 25 °C T _J = 125 °C	T _J = 25 °C	I _R ⁽²⁾	-	0.6	m۸
	V _R = 60 V		'R (=)	17	45	mA	
Typical junction capacitance	4.0 V, 1 MHz	4.0 V, 1 MHz		3400	-	pF	

Notes

 $\stackrel{(1)}{\longrightarrow}$ Pulse test: 300 μs pulse width, 1 % duty cycle

 $^{(2)}$ Pulse test: pulse width $\leq 5~ms$

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V20PL63	UNIT	
Turning the world register as	R _{0JA} (1)(2)	75	°C/W	
Typical thermal resistance	R _{0JM} ⁽³⁾	4		

Notes

⁽¹⁾ The heat generated must be less than the thermal conductivity from junction to ambient: $dP_D/dT_J < 1/R_{\theta JA}$ ⁽²⁾ Free air mounted on recommended copper pad area; thermal resistance $R_{\theta JA}$ - junction to ambient

 $^{(3)}$ Mounted on 30 mm x 30 mm aluminum PCB; thermal resistance $R_{\theta JM}$ - junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V20PL63-M3/H	0.10	н	1500	7" diameter plastic tape and reel	
V20PL63-M3/I	0.10	I	6500	13" diameter plastic tape and reel	
V20PL63HM3/H (1)	0.10	Н	1500	7" diameter plastic tape and reel	
V20PL63HM3/I ⁽¹⁾	0.10		6500	13" diameter plastic tape and reel	

Note

(1) AEC-Q101 qualified



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RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)

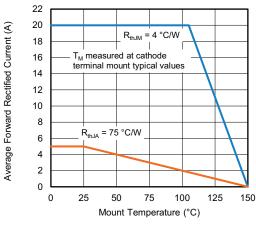


Fig. 1 - Forward Current Derating Curve

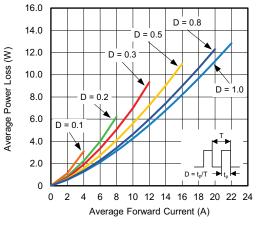


Fig. 2 - Forward Power Loss Characteristics

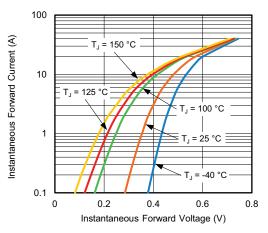


Fig. 3 - Typical Instantaneous Forward Characteristics

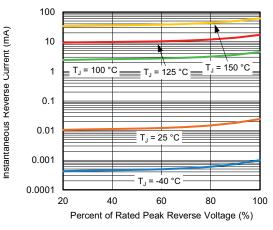


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode

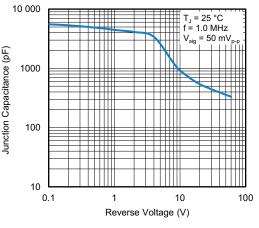


Fig. 5 - Typical Junction Capacitance

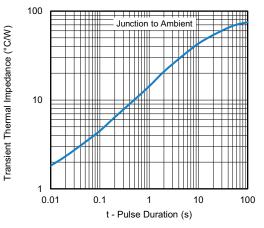


Fig. 6 - Typical Transient Thermal Impedance

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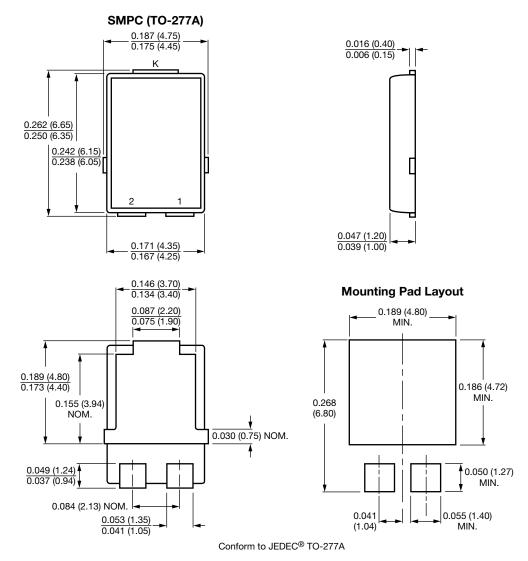
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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