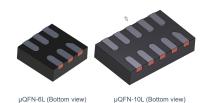


Datasheet

Common mode filter with ESD protection for high speed serial interface



Product status			
Part number	Package		
ECMF2-40A100N6	μQFN-6L		
FCMF4-40A100N10	uQFN-10I		

Features

- 10.7 GHz differential bandwidth to comply with HDMI 2.1, HDMI 2.0, HDMI 1.4, USB4, USB 3.2 Gen2, USB 2.0, MIPI, Display port 2.0, etc.
- · High common mode attenuation on WLAN frequencies:
 - -15 dB at 2.4 GHz
 - -21 dB at 5.0 GHz
 - -17 dB at 6.0 GHz
- Low serial resistance: 3.0 Ω
- Very low PCB space consumption
- Thin package: 0.5 mm max.
- High reduction of parasitic elements through integration
- · Lead free and RoHS package
- Exceeds IEC 61000-4-2 level 4 standard:
 - Contact discharge:
 - ±9 kV (contact discharge, ECMF2-40A100N6)
 - ±10 kV (contact discharge, ECMF4-40A100N10)
 - Air discharge:
 - ±20 kV (air discharge, ECMF2-40A100N6)
 - ±25 kV (air discharge, ECMF4-40A100N10)

Applications

- Notebook, laptop
- Streaming box, HDMI stick
- · Game console, Set top box
- Tablet
- · Portable devices

Description

The ECMF2-40A100N6 and ECMF4-40A100N10 are highly integrated common mode filters designed to suppress EMI/RFI common mode noise on high speed differential serial buses like HDMI 2.1, HDMI 2.0, HDMI1.4, USB4, USB 3.2 Gen 2, USB 2.0, ethernet, MIPI, Display Port and other high speed serial interfaces.

They have a very large differential bandwidth to comply with these standards and can also protect and filter one or two differential lanes.



1 Characteristics

Table 1. Absolute maximum ratings (T_{amb} = 25 °C)

Symbol		Parameter	Value	Unit
		IEC 61000-4-2 contact discharge:		
		ECMF2-40A100N6	±9	kV
V	De els modes controls	ECMF4-40A100N10	±10	
VPP	V _{PP} Peak pulse voltage	IEC 61000-4-2 air discharge:		
		ECMF2-40A100N6	±20	kV
		ECMF4-40A100N10	±25	
I _{RMS}	Maximum RMS current	Maximum RMS current		mA
T _{op}	Maximum operating temperature	Maximum operating temperature range		
T _{stg}	Storage temperature range		-55 to +150	°C
T _L	Maximum temperature for solder	Maximum temperature for soldering during 10 s		

Figure 1. Electrical characteristics (definitions)

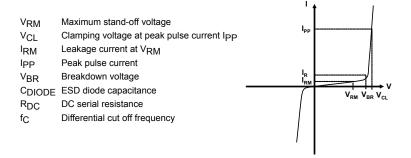


Table 2. Electrical characteristics (T_{amb} = 25 °C)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V_{BR}	Breakdown voltage	I _R = 1 mA	5.3	5.8		V
I		V _{RM} = 3.6 V per line		< 1	50	0
I _{RM}	Leakage current	V _{RM} = 5 V per line		3	70	nA
V_{RM}	Reverse working voltage				5	V
R _{DC}	DC serial resistance, I _{DC} = 20 m	nA		3.0		Ω
f _c	Differential mode cut-off frequency ⁽¹⁾			10.7		GHz
V_{CL}		TLP measurement (pulse duration 100 ns), 16 A I _{PP}		20.5		V
V CL	Reverse clamping voltage	8 kV contact discharge after 30 ns, IEC 61000-4-2		18		V
C _{DIODE}	Capacitance	V_{BIAS} = 0 V, F = 2.5 GHz to 9 GHz, V_{OSC} = 30 mV		0.25	0.40	pF
R_D	Dynamic resistance, TLP measurement (pulse duration 100 ns)			0.8		Ω

^{1.} Attenuation at 10 MHz as reference.

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Table 3. ECMF2-40A100N6 pin discription

Pin number	Description	Pin number	Description
1	D+ to connector	4	GND
2	D- to connector	5	D- to IC
3	GND	6	D+ to IC

Table 4. ECMF4-40A100N10 pin description

Pin number	Description	Pin number	Description
1	D1+ to connector	6	D2- to IC
2	D1- to connector	7	D2+ to IC
3	GND	8	GND
4	D2+ to connector	9	D1- to IC
5	D2- to connector	10	D1+ to IC

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1.1 Characteristics (curves)

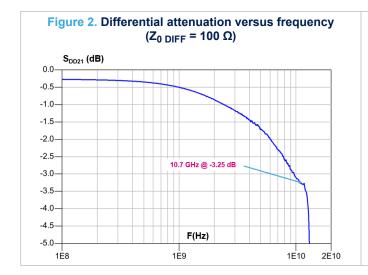


Figure 3. Common mode attenuation versus frequency $(Z_{0 \text{ COM}} = 50 \Omega)$ S_{CC21}(dB) -5 -10--15--20--25--30--35--40--45--50-F(Hz) -55-1Ė8 1Ė9 1E10 2E10

Figure 4. HDMI2.1 12 Gbps eye diagram without ECMFx-40A100Nx (with worst cable model (WCM3), EQ with 8 dB CTLE and One-tap DFE)

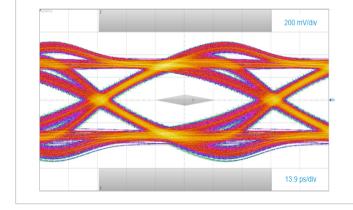


Figure 5. HDMI2.1 12 Gbps eye diagram with ECMFx-40A100Nx (with worst cable model (WCM3), EQ with 8 dB CTLE and One-tap DFE)

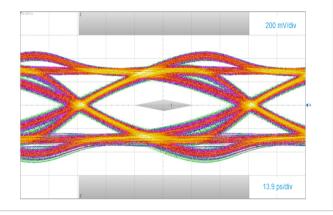


Figure 6. HDMI2.0 5.94 Gbps eye diagram without ECMFx-40A100Nx (with worst cable model and equalizer)

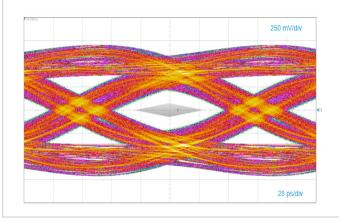
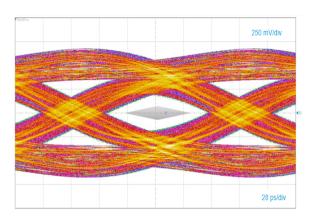


Figure 7. HDMI2.0 5.94 Gbps eye diagram with ECMFx-40A100Nx (with worst cable model and equalizer)



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Figure 8. USB4 20.0 Gbps eye diagram without ECMFx-40A100Nx (with Preset 0, reference cable 0.8m, equalizer with ADC = 0dB and DFE)

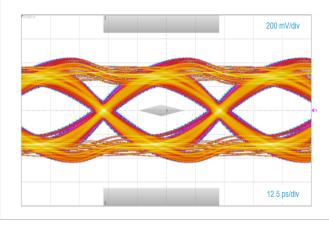


Figure 9. USB4 20.0 Gbps eye diagram with ECMFx-40A100Nx (with Preset 0, reference cable 0.8m, equalizer with ADC = 0dB and DFE)

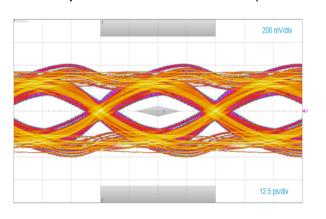


Figure 10. USB3.2 Gen 2 10.0 Gbps eye diagram without ECMFx-40A100Nx (with type C connector, reference cable, equalizer with ADC = 5 dB and DFE)

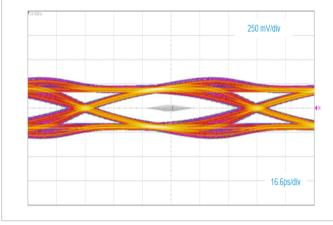


Figure 11. USB3.2 Gen 2 10.0 Gbps eye diagram with ECMFx-40A100Nx (with type C connector, reference cable, equalizer with ADC = 5 dB and DFE)

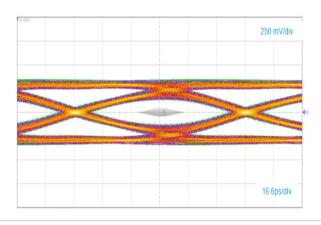


Figure 12. USB3.2 Gen 1 5.0 Gbps eye diagram without ECMFx-40A100Nx (with type C connector, reference cable and equalizer)

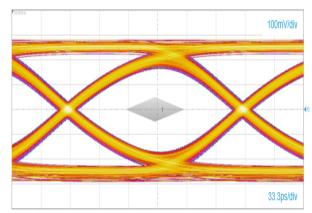
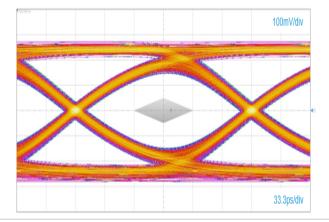


Figure 13. USB3.2 Gen 1 5.0 Gbps eye diagram with ECMFx-40A100Nx (with type C connector, reference cable and equalizer)



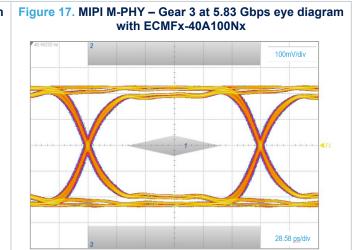
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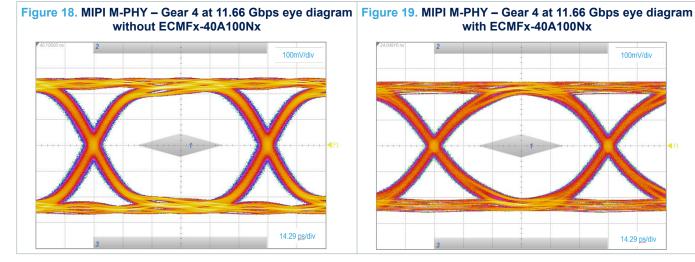


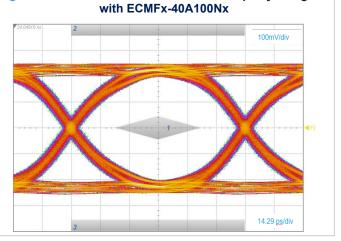
Figure 14. USB 2.0 High Speed 480 Mbps eye diagram, Template 1, without ECMFx-40A100Nx 200 mV/div 347.25 ps/div

Figure 15. USB 2.0 High Speed 480 Mbps eye diagram, Template 1, with ECMFx-40A100Nx 200 mV/div 347.25 ps/div

Figure 16. MIPI M-PHY – Gear 3 at 5.83 Gbps eye diagram without ECMFx-40A100Nx 100mV/div 28.58 ps/div







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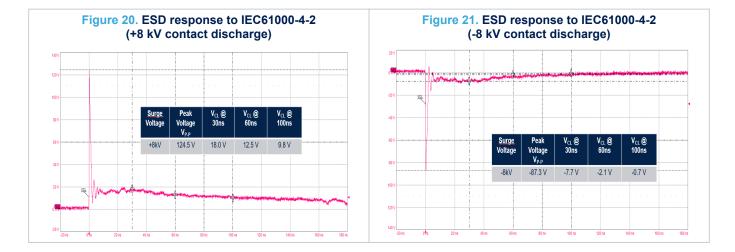
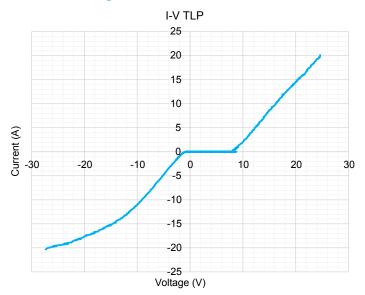


Figure 22. TLP characteristic



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Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

2.1 μQFN6L package information

Top view

Bottom view

PIN #1 ID

Side view

Side view

Figure 23. µQFN6L package outline

Table 5. µQFN6L package mechanical data

		Dimensions			
Ref.	Millimeters				
	Min.	Тур.	Max.		
A	0.41	0.45	0.50		
A1	0.00	0.02	0.05		
A3		0.127			
b	0.15	0.20	0.25		
D	1.35	1.40	1.45		
E	1.30	1.35	1.40		
е		0.40			
L	0.40	0.50	0.60		
N		6			

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2.2 μQFN10L package information

Figure 24. µQFN10L package outline

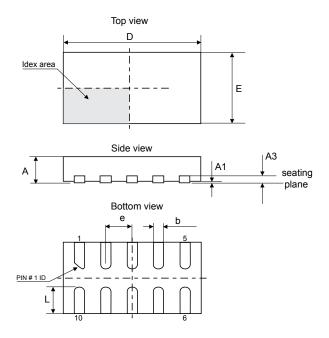


Table 6. µQFN10L package mechanical data

	Dimensions				
Ref.	Millimeters				
	Min.	Тур.	Max.		
Α	0.41	0.45	0.50		
A1	0.00	0.02	0.05		
A3		0.127			
b	0.15	0.20	0.25		
D	2.15	2.20	2.25		
E	1.30	1.35	1.40		
е		0.40			
L	0.40	0.50	0.60		

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2.3 Packing information

Figure 25. ECMF2-40A100N6 marking

Figure 26. ECMF4-40A100N10 marking

MK

MK

Note: The marking codes can be rotated by 90 ° or 180° to differentiate assembly location. In no case should this product marking be used to orient the component for its placement on a PCB. Only pin 1 mark is to be used for this purpose.

Pin 1 located according to EIA-481 Ø D0 P0 E1 ø во P1 P2 Ø D1 A0 ◆→ User direction of unreeling Pocket dimensions are not on scale Note: Pocket shape may vary depending on package

Figure 27. Tape and reel outline

Table 7. Tape and reel mechanical data

Pof	Dimensions (millimeters)			
Ref.	Min.	Тур.	Max.	
P1	3.90	4.00	4.10	
P0	3.90	4.00	4.10	
Ø D0	1.40	1.50	1.60	
Ø D1 (ECMF2-40A100N6)	0.45	0.50	0.55	
Ø D1 (ECMF4-40A100N10)	0.8			
F	3.45	3.50	3.55	
E1	1.65	1.75	1.85	
K0 (ECMF2-40A100N6)	0.70	0.75	0.80	
K0 (ECMF4-40A100N10)	0.60	0.65	0.70	
P2	1.95	2.00	2.05	
W	7.90	8.00	8.10	
A0 (ECMF2-40A100N6)	1.43	1.48	1.53	
A0 (ECMF4-40A100N10)	1.50	1.55	1.60	
B0 (ECMF2-40A100N6)	1.75	1.80	1.85	

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Packing information

Ref.	١	Dimensions (millimeters)
Ker.	Min.	Тур.	Max.
B0 (ECMF4-40A100N10)	2.35	2.40	2.45

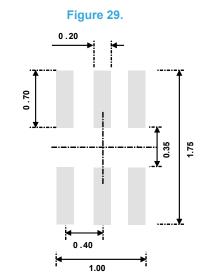
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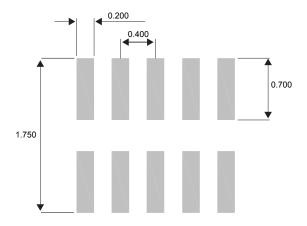
3 Recommendation on PCB assembly

3.1 Footprint

Figure 28. ECMF2-40A100N6 footprint in mm



ECMF4-40A100N10 footprint in mm



SMD footprint design is recommended.

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3.2 Stencil opening design

Recommended design reference: stencil opening thickness: 100 μm

Figure 30. ECMF2-40A100N6 stencil opening recommendations

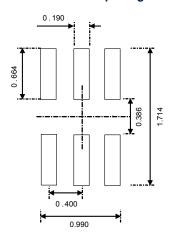
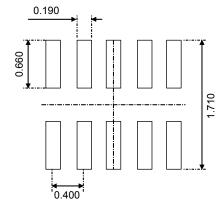


Figure 31. ECMF4-40A100N10 stencil opening recommendations



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3.3 Solder paste

- 1. Halide-free flux qualification ROL0 according to ANSI/J-STD-004.
- 2. "No clean" solder paste is recommended.
- 3. Offers a high tack force to resist component movement during PCB movement.
- 4. Solder paste with fine particles: powder particle size is 20-38 μm.

3.4 Placement

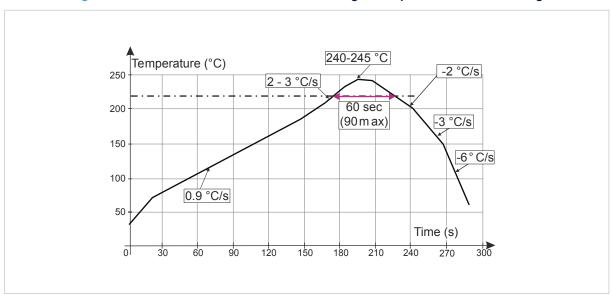
- 1. Manual positioning is not recommended.
- 2. It is recommended to use the lead recognition capabilities of the placement system, not the outline centering
- 3. Standard tolerance of ±0.05 mm is recommended.
- 4. 3.5 N placement force is recommended. Too much placement force can lead to squeezed out solder paste and cause solder joints to short. Too low placement force can lead to insufficient contact between package and solder paste that could cause open solder joints or badly centered packages.
- To improve the package placement accuracy, a bottom side optical control should be performed with a high resolution tool.
- For assembly, a perfect supporting of the PCB (all the more on flexible PCB) is recommended during solder paste printing, pick and place and reflow soldering by using optimized tools.

3.5 PCB design preference

- 1. To control the solder paste amount, the closed via is recommended instead of open vias.
- 2. The position of tracks and open vias in the solder area should be well balanced. A symmetrical layout is recommended, to avoid any tilt phenomena caused by asymmetrical solder paste due to solder flow away.

3.6 Reflow profile

Figure 32. ST ECOPACK® recommended soldering reflow profile for PCB mounting



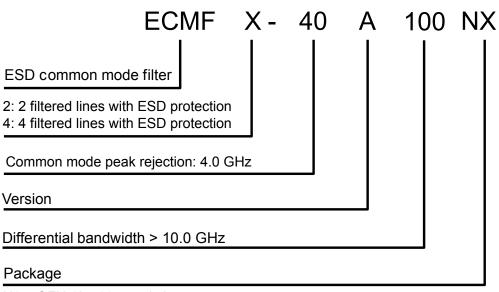
Note: Minimize air convection currents in the reflow oven to avoid component movement. Maximum soldering profile corresponds to the latest IPC/JEDEC J-STD-020.

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4 Ordering information

Figure 33. Ordering information scheme



N6: μ QFN-6L 400 μ m pitch N10: μ QFN-10L 400 μ m pitch

Table 8. Ordering information

Order code	Marking ⁽¹⁾	Package	Weight	Base qty.	Delivery mode
ECMF2-40A100N6	ML	μQFN-6L	2.4 mg	3000	Tape and reel
ECMF4-40A100N10	MK	μQFN-10L	3.9 mg	3000	Tape and reel

1. The marking can be rotated by 90° to differentiate assembly location

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Revision history

Table 9. Document revision history

Date	Revision	Changes	
14-Feb-2022	1	Initial release.	
19-May-2022	2	Added from Figure 15 to Figure 20.	

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