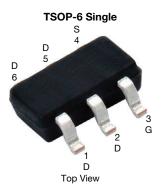


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Vishay Siliconix

# Automotive N-Channel 60 V (D-S) 175 °C MOSFET



Marking Code: 9Hxxx

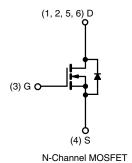
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	60			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.042			
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = 4.5 \text{ V}$	0.063			
I <sub>D</sub> (A)	7			
Configuration	Single			

#### **FEATURES**

- TrenchFET® power MOSFET
- AEC-Q101 qualified
- 100 % R<sub>q</sub> and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



ROHS COMPLIANT HALOGEN FREE



ORDERING INFORMATION	
Package	TSOP-6
Lead (Pb)-free and halogen-free	SQ3426CEV (for detailed order number please see <a href="https://www.vishay.com/doc?79771">www.vishay.com/doc?79771</a> )

PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V <sub>DS</sub>	60	V
Gate-source voltage		$V_{GS}$	± 20	V
Continuous drain current	T <sub>C</sub> = 25 °C	- I <sub>D</sub>	7	
	T <sub>C</sub> = 125 °C		4	
Continuous source current (diode conduction)		I <sub>S</sub>	4.6	Α
Pulsed drain current <sup>a</sup>		I <sub>DM</sub>	29	
Single pulse avalanche current	1 0.1 ml l	I <sub>AS</sub>	10	
Single pulse avalanche energy	L = 0.1 mH	E <sub>AS</sub>	5	mJ
Maximum power dissipation	T <sub>C</sub> = 25 °C	T <sub>C</sub> = 25 °C		W
	T <sub>C</sub> = 125 °C	$P_{D}$	1.6	V V
Operating junction and storage temperatu	re range	T <sub>J</sub> , T <sub>stq</sub>	-55 to +175	°C

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction to ambient	PCB mount b	$R_{thJA}$	110	°C/W	
Junction to foot (drain)		$R_{thJF}$	30		

#### Notes

- a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %
- b. When mounted on 1" square PCB (FR4 material)



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-source breakdown voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0, I <sub>D</sub> = 250 μA 60 -		-	-	V	
Gate-source threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$		1.5	2	2.5	V
Coto course legicare		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$		-	-	± 100	nA
Gate-source leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 300	
Zero gate voltage drain current	I <sub>DSS</sub>	$V_{GS} = 0 V$	V <sub>DS</sub> = 60 V	-	-	1	μΑ
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 60 V, T <sub>J</sub> = 125 °C	-	-	50	
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 60 V, T <sub>J</sub> = 175 °C	-	-	150	
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V	$V_{DS} \ge 5 V$	10	-	-	Α
Drain-source on-state resistance <sup>a</sup>	, ,	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 5 A	-	0.034	0.042	
	В	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 5 A, T <sub>J</sub> = 125 °C	-	-	0.073	0
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 5 A, T <sub>J</sub> = 175 °C	-	-	0.092	Ω
		V <sub>GS</sub> = 4.5 V	I <sub>D</sub> = 4 A	-	0.037	0.063	
Forward transconductance a	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 4 A		-	21	-	S
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 30 V, f = 1 MHz	-	862	1300	pF
Output Capacitance	C <sub>oss</sub>			-	75	110	
Reverse Transfer Capacitance	C <sub>rss</sub>			-	29	70	
Total Gate Charge <sup>c</sup>	Qg		V <sub>DS</sub> = 30 V, I <sub>D</sub> = 4 A	-	6.8	12	nC
Gate-Source Charge c	Q <sub>gs</sub>	$V_{GS} = 4.5 \text{ V}$		-	2.9	-	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			-	2.0	-	
Gate Resistance	$R_g$	f = 1 MHz		1.55	3.1	5.7	Ω
Turn-On Delay Time c	t <sub>d(on)</sub>	$V_{DD} = 30 \text{ V, } R_L = 7.5 \Omega$ $I_D \cong 4 \text{ A, } V_{GEN} = 10 \text{ V, } R_g = 1 \Omega$		-	9	14	ns ns
Rise Time <sup>c</sup>	t <sub>r</sub>			-	3	10	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	19	29	
Fall Time <sup>c</sup>	t <sub>f</sub>			-	4	10	
Source-Drain Diode Ratings and Charact	eristics <sup>b</sup>						
Pulsed current <sup>a</sup>	I <sub>SM</sub>			-	-	29	Α
Forward voltage	$V_{SD}$	I <sub>F</sub> = 1.6 A, V <sub>GS</sub> = 0 V		-	0.77	1.2	V
Body diode reverse recovery time	t <sub>rr</sub>			ı	19	38	ns
Body diode reverse recovery charge	Q <sub>rr</sub>	I <sub>F</sub> = 4 A, di/dt = 100 A/μs		-	18	36	nC
Reverse recovery fall time	t <sub>a</sub>			-	15	-	ns
Reverse recovery rise time	t <sub>b</sub>			-	4	-	
Body diode peak reverse recovery current	I <sub>RM(REC)</sub>			-	-2.1	-	Α

### Notes

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%$
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



3

0

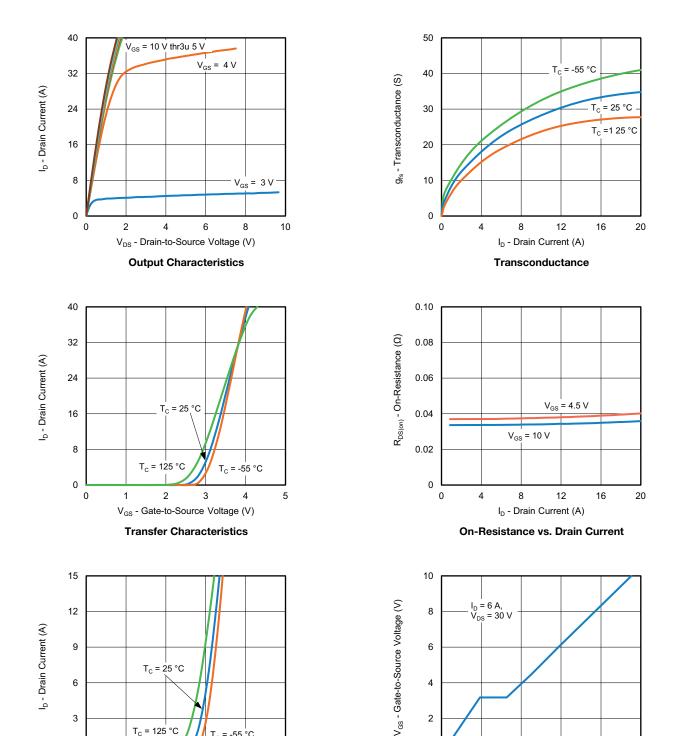
T<sub>C</sub> = 125 °C

V<sub>GS</sub> - Gate-to-Source Voltage (V)

**Transfer Characteristics** 

T<sub>C</sub> = -55 °C

## **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



2

0

0

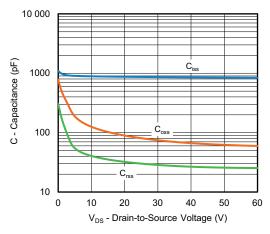
Q<sub>q</sub> - Total Gate Charge (nC)

**Gate Charge** 

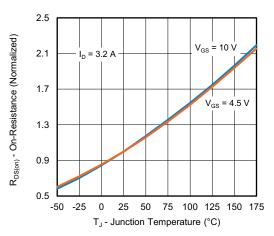
5



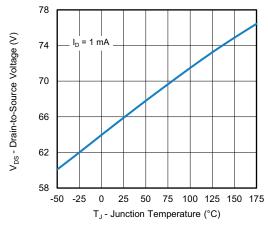
## **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



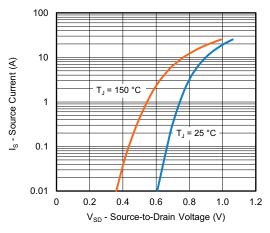
#### Capacitance



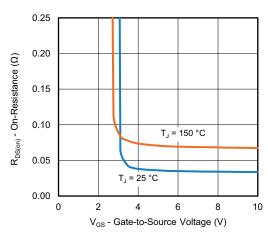
On-Resistance vs. Junction Temperature



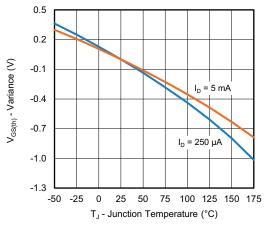
Drain Source Breakdown vs. Junction Temperature



**Source Drain Diode Forward Voltage** 



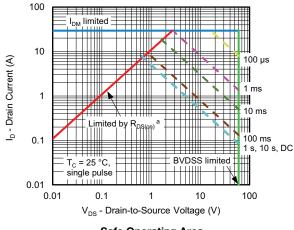
On-Resistance vs. Gate-Source Voltage



**Threshold Voltage** 



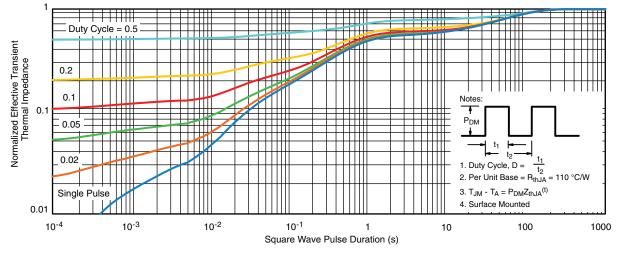
## **THERMAL RATINGS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



Safe Operating Area

#### Note

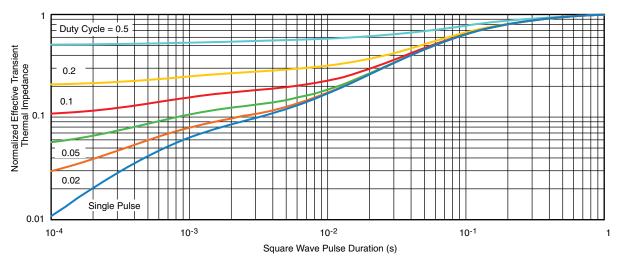
d.  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified



Normalized Thermal Transient Impedance, Junction-to-Ambient



### **THERMAL RATINGS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



Normalized thermal Transient Impedance, Junction-to-Foot

#### Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction to Foot (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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