

Note: This datasheet may be out of date. Please download the latest datasheet of CSTNE10M0G520000R0 from the official website of Murata

Manufacturing Co., Ltd.

http://www.murata.com/en-sg/products/productdetail?partno=CSTNE10M0G520000R0

CSTNE10M0G520000R0





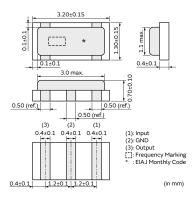






Appearance & Shape







Features

MURATA's package technology expertise has enabled the development of the Chip CERALOCK(R) with built-in load capacitors.

High-density mounting is made possible by the small package and the elimination of the need for an external load capacitor.

Features

- 1. Oscillation circuits do not require external load capacitors.
- 2. Available for a wide frequency range.
- 3. Extremely small and have a low profile.
- 4. No adjustment is necessary for oscillation circuits.
- 5. Stable supply is ensured due to not using precious metal(Palladium).



Applications

| | Other Usage | Consumer/Industrial |
|---|-------------|---------------------|
| 1 | | |



Packaging Information

| Packaging | Specifications | Standard Packing Quantity |
|-----------|---------------------|---------------------------------|
| R0 | 180mm Embossed Tape | 3000 |

1 of 2

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2. This datasheet has only typical specifications because there is no space for detailed specifications

Therefore, please review our product specifications or consult the approval sheet for product specifications before ordering





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Specifications

| Product Type | Ceramic Resonator | |
|-----------------------------|-------------------|--|
| | (CERALOCK) | |
| Series | CSTNE_G | |
| Frequency | 10.000MHz | |
| Frequency Tolerance | +/-0.50% max. | |
| Operating Temperature Range | -40°C~85°C | |
| | | |
| Frequency Shift by | +/-0.20% max. | |
| Temperature | , 0.20 %axi | |
| Frequency Aging | +/-0.10% max. | |
| Resonant Impedance (R1) | 40ohm max. | |
| Built-in Load Capacitance | | |
| (CL1/CL2) | 10pF | |
| Shape | SMD | |
| Wash | Not available | |
| L x W (size) | 3.2x1.3mm | |
| Mass | 10.48mg | |

2 of 2

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