# 32.768kHz TCXO

ATXK-H11

Request Samples 🕥



3.28 x 2.58 x 1.36 mm RoHS/RoHS II Compliant MSL Level = 1

### Features

- Frequency Stability options: ±3.8ppm over -10 to +60°C, ±5.0ppm over -40 to +85°C, & ±8.0ppm over -40 to +105°C
- Output waveform CMOS
- Low power consumption
- Supply Voltage options: 3.3V, 2.5V, 1.8V

# Applications

- Frequency reference for real time clocks (RTCs)
- Portable & wearable electronics
- Internet of Things (IoT)
- Consumer electronics
  - Timing synchronization for networks, servers, hubs, routers & switches

## Electrical Specifications [Note 1]

Parameters		Min.	Тур.	Max.	Units	Notes
Frequency (fc)		32.768			kHz	
Operating Temperature Range		-40		+85	°C	See Options
Storage Temperature Range		-55		+85	°C	
Frequency Stabilit	$v \Delta f/f_o vs:$	-				
	Tolerance			+2.5	ppm	Reference to fc (at 25°C±2°C), Pre-reflow
Tolerance		-3.0		+3.0		Reference to fc (at $25^{\circ}C \pm 2^{\circ}C$ ), 24 hours after reflow, one time
Temperature		-5.0		+5.0		See Options Reference to frequency tolerance reading (fo) at $25^{\circ}C \pm 2^{\circ}C$
Load Change		-0.2		+0.2		Load $\pm 10\%$
Supply Voltage Change		-1.0		+1.0	ppm/V	
Aging		-3.0		+3.0	ppm	First year at +25°C±2°C
Timing error over time (±5 ppm over -40°C to +85°C)		$\pm 0.432$ sec/day; $\pm 12.960$ sec/month; $\pm 2.628$ minutes/year				Reference to frequency tolerance reading (fo) at $25^{\circ}C \pm 2^{\circ}C$
Supply Voltage (V <sub>DD</sub> ) [Note 2]		+3.135	+3.3	+3.465	V	Option E
		+2.375	+2.5	+2.625		Option C
		+1.71	+1.8	+1.89		Option D
Supply Current (IDD)			1.0	2.0	μA	Without load
Disable Current				1.0	μA	Pad 1 logic low
Start-up Time				0.5	sec	
Rise and Fall Time (Tr/Tf)				100	ns	20% to 80% of waveform,15pF Load
Symmetry @ 1/2 VDD		40		60	%	
Output Voltage	Voh	90%V <sub>DD</sub>			V	
	Vol			10%V <sub>DD</sub>	v	
Output Load				15	pF	CMOS
Output Waveform		CMOS				
Tri-state function [Note 3]		"1" (VIH≥0.8*VDD): Oscillation; "0" (VIL<0.2*VDD): No Oscillation/Hi Z			V	

Note 1: All measurements made over specified operating temperature range, at nominal V<sub>DD</sub>, and 15pf load, unless otherwise specified.

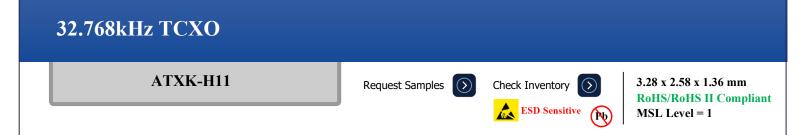
Note 2: Operational voltage range: 1.5V to 3.63V

Note 3: Do not leave pad 1 (tri-state) floating (no connect). Pad 1 must be tied to Vdd (logic 1) for proper oscillation on pad 3.



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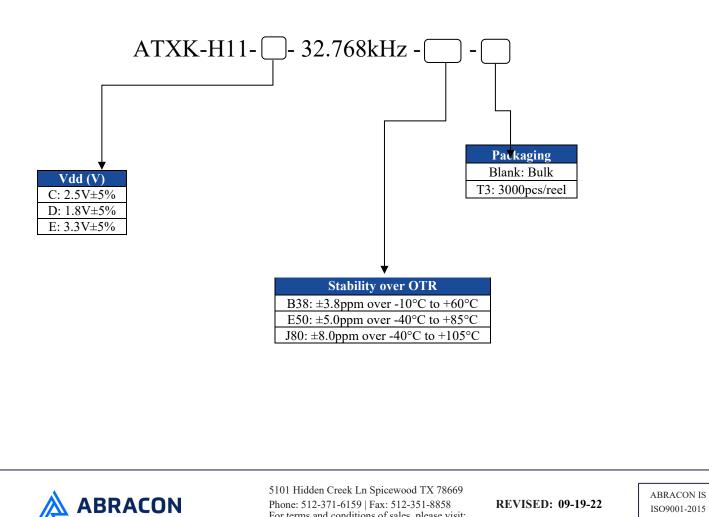


#### **Absolute Maximum Ratings**

Parameters	Symbol	Conditions	Rating	Unit
Supply voltage range (4)	VDD	Between V <sub>DD</sub> and V <sub>SS</sub>	-0.3 to +4.5	V
Input voltage range (4)	VIN	Between INH and Vss	-0.3 to V <sub>DD</sub> +0.3 (5)	V
Output voltage range (4)	VOUT	Output pad	-0.3 to V <sub>DD</sub> +0.3 (5)	V
Junction temperature (4)	Tj	-	150	°C
Storage temperature range	TSTG	-	-55 to +105	°C

Absolute maximum ratings are the values that must not be exceeded. This product may suffer damage if any one of these parameter Note 4: ratings is exceeded. Operation and characteristics are guaranteed only when the product is operated per the specification datasheet. Note 5: V<sub>DD</sub> is a V<sub>DD</sub> value of recommended operating conditions.

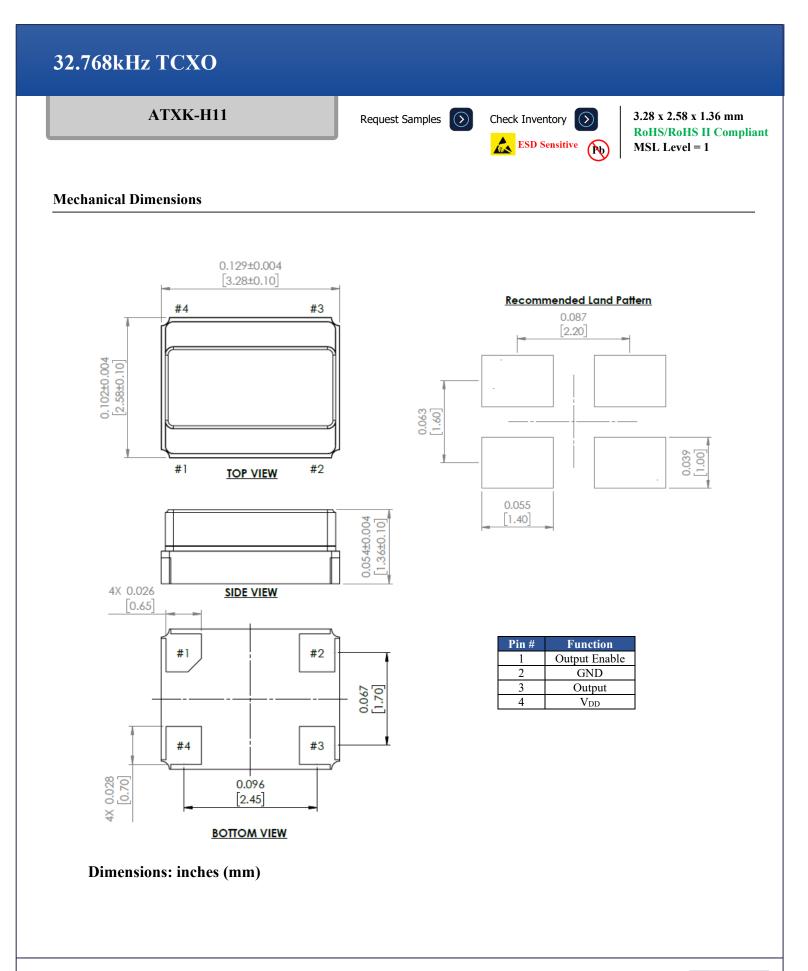
### **Part Identification**



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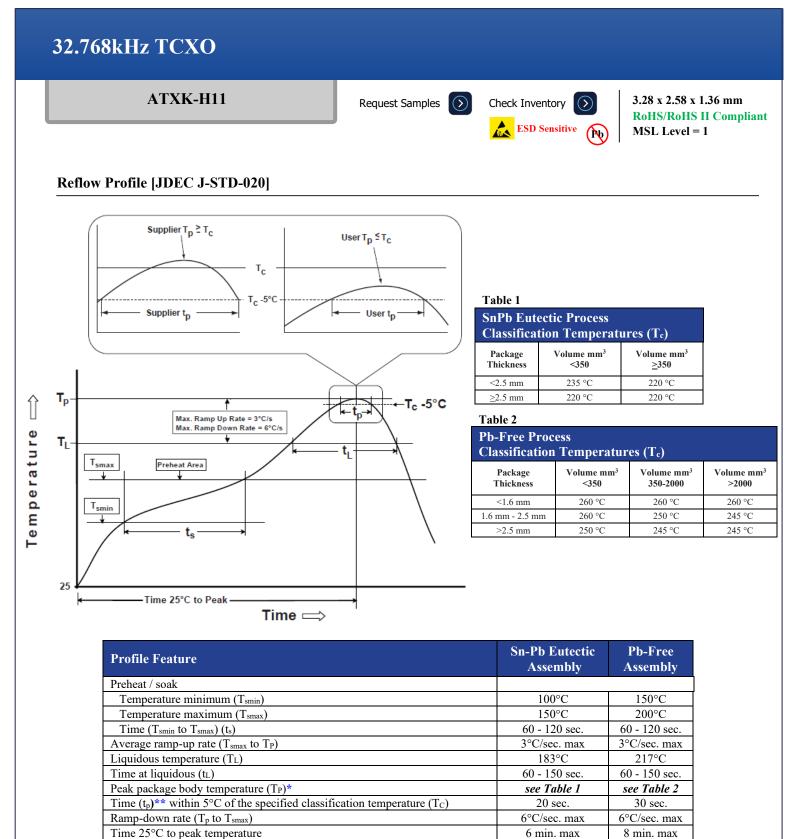
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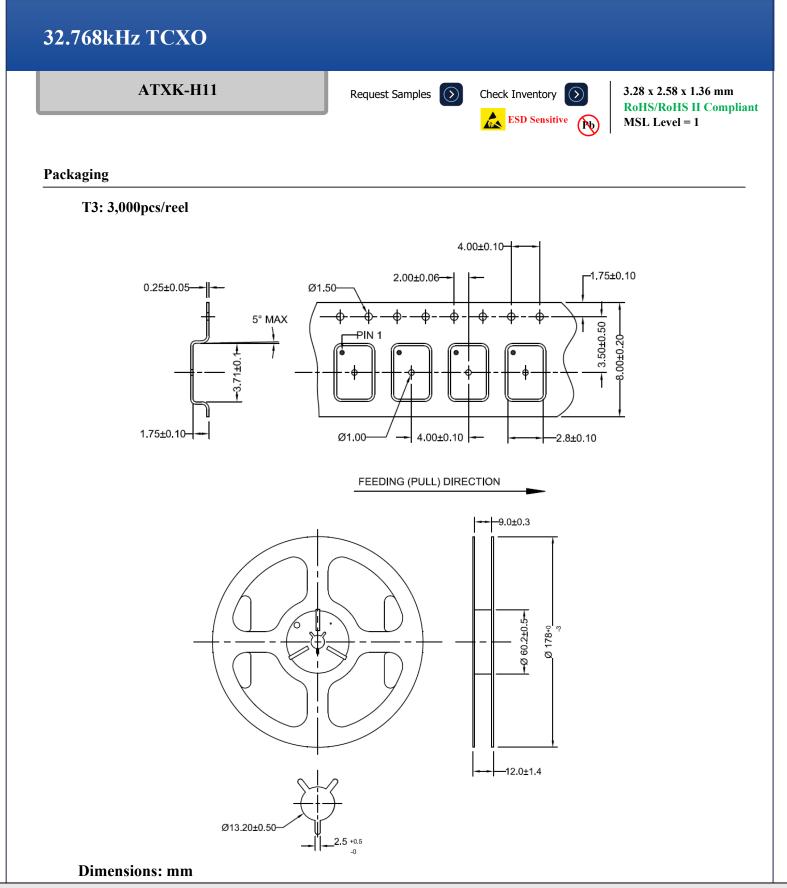
\*Tolerance for peak profile temperature (T<sub>P</sub>) is defined as a supplier minimum and a user maximum.

\*\*Tolerance for time at peak profile temperature (tp) is defined as supplier minimum and a user maximum.



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