Multilayer SMD Ceramic Capacitors **MultiComp** PRO



Features:

- · High reliability and stability.
- Small size and high capacitance
- Safety standard approval by
- EN 60384-14 : 2013
- IEC 60384-14 : 2013

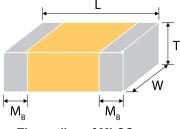
UL 60384-14 (Ed 2.0)

· Certificate number:

TUV: R50195920, TUV: R50381780

- UL: E182369
- HALOGEN compliant.

External Dimensions & Structure



The outline of MLCC

Description:

SAFETY CERTIFIED CAPACITORS are designed for surge or lightning immunity in modem facsimile and other equipments. The capacitors of series S2 are class X1/Y2 compliant respectively.

The green type capacitors in S2 and S3 series are manufactured by using environmentally friendly materials without lead or cadmium.

The terminations are composed of plated nickel and pure tin to feature the superior leaching resistance during soldering.

Applications:

- Modem.
- · Facsimile.
- Telephone.
- Other electronic equipment for lighting or surge protection and isolation

Safety certified Caps.

Size	L	W	T	M _B min
Inch (mm)	(mm)	(mm)	(mm)/Symbol	(mm)
2220 (5750)	5.7 ±0.4	5 ±0.4	2.8 ±0.3 (M)	

Safety certified Caps. with soft termination

Size Inch (mm)	L (mm)	W(mm)	T(mm)	Mв (mm)
2220 (5750)	5.7 ±0.5	5 ±0.5	2.8 ±0.3 (M)	0.6 ±0.3

Reflow soldering only is recommended.

General Electrical Data:

Dielectric	X7R	
Size	2220	
Capacitance	100pF to 4700pF	
Capacitance tolerance	J (±5%), K (±10%), M (±20%)	
Rated voltage (WVDC)	250V AC	
Q/DF (Tan δ)	DF≤2.5%	
Insulation resistance at Ur	≥10GΩ	
Peak impulse voltage	5000V ~ 6000V	
Operating temperature	-55°C to +125°C	
Capacitance characteristic	±15%	
Termination	Ni/Sn (lead-free termination)	
Certified number	TUV: R50195920, TUV: R50381780, UL: E182369	
Test standard	EN 60384-14 : 2013, IEC 60384-14 : 2013, UL 60384-14 (Ed 2.0)	
$*$ X7P: Apply 1.0+0.2\/rms. 1.0	(Hz+10%, at 25°C, ambie of temperature	

* X7R: Apply 1.0±0.2Vrms, 1.0kHz±10%, at 25°C ambie nt temperature.

Newark.com/multicomp-pro Farnell.com/multicomp-pro sg.element14.com/b/multicomp-pro

multicomp PRO

24/12/22 V1.0

RoHS Compliant

Packaging Style And Quantity

Size	Thickness (mm)/S	umbol	Paper	[.] tape
Size	Thickness (mm)/Symbol		7" reel	13" reel
2220 (5750)	2 ±0.2	К	1K	
2220 (5750)	2.5 ±0.3	М	0.5k	2k

Capacitance Range

Dielectric		X7	R	
Size		222	20	
Pe	ak Impulse Voltage	5000		
Ra	Rated Voltage (VDC) TUV IEC60384-14		UL 60384	
	100pF (101)			
	120pF (121)			
	130pF (131)			
	150pF (151)			
	160pF (161)	K	K	
	180pF (181)	K	K	
	220pF (221)	K	K	
	270pF (271)	K	K	
	300pF (301)		K	
	330pF (331)	К	К	
Ø	390pF (391)	К	К	
Capacitance	470pF (471)	К	К	
acit	560pF (561)	К	К	
Cap	680pF (681)	К	К	
	720pF (721)	K	К	
	820pF (821)	K	К	
	1,000pF (102)	K	K	
	1,200pF (122)	М	М	
	1,500pF (152)	М	М	
	1,800pF (182)	М	М	
	2,200pF (222)	М	М	
	2,700pF (272)	М	М	
	3,300pF (332)	М	М	
	3,900pF (392)	М	М	
	4,700pF (472)	М	М	

The letter in cell is expressed the symbol of product thickness.



Reliability Test Conditions And Requirements

No	ltem	Standard Method	Test Condition	Requirements
1	Visual examination and Dimensions	IEC 60384-1 4.1	-	 * No remarkable defect. * Dimensions to confirm to individual specification sheet
2	Capacitance	IEC 60384-1 4.2.2	* Test temp.: Room Temperature. * Class I : (C0G) Cap.1000pF, 1.0±0.2Vrms, 1MHz±10% Cap.>1000pF, 1.0±0.2Vrms, 1KHz±10%	
3	D.F. (Dissipation Factor) Tangent of loos angle	IEC 60384-1 4.2.3	* Class II : (X7R) 1.0±0.2Vrms, 1KHz±10%.	Dielectric Q/D.F. Remark Class II (X7R) D.F.≤2.5%
4	Temperature Coefficient	IEC 60384-21/22 4.6	With no electrical load.T.C.Operating TempX7R-55°C to 125°C at 25°C	T.C.Capacitance ChangeX7RWithin ±15%
5	Voltage proof (Dielectric Strength)	IEC 60384-14 4.2.1	 * To apply voltage : X Capacitor : 1075Vdc (4.3UR). Y Capacitor : 1500Vac. * Duration : 60 sec. * The charge current shall not exceed 0.05A. * The voltage shall be raised from the near zero to the test voltage a rate no exceeding 150V(r.m.s.)/sec. 	* No evidence of damage or flash over during test.
6	Insulation	IEC	Rated Apply Charge Char Vol.(V) Voltage Current Time	Delectric Requirements
0	Resistance	60384-21/22 4.5.3	>500 500V DC ≤50mA 60 s *Test temp.: Room Temperature.	c. Class II (X7R) ≥10G or RxC500-F, whichever is smaller
7	Solderability	IEC 60384-21/22 4.1	* Solder temperature: 235±5°C(0201~1210). * Solder temperature: 245±5°C(1808~2225). * Dipping time : 2±0.5 sec.	* 75% min. coverage of all metalized area
8	Resistance to Soldering Heat	IEC 60384-14 4.4 IEC 60384-21/22 4.9	 * Solder temperature : 260±5°C. * Dipping time : 10±1 sec. * Preheating : 120 to 150°C for 1 minu before immerse the capacitor in a eutectic solder. * Measurement to be made after keepi at room 	Class II ≥1GQ Within initial
			temperature for 24±2 hrs.	



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No	ltem	Standard Method		Test Condition			Req	uirements	
				he five cycles acc res and time.	ording to the				
			Step 1	ſemp.(°C)	Time (min.)	Dielectric	I.R.	Cap. Change	Q/D.F.
9	Temperature Cycle	IEC 60384-21/22 4.11	' t	Min. operating emp. +0/-3	30±3	Class II	To meet initial	Within	≤1.5(D.F.)
				Room temp. Max.operating	2~3 30±3	(X7R)	require ment	±7.5%	× initial requirement
				emp. +3/-0 Room temp.	2~3				
						* No remark	able damag	e.	
	Line and the s			90~95% RH.		Dielectric	I.R.	Cap. Change	Q/D.F.
10	Humidity (Damp Heat) Steady State	IEC 60384-14 4.12	 * Test time : 500 +24/-0hrs. * Applied voltage : 250Vac. * Measurement to be made after keeping at room temp. for 24±2 hrs (Class I) and 48±4 hrs (Class II). 			Class II (X7R)	≤1G or RxC≥ 25-F, whichever is smaller		≤2.0(D.F.) × initial requirement
11	Passive Flammability	IEC 60384-14 4.17 IEC 60384-1 4.38	* Volume sample: 21.56 mm3 * Flame exposure time: 5 sec Max. * Category of flammability : C.			* Capacitor	didn't burn a	at all.	
12	Active Flammability	IEC 60384-21/22 4.18	* The capacitors applied UR (250Vac). Then each sample shall be subjected to 20 discharges from a tank ca- pacitor, charge to a voltage that, when discharged, plase Ui 2500V for X1Y2 across the capacitor under test. The interval between successive discharges shall be 5 sec.			* The chees	se cloth shall	not burn with	n a flame.
13.	High Temperature	IEC 60384-	* Impulse V	oltage : Jual capacitor sha		* Appeara			
	Load (Endurance)	14 4.14	subjected to			No mecha * Cap. cha	nical dama	ge.	
	(Endurance)		= 6.0KV	·	<i>,</i> .	X7R withir	-		
			three times	s Impulse 6KV) ir before applied to		* D.F. valu			
			test. * Test temp.			X7R5.0%. * I.R.1G.			
			* Test time: * Applied vo	1000 +48/-0 hrs. oltage :			strength s	atisfies the	specified initial
			X capacitor:	: 1.25UR (312.5V : 1.70UR (425Vac		value.	0		
				hour the voltage					
			1000Vrms f		ifter keeping				
			1	l±2 hrs (Class I) a	and 48±4 hrs				



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No	ltem	Standard Method	Test Condition	Requirements
14.	Resistance to Flexure of Substrate	IEC 60384-21/22 4.8	* Capacitors mounted on a substrate. The board shall be bent 1mm with a rate of 1mm/sec. 20 60 1 1 1 1 1 1 1 1 1 1	* No remarkable damage. Dielectric Cap. Change Class II (X7R) Within ±12.5% (This capacitance change means the change of capacitance under specified flexure of substrate from the capacitance measured before the test)
15.	Adhesive Strength of Termination	IEC 60384-21/22 4.15 IEC 60384-1 4.13	* Capacitors mounted on a substrate. A force of 10N applied perpendicular to the place of substrate and parallel the line joining the center of terminations for 10 ±1 sec. Pressurizing force. Capacitor. P.C. Board.	* No remarkable damage or removal of the terminations.
16.	Vibration	IEC 60384-1 4.17	 * Reflow solder the capacitors on P. C. Board before test. * Vibration frequency : 10~55 Hz/ min. * Total amplitude : 1.5mm. * Repeat the conditions for 2 hours each in 3 perpendicular directions. 	* No remarkable damage. * Cap. change and Q/D.F. : To meet initial spec.
17.	Impulse Voltage	IEC 60384-14 4.13	* X1 : 4.0KV * Y2 : 5.0KV. * Number of impulse : 24 max.	* There shall be no permanent breakdown or flashover.

* "Room condition" Temperature: 15 to 35°C, Relativ e humidity: 25 to 75%, Atmospheric pressure: 86 to 106kPa.



Embossed Tape Dimensions

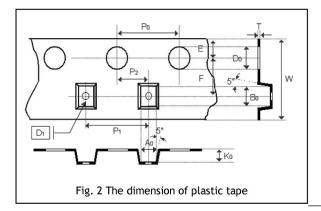


Fig. 3 The dimension of reel	

Size	2220		
Chip Thickness	2 ±0.2	2.5 ±0.3	
A ₀	<5.8	<5.8	
B ₀	<6.5	<6.5	
Т	0.3 ±0.1	0.3 ±0.1	
K ₀	<2.5	<3.5	
w	12 ±0.3	12 ±0.3	
P ₀	4 ±0.1	4 ±0.1	
10xP ₀	4 ±0.2	4 ±0.2	
P1	8 ±0.1	8 ±0.1	
P ₂	2 ±0.1	2 ±0.1	
D ₀	1.5 +0.10/-0	1.5 +0.10/-0	
D ₁	1.5 ±0.1	1.5 ±0.1	
E	1.75 ±0.1	1.75 ±0.1	
F	5.5 ±0.1	5.5 ±0.1	

Size	2220		
Reel size	7" 13"		
С	13 +0.5/-0.2	13 +0.5/-0.2	
W 1	12.4+2.0/-0	12.4+2.0/-0	
Α	178 ±0.1	330 ±1	
N	60 +1/-0	100 ±1	



Application Notes

Storage and handling conditions

To prevent the damage of solderability of terminations, the following storage conditions are recommended: Indoors under $5 \sim 40^{\circ}$ C and $20\% \sim 70\%$ RH; MSL Level 1.

No harmful gases containing sulfuric acid, ammonia, hydrogen sulfide or chlorine.

Packaging should not be opened until the capacitors are required for use. If opened, the pack should be re-sealed as soon as is practicable. Taped product should be stored out of direct sunlight, which might promote deterioration in tape or adhesion performance. The product is recommended to be used within 12 months after shipment and checked the solderability before use.

Handling

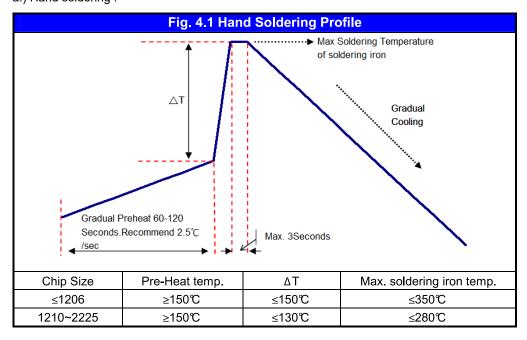
Chip capacitors are dense, hard, brittle, and abrasive materials. They are liable to suffer mechanical damage, in the form of cracks or chips. Chip Capacitors should be handled with care to avoid contamination or damage. To use vacuum or plastic tweezers to pick up or plastic tweezers is recommended for manual placement. Tape and reeled packages are suitable for automatic pick and placement machine.

Preheat

In order to minimize the risk of thermal shock during soldering, a carefully controlled preheat is required. The rate of preheat should not exceed 3°C per secon d.

Soldering

Use middy activated rosin RA and RMA fluxes do not use activated flux. The amount of solder in each solder joint should be controlled to prevent the damage of chip capacitors caused by the stress between solder, chips, and substrate. a.) Hand soldering :



Soldering iron tip diameter 1.0 mm and wattage max. 20W.

- The Capacitors shall be pre-heated and that the temperature gradient between the devices and the tip of the soldering iron.
- The required amount of solder shall be melted on the soldering tip.
- The tip of iron should not contact the ceramic body directly.

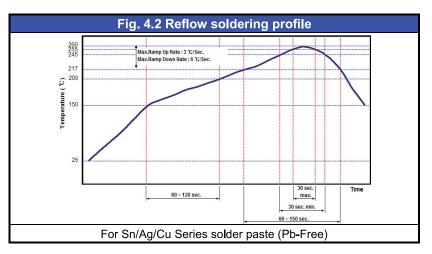


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• The Capacitors shall be cooled gradually at room temperature after soldering.

Forced air cooling is not allowed.

b.) Reflow soldering :



Cooling

After soldering, cool the chips and the substrate gradually to room temperature. Natural cooling in air is recommended to minimize stress in the solder joint.

Cleaning

All flux residues must be removed by using suitable electronic-grade vapor-cleaning solvents to eliminate contamination that could cause electrolytic surface corrosion. Good results can be obtained by using ultrasonic cleaning of the solvent. The choice of the proper system is depends upon many factors such as component mix, flux, and solder paste and assembly method. The ability of the cleaning system to remove flux residues and contamination from under the chips is very important.

Part Number Table

[Description	Part Number
ĺ	Capacitor, 2220, 2200pF, X7R, 5000V	MPS255B222K502CT

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