

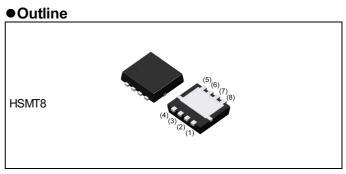
V _{DSS}	-100V
R _{DS(on)} (Max.)	86mΩ
I _D	±14.5A
P _D	20W

Features

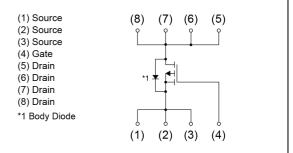
- 1) Low on resistance
- 2) High Power small mold Package (HSMT8)
- 3) Pb-free plating ; RoHS compliant
- 4) Halogen Free

Application

Switching



●Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	12
	Quantity (pcs)	3000
	Taping code	TB1
	Marking	P045AT

• Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

Paramete	Symbol	Value	Unit	
Drain - Source voltage	V _{DSS}	-100	V	
Continuous durin current	$T_c = 25^{\circ}C$	ا _D *1	±14.5	А
Continuous drain current	T _a = 25°C	I _D	±4.5	А
Pulsed drain current	I _{DP} *2	±18	А	
Gate - Source voltage		V _{GSS}	±20	V
Avalanche current, single pulse		I _{AS} *3	-4.5	А
Avalanche energy, single pulse		E _{AS} *3	1.4	mJ
		P _D ^{*1}	20	W
Power dissipation		P _D *4	2.0	W
Junction temperature		Tj	150	°C
Operating junction and storage temperature range		T _{stg}	-55 to +150	C°

Thermal resistance

Deremeter	Sumbol	Values			Linit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *1	-	-	6.2	°C/W
Thermal resistance, junction - ambient	R_{thJA}^{*4}	-	-	62.5	°C/W

• Electrical characteristics (T_a = 25°C)

Devenantes	Queek el	Canditiana		Values		1.1	
Parameter	Symbol Conditions –		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = -1mA$		-100	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{i}} I_{D} = -1mA$ referenced to 25°C		-68	-	mV/°C	
Zero gate voltage drain current	I_{DSS} V_{DS} = -100V, V_{GS} = 0V		-	-	-1	μA	
Gate - Source leakage current	I_{GSS} $V_{GS} = \pm 20V, V_{DS} = 0V$		-	-	±100	nA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -1mA$	-1.0	-	-2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	$\Delta V_{GS(th)} I_D = -1mA$		3.7	-	mV/°C	
Static drain - source	D *5	V _{GS} = -10V, I _D = -4.5A	-	67	86		
on - state resistance	${\sf R}_{\sf DS(on)}{}^{*5}$	V _{GS} = -4.5V, I _D = -4.5A	-	71	92	mΩ	
Gate resistance	R _G	R _G f=1MHz, open drain		5.7	-	Ω	
Forward Transfer Admittance	Y _{fs} * ⁵	$ Y_{fs} ^{*5}$ V _{DS} = -5V, I _D = -4.5A		-	-	S	

*1 T_c = 25°C, Limited only by maximum temperature allowed.

*2 Pw \leq 10µs, Duty cycle \leq 1%

*3 L \simeq 0.1mH, V_{DD} = -50V, R_G = 25 Ω , Starting T_j = 25°C Fig.3-1,3-2

- *4 Mounted on a Cu board (40×40×0.8mm)
- *5 Puls



•Electrical characteristics (T_a = 25°C)

Deremeter	Cumph of	Conditions	Values			Unit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	1990	-		
Output capacitance	C _{oss}	V _{DS} = -50V	-	95	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	80	-		
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq -50V, V_{GS} = -10V$	-	13	-		
Rise time	t _r *5	I _D = -2.25A	-	15	-	-	
Turn - off delay time	t _{d(off)} *5	$R_L \simeq 22.2\Omega$	-	170	-	ns	
Fall time	t _f *5	R _G = 10Ω	-	90	-		

• Gate charge characteristics ($T_a = 25^{\circ}C$)

Deremeter	Sumbol	Conditions		Values			1.1
Parameter	Symbol Cond		UNS	Min.	Тур.	Max.	Unit
Tatal asta al anna	Q_g^{*5}	V _{DD} ≃ -50V	V _{GS} = -10V	-	49.0	-	
Total gate charge				-	25.0	-	
Gate - Source charge	Q _{gs} *5	I _D = -4.5A	V _{GS} = -4.5V	-	4.8	-	nC
Gate - Drain charge	Q _{gd} *5			-	11.1	-	

•Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Deremeter	Symbol Conditions		Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	۱ _s	T _a = 25℃	-	-	-1.67	А
Pulse forward current	I_{SP}^{*2}	$T_a = 25 C$	-	-	-18	А
Forward voltage	V_{SD}^{*5}	V _{GS} = 0V, I _S = -1.67A	-	-	-1.2	V
Reverse recovery time	t _{rr} *5	I _S = -4.5A, V _{GS} =0V	-	37	-	ns
Reverse recovery charge	Q _{rr} *5	di/dt = 100A/µs	-	44	-	nC



• Electrical characteristic curves

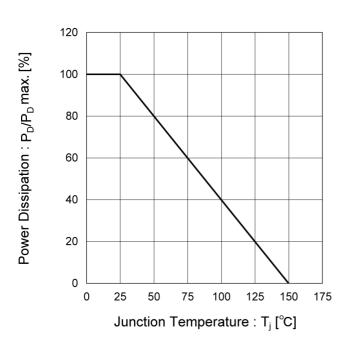
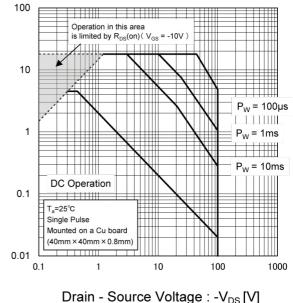


Fig.1 Power Dissipation Derating Curve

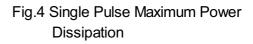
Fig.2 Maximum Safe Operating Area

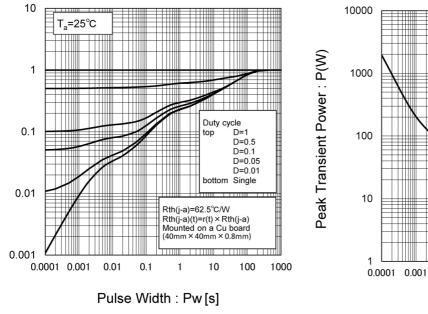


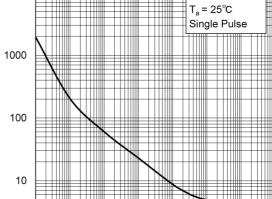
Drain Current : -I_D [A]

Drain - Source voltage . -v_{DS}[v

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width







0.1

1

Pulse Width : Pw [s]

0.01

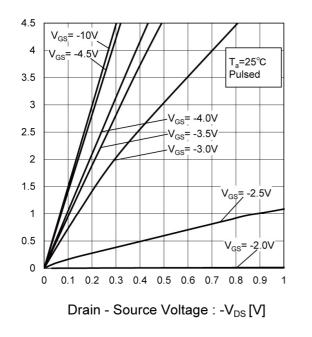
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10

100

1000

Normalized Transient Thermal Resistance : $r_{\scriptscriptstyle (t)}$



Drain Current : -I_D [A]

Fig.5 Typical Output Characteristics(I)

Fig.7 Normalized Breakdown Voltage vs. Junction Temperature

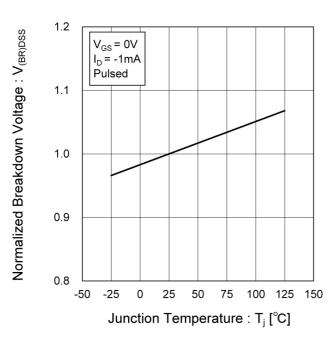


Fig.6 Typical Output Characteristics(II)

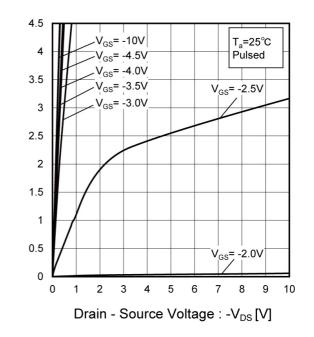
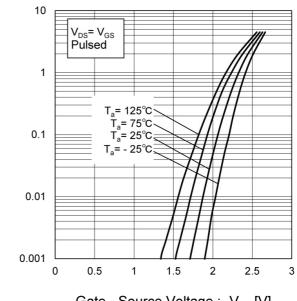


Fig.8 Typical Transfer Characteristics





Drain Current : -I_D [A]

• Electrical characteristic curves

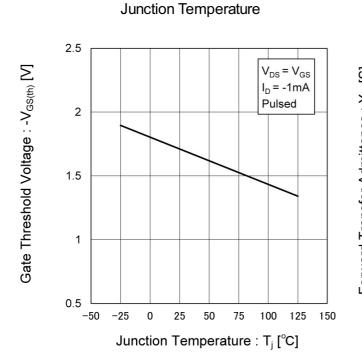


Fig.9 Gate Threshold Voltage vs.

Fig.10 Forward Transfer Admittance vs. Drain Current

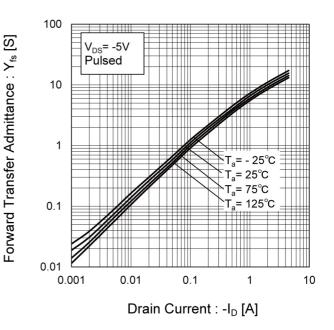
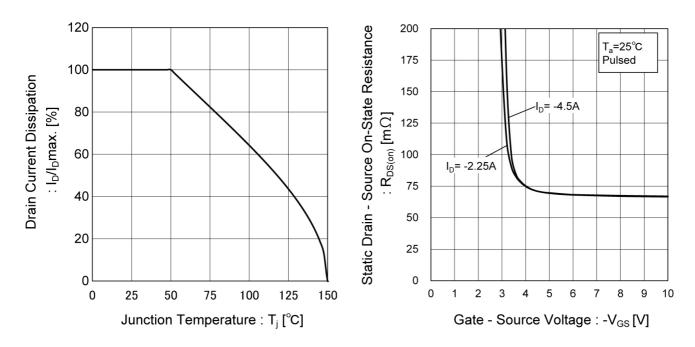


Fig.11 Drain Current Derating Curve

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



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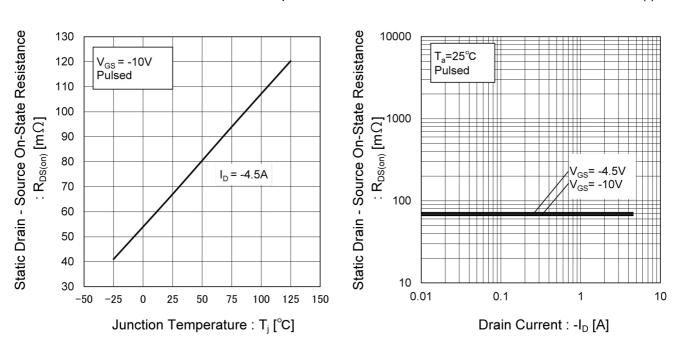
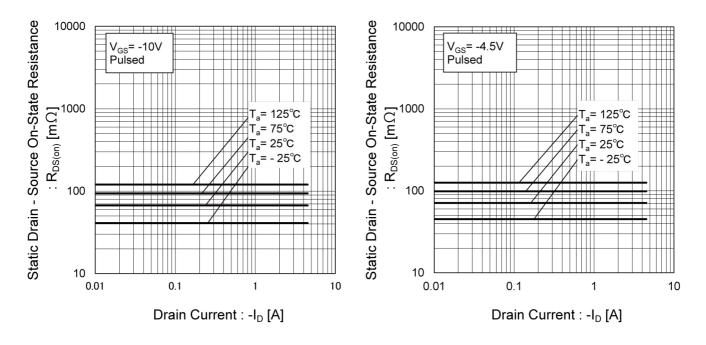


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

Fig.15 Static Drain - Source On - State
Resistance vs. Drain Current (II)

Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)



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• Electrical characteristic curves

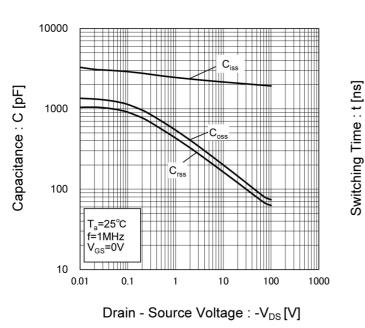
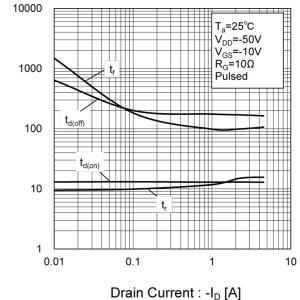


Fig.17 Typical Capacitances vs.

Drain - Source Voltage

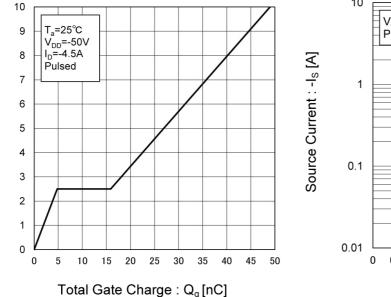
Fig.18 Switching Characteristics



_.....

Fig.19 Typical Gate Charge

Fig.20 Source Current vs. Source Drain Voltage



10 $V_{GS}=0V$ Pulsed 1 $T_a=125^{\circ}C$ $T_a=75^{\circ}C$ $T_a=25^{\circ}C$ $T_a=25^{\circ}C$ $T_a=2$

Gate - Source Voltage : -V_{GS} [V]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

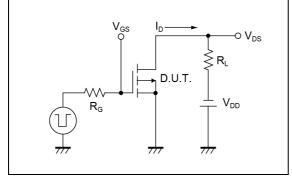


Fig.2-1 Gate Charge Measurement Circuit

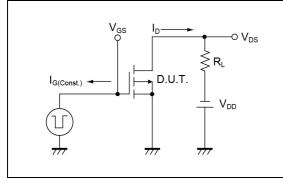


Fig.3-1 Avalanche Measurement Circuit

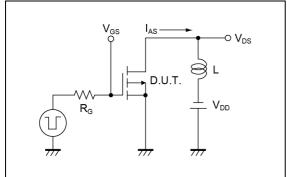


Fig.1-2 Switching Waveforms

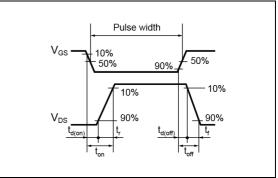


Fig.2-2 Gate Charge Waveform

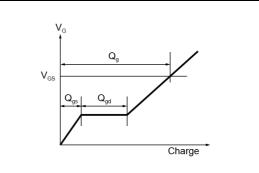
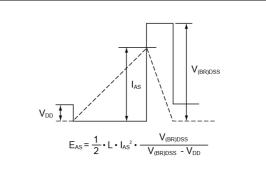


Fig.3-2 Avalanche Waveform



Notice

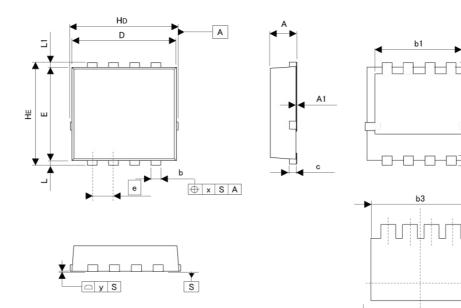
This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.



Dimensions

HSMT8 (TB1)

(3.3x3.3)



b2 Pattern of terminal position areas [Not a recommended pattern of soldering pads]

е

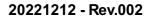
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2 <u>2</u>

4

DIM	MILIME	TERS	INCI	HES
DIM	MIN	MAX	MIN	MAX
А	0.65	0.85	0.026	0.033
A1	0.00	0.10	0.000	0.004
b	0.24	0.42	0.009	0.017
b1	2.29	2.69	0.090	0.106
С	0.05	0.25	0.002	0.010
D	3.05	3.25	0.120	0.128
Е	2.95	3.15	0.116	0.124
е	0.	65	0.0)26
HD	3.20	3.40	0.126	0.134
HE	3.20	3.40	0.126	0.134
L	0.05	0.23	0.002	0.009
L1	0.05	0.23	0.002	0.009
Lp	0.20	0.60	0.008	0.024
Lp1	0.20	0.60	0.008	0.024
Lp2	1.83	2.63	0.072	0.104
х	-	0.10	-	0.004
У	-	0.10	-	0.004
DIM	MILIME	TERS	INCI	HES
DIVI	MIN	MAX	MIN	MAX
b2	-	0.52	-	0.020
b3	-	2.79	-	0.110
11	-	0.70	-	0.028
12	· - ·	0.70	-	0.028
13	12	2.53	-	0.100
14	-	3.60	-	0.142

Dimension in mm/inches



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Notice

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(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSI	CLASS II b	CLASSII
CLASSⅣ	CLASSII	CLASSⅢ	CLASSI

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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