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Vishay Siliconix

Automotive N-Channel 40 V (D-S) 175 °C MOSFET



PRODUCT SUMMARY				
V _{DS} (V)	40			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.0006			
$R_{DS(on)}$ (Ω) at $V_{GS} = 4.5 \text{ V}$	0.00087			
I _D (A) ^e	504			
Configuration	Single			

FEATURES

- TrenchFET® Gen IV power MOSFET
- AEC-Q101 qualified
- 100 % R_q and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



	O ^D
GO-	
N-Channel MOSFET	o _s

ORDERING INFORMATION	
Package	PowerPAK® SO-8SW
Lead (Pb)-free and halogen-free	SQRS140ELP (for detailed order number please see www.vishay.com/doc?79771)

ABSOLUTE MAXIMUM RATINGS ($T_C = 25 ^{\circ}C$, unless	s otherwise noted)	
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V _{DS}	40	V
Gate-source voltage		V _{GS}	± 20	
Continuous drain current e	T _C = 25 °C	l _D	504	
	T _C = 125 °C		291	
Continuous source current (diode conduction)		I _S	242	А
Pulsed drain current ^{a, e}			1342	
Single pulse avalanche current	1 0111	I _{AS}	68	
Single pulse avalanche energy	L = 0.1 mH	E _{AS}	234	mJ
Maximum power dissipation ^a	T _C = 25 °C	P5	266	W
	T _C = 125 °C		88	
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +175	°C
Soldering recommendations (peak temperature) c			260	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-ambient	PCB mount b	R_{thJA}	42	- °C/W	
Junction-to-case (drain) ^d		R _{thJC}	0.56		

Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%$
- b. When mounted on 1" square PCB (FR4 material)
- c. See solder profile (www.vishay.com/doc?73257). The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection
- d. As per on JESD51-14
- e. Values based on R_{thJC} and T_C of 25 °C. Actual values achievable will be dependent on the thermal characteristics of the complete system



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PARAMETER	SYMBOL	TES	ST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static							•
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0$, $I_D = 250 \mu A$		40	-	-	V
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$		1.2	1.7	2.2	V
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA
		$V_{GS} = 0 V$	V _{DS} = 40 V	-	-	1	
Zero gate voltage drain current	I _{DSS}	V _{GS} = 0 V	V _{DS} = 40 V, T _J = 125 °C	-	-	50	μΑ
		V _{GS} = 0 V	V _{DS} = 40 V, T _J = 175 °C	-	-	250	
On-state drain current a	I _{D(on)}	V _{GS} = 10 V	V _{DS} ≥ 5 V	30	-	-	Α
Drain-source on-state resistance ^a		V _{GS} = 10 V	I _D = 15 A	-	0.00048	0.0006	
	Б	V _{GS} = 10 V	I _D = 15 A, T _J = 125 °C	-	-	0.00088	
	R _{DS(on)}	V _{GS} = 10 V	I _D = 15 A, T _J = 175 °C	-	-	0.0011	Ω
		V _{GS} = 4.5 V	I _D = 15 A	-	0.00066	0.00087	
Forward transconductance b	9 _{fs}	V_{DS}	= 15 V, I _D = 40 A	-	160	-	S
Dynamic ^b							
Input capacitance	C _{iss}		V _{DS} = 25 V, f = 1 MHz	-	10998	15398	pF
Output capacitance	C _{oss}	V _{GS} = 0 V		_	3045	4263	
Reverse transfer capacitance	C _{rss}			-	282	395	
Total gate charge c	Q_{g}			-	196	294	nC
Gate-source charge c	Q_{gs}	V _{GS} = 10 V	$V_{DS} = 20 \text{ V}, I_{D} = 15 \text{ A}$	_	33	-	
Gate-drain charge ^c	Q_{gd}]		-	33	-	
Gate resistance	R_g	f = 1 MHz		0.6	1.2	1.8	Ω
Turn-on delay time ^c	t _{d(on)}	$V_{DD} = 20 \text{ V}, \text{ R}_{L} = 1.33 \Omega$ $I_{D} \cong 15 \text{ A}, \text{ V}_{GEN} = 10 \text{ V}, \text{ R}_{g} = 1 \Omega$		-	20	30	
Rise time ^c	t _r			-	20	30	
Turn-off delay time ^c	t _{d(off)}			-	78	117	ns
Fall time ^c	t _f			-	21	32	
Source-Drain Diode Ratings and Cha	aracteristics b						
Pulsed current ^a	I _{SM}				-	480	Α
Forward voltage	V_{SD}	I _F = 15 A, V _{GS} = 0 V		-	-	1.1	V
Body diode reverse recovery time	t _{rr}	I _F = 40 A, di/dt = 100 A/μs		-	78	156	ns
Body diode reverse recovery charge	Q _{rr}				122	224	nC
Reverse recovery fall time	t _a			-	36	-	
Reverse recovery rise time	t _b			-	42	-	ns
Body diode peak reverse recovery current	I _{RM(REC)}			-	2.7	-	Α

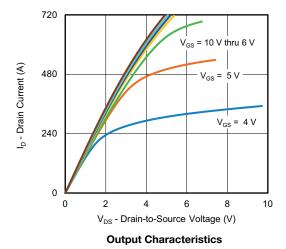
Notes

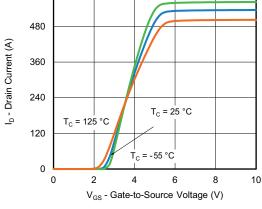
- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

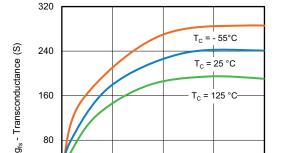


TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)





600

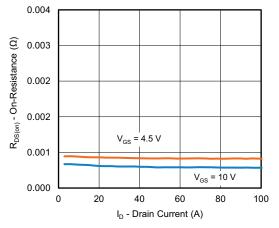


0

0

45

Transfer Characteristics



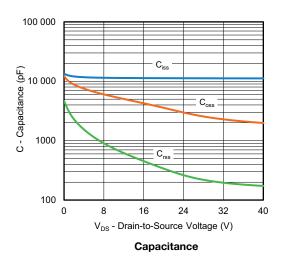
I_D - Drain Current (A) **Transconductance**

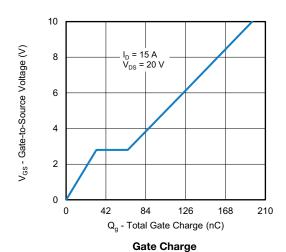
90

135

180

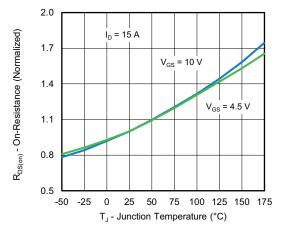
On-Resistance vs. Drain Current



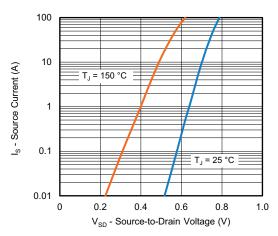




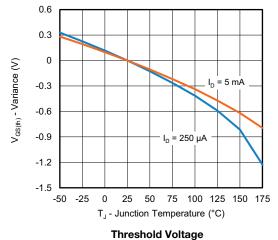
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



On-Resistance vs. Junction Temperature

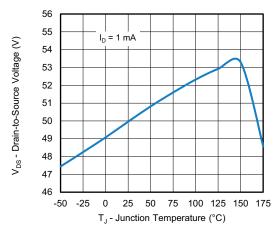


Source Drain Diode Forward Voltage

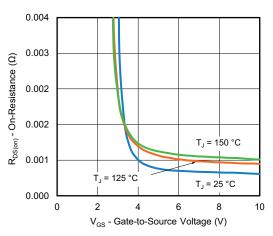


Note

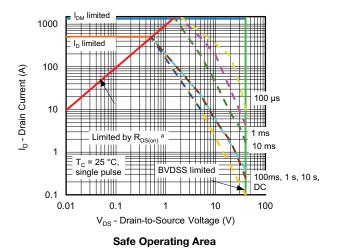
a. V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified



Drain Source Breakdown vs. Junction Temperature

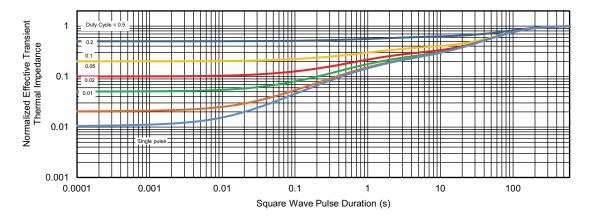


On-Resistance vs. Gate-to Source Voltage

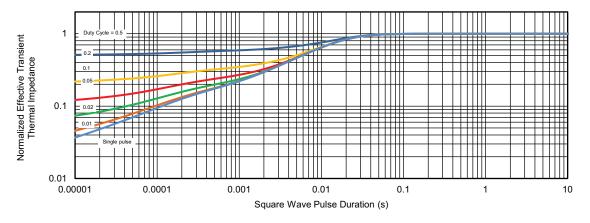




TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?62116.



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