



Part Number: EV53D52A

EVB-LAN8814 Evaluation Board

Quad-port triple-speed (10BASE-T/100BASE-TX/1000BASE-T) Ethernet PHY transceiver ports available through LAN8814

Four VSC7514XKS integrated triple-speed Ethernet ports for general test traffic flow as well as board image netbooting

PTP timestamping-in-the-PHY — LAN8814 Linux® driver can support Linux PTP (ptp4l) application

Voltage-mode Integrated Connector Magnetic (ICM) footprint-compatible with Pulse JXK0 devices

Interrupt-capable MDIO management of LAN8814 to the VSC7514XKS host

On-chip regulation for LAN8814 1V1 power supported

Clock-source selection options for LAN8814

Direct-connect RS-232 management interface via USB2 UART interface

Overview

The EVB-LAN8814 Evaluation Board is used to evaluate the LAN8814 physical layer (PHY) device. The LAN8814 is a low-power, quad-port triple-speed (10BASE-T/100BASE-TX/1000BASE-T) Ethernet PHY transceiver. It supports industry-standard Quad Serial Gigabit Media Independent Interface (QSGMII) and Quad Universal Serial Gigabit Media Independent Interface (Q-USGMII), as well as high-accuracy timestamping functions to support IEEE-1588 solutions. The EVB-LAN8814 allows users to gain understanding of the product, develop working proof-of-concepts using the LAN8814 hardware and software driver, and thus accelerating integration of the LAN8814 into their end-product.

Package Contents:

EVB-LAN8814 Evaluation Board