



UnitedSiC offers the 3<sup>rd</sup> generation of high performance SiC Merged-PiN-Schottky (MPS) diodes. With zero reverse recovery charge and 175°C maximum junction temperature, these diodes are ideally suited for high frequency and high efficiency power systems with minimum

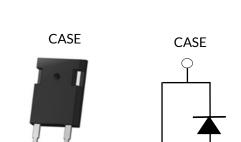
## 25A - 1700V SiC Schottky Diode

Rev. A, September 2020

Description

### DATASHEET

# JJ3D1725K2



1

2

#### Features

- Maximum operating temperature of 175°C
- Easy paralleling

cooling requirements.

- Extremely fast switching not dependent on temperature
- No reverse or forward recovery
- Enhanced surge current capability, MPS structure
- 100% UIS tested
- AEC-Q101 qualified

#### **Typical applications**

- Power converters
- Industrial motor drives
- Switch mode power supplies
- Power factor correction modules

Part Number	Package	Marking
UJ3D1725K2	TO-247-2L	UJ3D1725K2



1

2





#### Maximum Ratings

Parameter	Symbol	Test Conditions	Value	Units	
DC blocking voltage	V <sub>R</sub>		1700	V	
Repetitive peak reverse voltage, T <sub>J</sub> =25°C	V <sub>RRM</sub>		1700	V	
Surge peak reverse voltage	V <sub>RSM</sub>		1700	V	
Maximum DC forward current	I <sub>F</sub>	T <sub>C</sub> = 138°C	25	А	
Non-repetitive forward surge current sine halfwave	1	$T_{C} = 25^{\circ}C, t_{p} = 10ms$	180	180 A	
	I <sub>FSM</sub>	T <sub>C</sub> = 110°C, t <sub>p</sub> = 10ms	163		
Repetitive forward surge current		$T_{\rm C}$ = 25°C, $t_{\rm p}$ = 10ms	117	А	
sine halfwave, D=0.1	I <sub>FRM</sub>	T <sub>C</sub> = 110°C, t <sub>p</sub> = 10ms	68.7		
Non-repetitive peak forward current	I <sub>F,max</sub> —	T <sub>C</sub> = 25°C, t <sub>p</sub> = 10μs	1100		
		T <sub>C</sub> = 110°C, t <sub>p</sub> = 10μs	1100	A	
i <sup>2</sup> t value	∫i <sup>2</sup> dt —	$T_{c} = 25^{\circ}C, t_{p} = 10ms$	162	— A <sup>2</sup> s	
		$T_{\rm C} = 110^{\circ} {\rm C}, t_{\rm p} = 10 {\rm ms}$	133		
Power dissipation	P <sub>tot</sub> —	T <sub>C</sub> = 25°C	283		
		T <sub>C</sub> = 138°C	69.8	W	
Maximum junction temperature	T <sub>J,max</sub>		175	°C	
Operating and storage temperature	T <sub>J</sub> , T <sub>STG</sub>		-55 to 175	°C	
Soldering temperatures, wavesoldering only allowed at leads	T <sub>sold</sub>	1.6mm from case for 10s	260	°C	

#### **Thermal Characteristics**

Parameter	Symbol	Test Conditions	Value			Units
			Min	Тур	Max	Units
Thermal resistance, junction-to-case	$R_{\theta JC}$			0.41	0.53	°C/W



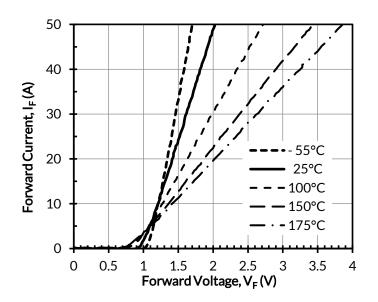
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#### Electrical Characteristics (T<sub>J</sub> = +25°C unless otherwise specified)

Parameter	Symbol	Test Conditions	Value			Linita
			Min	Тур	Max	Units
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 25A, T <sub>J</sub> =25°C	-	1.54	1.7	V
		I <sub>F</sub> = 25A, T <sub>J</sub> =150°C	-	2.1		
		I <sub>F</sub> = 25A, T <sub>J</sub> =175°C	-	2.3	2.75	
Reverse current	۱ <sub>R</sub>	V <sub>R</sub> =1700V, T <sub>J</sub> =25°C	-	24	360	μA
		V <sub>R</sub> =1700V, T <sub>J</sub> =175°C	-	950		
Total capacitive charge <sup>(1)</sup>	Q <sub>C</sub>	V <sub>R</sub> =1200V		184		nC
Total capacitance	с	$V_R$ =1V, f = 1MHz		1500		pF
		V <sub>R</sub> =800V, f = 1MHz		100		
		V <sub>R</sub> =1700V, f = 1MHz		80		
Capacitance stored energy	E <sub>C</sub>	V <sub>R</sub> =1200V		78		μJ

(1)  $Q_c$  is independent on  $T_J$ ,  $di_F/dt$ , and  $I_F$  as shown in the application note USCi\_AN0011.

#### **Typical Performance Diagrams**





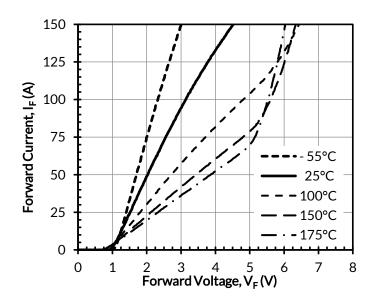


Figure 2. Typical forward characteristics in surge current





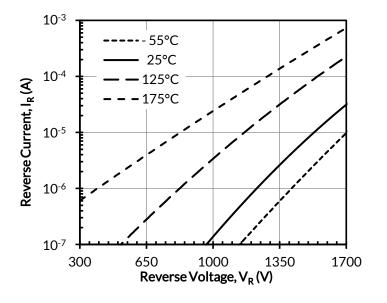


Figure 3. Typical reverse characteristics

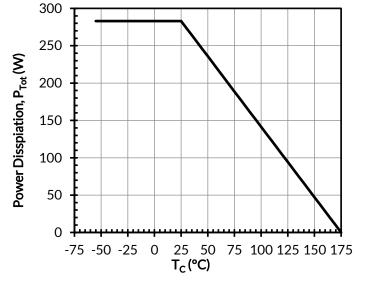


Figure 4. Power dissipation

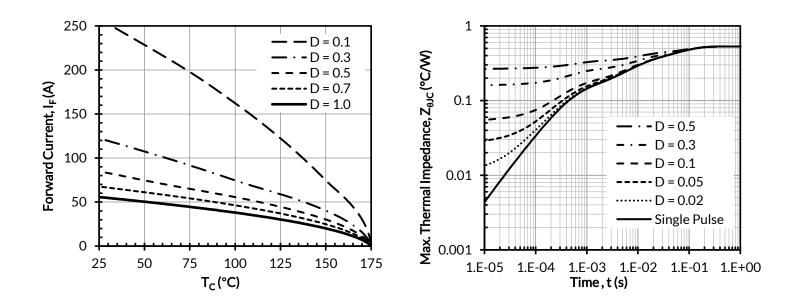


Figure 5. Diode forward current

Figure 6. Maximum transient thermal impedance





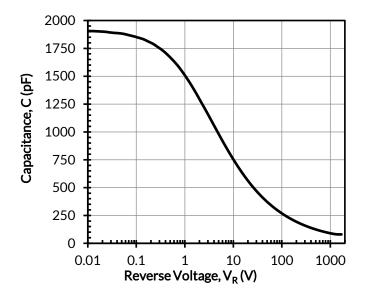


Figure 7. Capacitance vs. reverse voltage at 1MHz

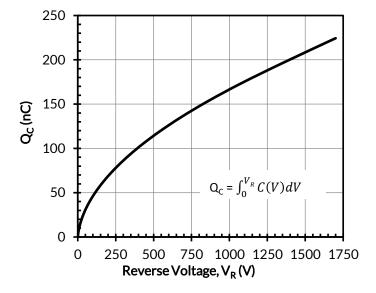


Figure 8. Typical capacitive charge vs. reverse voltage

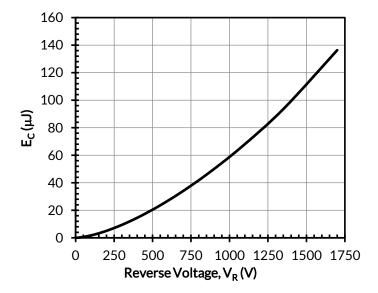


Figure 9. Typical capacitance stored energy vs. reverse voltage









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