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Vishay Siliconix

Automotive N-Channel 60 V (D-S) 175 °C MOSFET

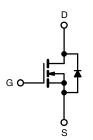


PRODUCT SUMMARY			
V _{DS} (V)	60		
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.005		
I _D (A)	166		
Configuration	Single		
Package	PowerPAK SO-8L		

FEATURES

- TrenchFET® Gen IV power MOSFET
- AEC-Q101 qualified
- 100 % Rq and UIS tested
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912





N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)						
PARAMETER		SYMBOL	LIMIT	UNIT		
Drain-source voltage		V_{DS}	60	V		
Gate-source voltage		V_{GS}	± 20	V		
Continuous drain current	T _C = 25 °C	- I _D	166			
	T _C = 125 °C		96			
Continuous source current (diode conduction)		Is	227	Α		
Pulsed drain current ^a		I _{DM}	238			
Single pulse avalanche current	L = 0.1 mH	I _{AS}	39			
Single pulse avalanche energy	L = 0.1 IIII	E _{AS}	76	mJ		
Maximum power dissipation	T _C = 25 °C	P_{D}	250	W		
	T _C = 125 °C	rD	83			
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +175	°C		
Soldering recommendations (peak temperature) ^c			260	C		

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-ambient	PCB mount b	R_{thJA}	42	°C/W	
Junction-to-case (drain)			0.6	C/VV	

Notes

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- b. When mounted on 1" square PCB (FR4 material)
- c. See solder profile (www.vishay.com/doc?73257). The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							•
Drain-source breakdown voltage	V_{DS}	$V_{GS} = 0$, $I_D = 250 \mu A$		60	-	-	V
Gate-source threshold voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$		2.0	2.5	V
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA
Zero gate voltage drain current		$V_{GS} = 0 V$	V _{DS} = 60 V	-	-	1	
	I _{DSS}	V _{GS} = 0 V	V _{DS} = 60 V, T _J = 125 °C	-	-	50	μΑ
		V _{GS} = 0 V	V _{DS} = 60 V, T _J = 175 °C	-	-	250	
On-state drain current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 \text{ V}$	30	-	-	Α
Drain-source on-state resistance ^a		V _{GS} = 10 V	I _D = 15 A	-	0.004	0.005	Ω
	R _{DS(on)}	V _{GS} = 10 V	I _D = 15 A, T _J = 125 °C	-	-	0.0072	
		V _{GS} = 10 V	I _D = 15 A, T _J = 175 °C	-	-	0.009	
Forward transconductance b	9 _{fs}	V _{DS} = 15 V, I _D = 10 A		-	26	-	S
Dynamic ^b							
Input capacitance	C _{iss}		V _{DS} = 25 V, f = 1 MHz	-	2810	3930	pF
Output capacitance	C _{oss}	$V_{GS} = 0 V$		-	1210	1700	
Reverse transfer capacitance	C _{rss}			-	42	60	
Total gate charge ^c	Qg		V _{DS} = 30 V, I _D = 10 A	-	34	51	nC
Gate-source charge ^c	Q _{gs}	V _{GS} = 10 V		-	11	-	
Gate-drain charge c	Q_{gd}			-	3	-	
Gate resistance	Rg	f = 1 MHz		0.5	1.1	1.7	Ω
Turn-on delay time ^c	t _{d(on)}				13	20	
Rise time ^c	t _r	V_{DD} = 30 V, R_L = 3.0 Ω $I_D \cong$ 10 A, V_{GEN} = 10 V, R_g = 1 Ω		-	3	5	ns
Turn-off delay time ^c	t _{d(off)}			-	22	33	
Fall time ^c	t _f			-	5	8	
Source-Drain Diode Ratings and Chara	acteristics ^b						•
Pulsed current ^a	I _{SM}			-	-	238	Α
Forward voltage	V _{SD}	I _F = 10 A, V _{GS} = 0 V		-	-	1.1	V
Body diode reverse recovery time	t _{rr}	I _F = 10 A, dl/dt = 100 A/μs		-	32	64	ns
Body diode reverse recovery charge	Q _{rr}			-	23	46	nC
Reverse recovery fall time	t _a			-	18	-	
Reverse recovery rise time	t _b			-	14	-	ns
Body diode peak reverse recovery current	I _{RM(REC)}			-	1.3	-	Α

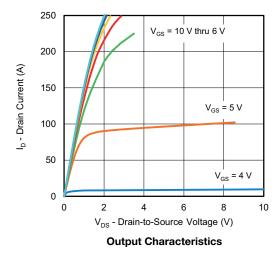
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%$
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

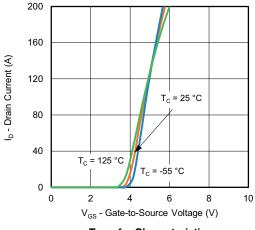
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



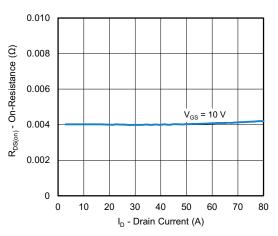
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



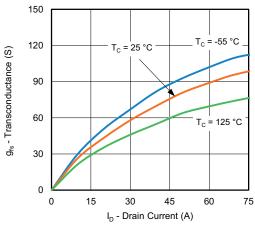




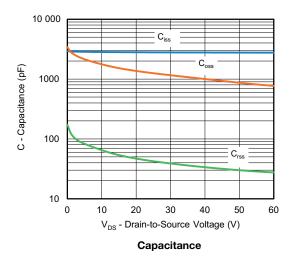
Transfer Characteristics

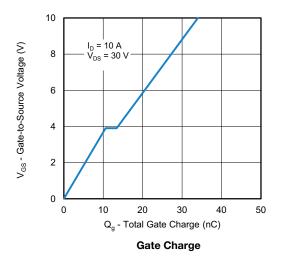


On-Resistance vs. Drain Current



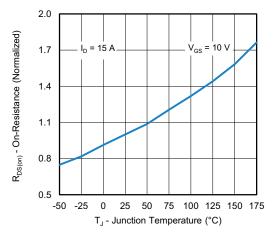
Transconductance



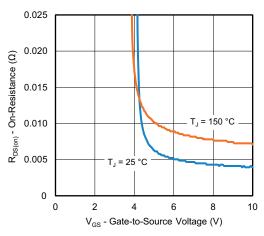




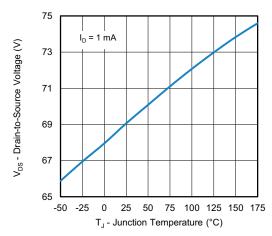
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



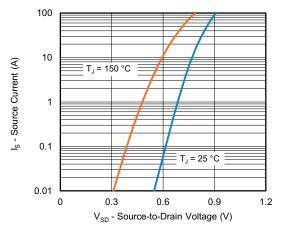
On-Resistance vs. Junction Temperature



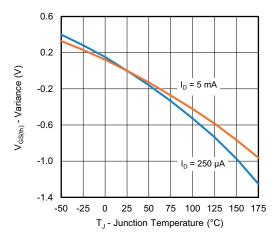
On-Resistance vs. Gate-to Source Voltage



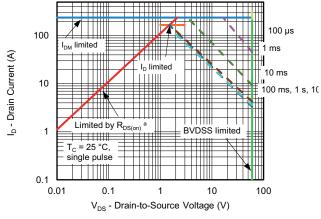
Drain Source Breakdown vs. Junction Temperature



Source Drain Diode Forward Voltage



Threshold Voltage



Safe Operating Area

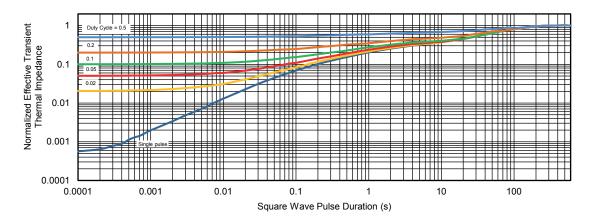
Note

a. V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

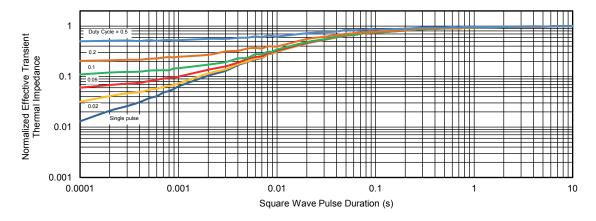
For technical questions, contact: automostech



TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

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