

SiC Schottky Barrier Diode

# TRS12V65H

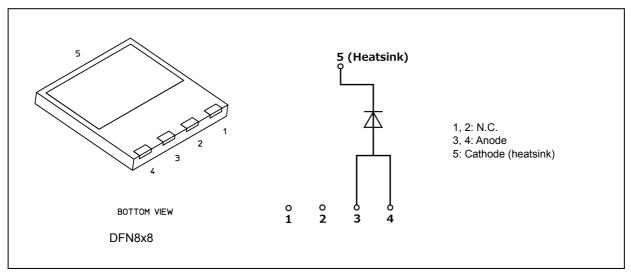
#### 1. Applications

- · Power Factor Correction
- · Solar Inverters
- · Uninterruptible Power Supplies
- · DC-DC Converters

#### 2. Features

- (1) Chip design of 3rd generation
- (2) Low forward voltage :  $V_F = 1.2 \text{ V (typ.)}$
- (3) Low total capacitive charge:  $Q_c = 33 \text{ nC}$  (typ.)
- (4) Low reverse current:  $I_R = 2.4 \mu A$  (typ.)

# 3. Packaging and Internal Circuit





# 4. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Note	Rating	Unit
Repetitive peak reverse voltage	$V_{RRM}$		650	V
Forward DC current	I <sub>F(DC)</sub>	(Note 1)	12	Α
		(Note 2)	30	
Non-repetitive peak forward surge current	I <sub>FSM</sub>	(Note 3)	60	Α
		(Note 4)	55	
		(Note 5)	640	
Power dissipation	P <sub>D</sub>	(Note 2)	88	W
Junction temperature	Tj		175	°C
Storage temperature	T <sub>stg</sub>		-55 to 175	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $T_C = 142 \,^{\circ}C$ Note 2:  $T_C = 25 \,^{\circ}C$ 

Note 3: f = 50 Hz (half-sine wave, t = 10 ms),  $T_c$  = 25 °C Note 4: f = 50 Hz (half-sine wave, t = 10 ms),  $T_c$  = 150 °C

Note 5: Square wave,  $t = 10 \mu s$ ,  $T_c = 25 ^{\circ}C$ 

#### 5. Thermal Characteristics

Characteristics	Symbol	Note	Max	Unit
Thermal resistance (junction-to-case)		(Note 1)	1.70	°C/W

Note 1:  $T_c = 25^{\circ}C$ 

### 6. Electrical Characteristics (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Forward voltage (pulse measurement)	V <sub>F</sub>	I <sub>F</sub> = 6 A	_	1.0	_	V
		I <sub>F</sub> = 12 A	_	1.2	1.35	
		I <sub>F</sub> = 12 A, T <sub>a</sub> = 150°C	_	1.36	_	
Reverse current (pulse measurement)	I <sub>R</sub>	V <sub>R</sub> = 650 V	_	2.4	120	μА
		V <sub>R</sub> = 650 V, T <sub>a</sub> = 150°C	_	21	_	
Total capacitance	Ct	V <sub>R</sub> = 1 V, f = 1 MHz	_	778	_	pF
		V <sub>R</sub> = 400 V, f = 1 MHz	_	46	_	
		V <sub>R</sub> = 650 V, f = 1 MHz	_	43	_	
Total capacitive charge	Q <sub>c</sub>	V <sub>R</sub> = 400 V, f = 1 MHz	_	33	_	nC



# 7. Marking

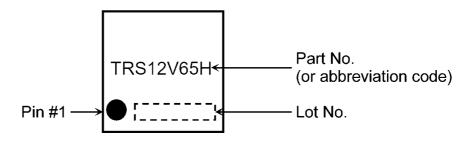


Fig. 7.1 Marking

# 8. Usage Considerations

For other design considerations, see the Toshiba website.



#### 9. Characteristics Curves (Note)

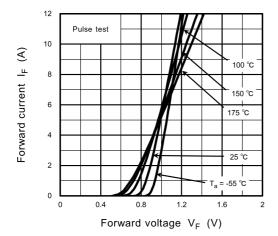


Fig. 9.1 I<sub>F</sub> - V<sub>F</sub>

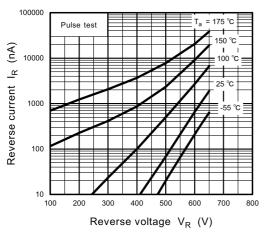


Fig. 9.3 I<sub>R</sub> - V<sub>R</sub>

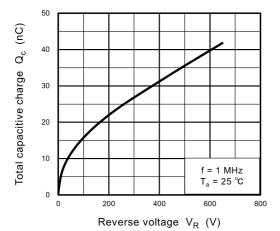


Fig. 9.5  $Q_c - V_R$ 

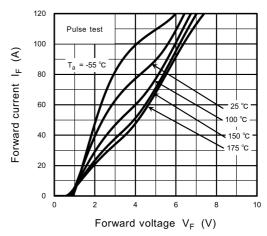


Fig. 9.2 I<sub>F</sub> - V<sub>F</sub>

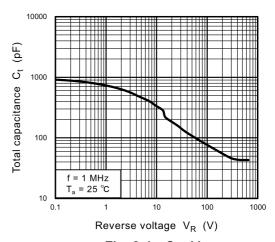


Fig. 9.4 Ct - VR



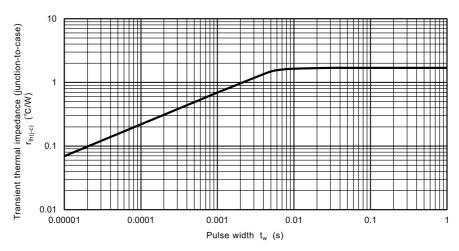
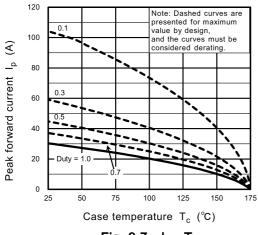
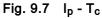


Fig. 9.6 r<sub>th(j-c)</sub> - t<sub>w</sub> (Guaranteed Maximum)





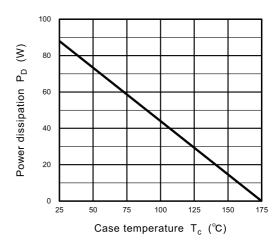


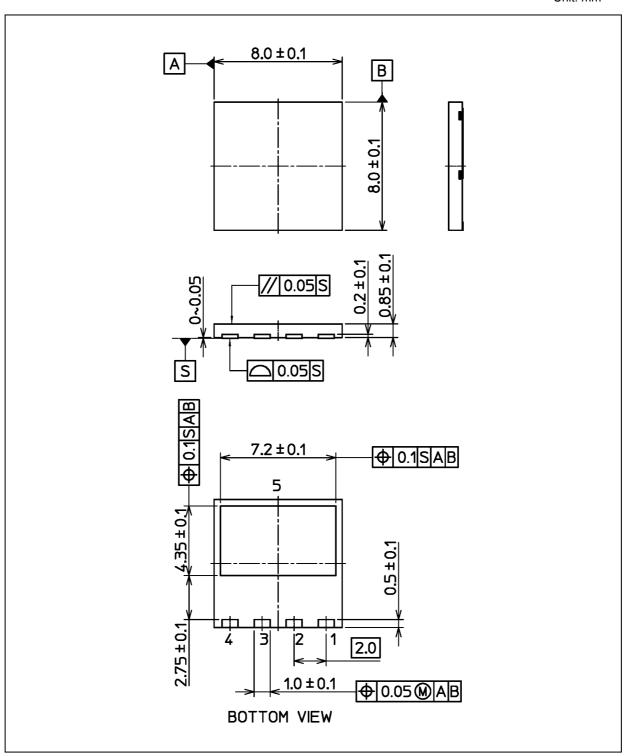
Fig. 9.8 P<sub>D</sub> - T<sub>c</sub> (Guaranteed Maximum)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



## **Package Dimensions**

Unit: mm



Weight: 0.175 g (typ.)

	Package Name(s)
TOSHIBA: 2-8T1A	
Nickname: DFN8x8	



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