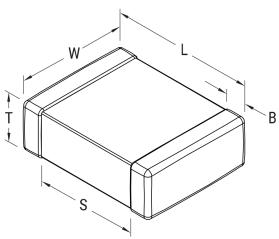


C0805H102J3GACT500

SMD Indust COG HT200C, Ceramic, 1000 pF, 5%, 25 VDC, COG, SMD, MLCC, High Temperature, Ultra-Stable, Low Loss, 0805



Click here for the 3D model.

| Dimensions | |
|------------|------------------|
| Chip Size | 0805 |
| L | 2mm +/-0.2mm |
| W | 1.25mm +/-0.2mm |
| Т | 0.78mm +/-0.10mm |
| S | 0.75mm MIN |
| В | 0.5mm +/-0.25mm |

| Packaging Specifications | |
|--------------------------|----------|
| Packaging | Cut Reel |
| Packaging Quantity | 500 |

| General Information | |
|---------------------|---|
| Series | SMD Indust COG HT200C |
| Style | SMD Chip |
| Description | SMD, MLCC, High Temperature, Ultra-Stable, Low Loss |
| Features | High Temp, Ultra-Stable, Low Loss |
| RoHS | Yes |
| Termination | Tin |
| Marking | No |
| AEC-Q200 | No |
| Component Weight | 11 mg |
| Shelf Life | 78 Weeks |
| MSL | 1 |

| Specifications | |
|--|------------------------------|
| Capacitance | 1000 pF |
| Measurement Condition | 1MHz 1.0Vrms |
| Capacitance Tolerance | 5% |
| Voltage DC | 25 VDC |
| Dielectric Withstanding Voltage | 62.5 VDC |
| Temperature Range | -55/+200°C |
| Temperature Coefficient | COG |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC) | 30 ppm/C, 1MegaHz 1.0Vrms |
| Dissipation Factor | 0.1% 1 MHz 1.0Vrms |
| Aging Rate | 0% Loss/Decade Hour |
| Insulation Resistance | 100 GOhms |

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.