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Product Termination Notification

Product Group: SIL/Wed Jun 14, 2023/PTN-SIL-031-2023-REV-0



Conversion to Copper (Cu) Wire – SQ4850EY

For further information, please contact your regional Vishay office.

CONTACT INFORMATION

Americas

Vishay Siliconix
2565 Junction Ave
-
San Jose CA United States 95134
Phone: 4089705799
Fax: 4089705799
business-americas@vishay.com

Europe

Vishay Electronic GmbH
Dr.-Felix-Zandman-Platz 1
-
Selb Germany 95100
Phone: 49-9287-71 0
Fax: 49-9287-70435
business-europe@vishay.com

Asia

Vishay Intertechnology Asia Pte. Ltd
37A Tampines Street 92 #07-01
-
Singapore Singapore 528886
Phone: 65 6788 6668
Fax: 65 6788 0988
business-asia@vishay.com

Description of Change: The affected part number listed in this notification will be converted to a Copper wire material set. The new ordering code is SQ4850CEY-T1_GE3, which has the exact same product performance and fit as SQ4850EY. There will be no change to the wafer fab or assembly location (Note: parts with _BE3 suffix will be consolidated to single assembly location in China). There will be no changes to the parameters on the datasheet (reference: SQ4850CEY Doc #62018 Rev.B).

Classification of Change: Standardization of materials

Expected Influence on Quality/Reliability/Performance: None

Part Numbers/Series/Families Affected: SQ4850EY-T1_GE3, SQ4850EY-T1_BE3,

Vishay Brand(S): Vishay Siliconix

Time Schedule:

Last Time Buy Date: Mon Dec 18, 2023

Last Time Ship Date: Mon Jun 17, 2024

Sample Availability: Qualified samples of replacement product are available on request

Product Identification: SQ4850CEY-T1_GE3

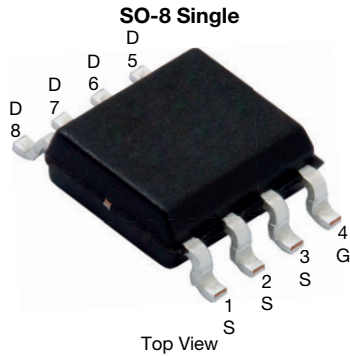
Qualification Data: AEC Q101 qualification data of replacement product is available. Qualification PPAP is available now

This PTN is considered approved, without further notification, unless we receive specific customer concerns before Mon Dec 18, 2023 or as specified by contract.

Issued By: Vishay Siliconix, business-americas@vishay.com



Automotive N-Channel 60 V (D-S) 175 °C MOSFET

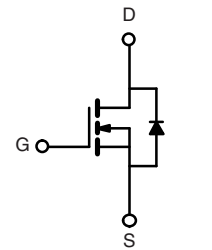


FEATURES

- TrenchFET® power MOSFET
- 100 % R_g and UIS tested
- AEC-Q101 qualified
- Material categorization:
for definitions of compliance please see
www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE



N-Channel MOSFET

PRODUCT SUMMARY

V_{DS} (V)	60
$R_{DS(on)}$ (Ω) at $V_{GS} = 10$ V	0.022
$R_{DS(on)}$ (Ω) at $V_{GS} = 4.5$ V	0.031
I_D (A)	12
Configuration	Single

ORDERING INFORMATION

Package	SO-8
Lead (Pb)-free and halogen-free	SQ4850CEY (for detailed order number please see www.vishay.com/doc?79771)

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-source voltage	V_{DS}	60	V
Gate-source voltage	V_{GS}	± 20	
Continuous drain current	I_D	$T_C = 25$ °C	A
		$T_C = 125$ °C	
Continuous source current (diode conduction)	I_S	6.2	
Pulsed drain current ^a	I_{DM}	48	
Single pulse avalanche current	I_{AS}	23	mJ
Single pulse avalanche energy			
	E_{AS}	26	
Maximum power dissipation	P_D	$T_C = 25$ °C	W
		$T_C = 125$ °C	
Operating junction and storage temperature range	T_J, T_{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-ambient	R_{thJA}	85	°C/W
Junction-to-foot (Drain)	R_{thJF}	22	

Notes

- a. Pulse test; pulse width ≤ 300 μ s, duty cycle ≤ 2 %
b. When mounted on 1" square PCB (FR4 material)



SPECIFICATIONS (T _C = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0, I _D = 250 μA		60	-	-	V
Gate-source threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA		1.5	2	2.5	
Gate-source leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V		-	-	± 100	nA
Zero gate voltage drain current	I _{DSS}	V _{GS} = 0 V	V _{DS} = 60 V	-	-	1	μA
		V _{GS} = 0 V	V _{DS} = 60 V, T _J = 125 °C	-	-	50	
		V _{GS} = 0 V	V _{DS} = 60 V, T _J = 175 °C	-	-	150	
On-state drain current ^a	I _{D(on)}	V _{GS} = 10 V	V _{DS} ≥ 5 V	30	-	-	A
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = 10 V	I _D = 6 A	-	0.018	0.022	Ω
		V _{GS} = 10 V	I _D = 6 A, T _J = 125 °C	-	-	0.037	
		V _{GS} = 10 V	I _D = 6 A, T _J = 175 °C	-	-	0.047	
		V _{GS} = 4.5 V	I _D = 5 A	-	0.025	0.031	
Forward transconductance ^b	g _{fs}	V _{DS} = 15 V, I _D = 6 A		-	21	-	S
Dynamic ^b							
Input capacitance	C _{iss}	V _{GS} = 0 V	V _{DS} = 25 V, f = 1 MHz	-	1100	1250	pF
Output capacitance	C _{oss}			-	171	235	
Reverse transfer capacitance	C _{rss}			-	77	95	
Total gate charge ^c	Q _g	V _{GS} = 10 V	V _{DS} = 30 V, I _D = 6 A	-	22.3	30	nC
Gate-source charge ^c	Q _{gs}			-	4.1	-	
Gate-drain charge ^c	Q _{gd}			-	5.1	-	
Gate resistance	R _g	f = 1 MHz		0.2	0.41	2.1	Ω
Turn-on delay time ^c	t _{d(on)}	V _{DD} = 30 V, R _L = 30 Ω I _D ≡ 1 A, V _{GEN} = 10 V, R _g = 1 Ω		-	10	11	ns
Rise time ^c	t _r			-	4	14	
Turn-off delay time ^c	t _{d(off)}			-	23	35	
Fall time ^c	t _f			-	11	14	
Source-Drain Diode Ratings and Characteristics ^b							
Pulsed current ^a	I _{SM}			-	-	48	A
Forward voltage	V _{SD}	I _F = 1.7 A, V _{GS} = 0 V		-	0.73	1.2	V
Body diode reverse recovery time	t _{rr}	I _F = 2.8 A, di/dt = 100 A/μs		-	20	40	ns
Body diode reverse recovery charge	Q _{rr}			-	19	38	nC
Reverse recovery fall time	t _a			-	15	-	ns
Reverse recovery rise time	t _b			-	5	-	
Body diode peak reverse recovery current	I _{RM(REC)}			-	-2	-	A

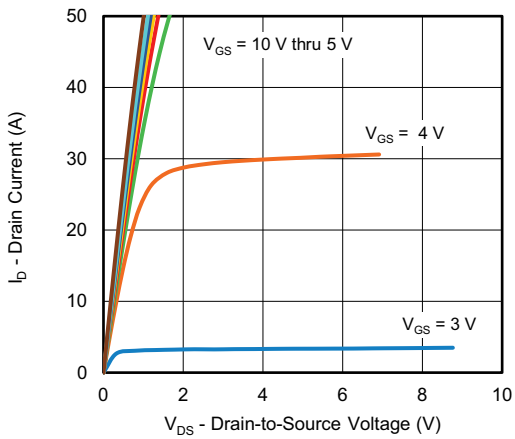
Notes

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$
b. Guaranteed by design, not subject to production testing
c. Independent of operating temperature

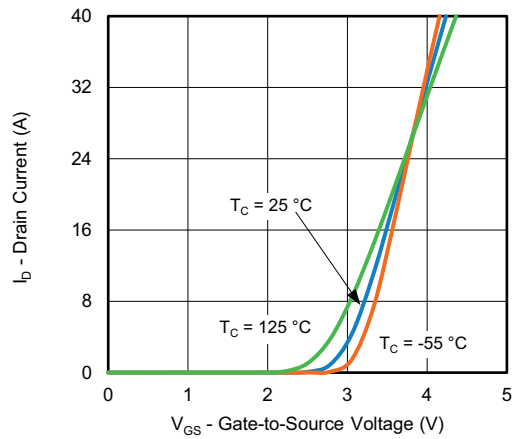
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



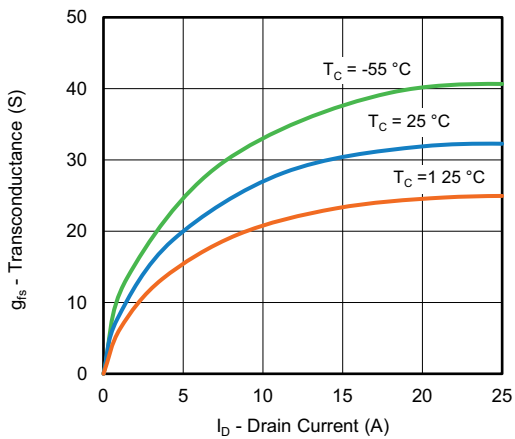
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



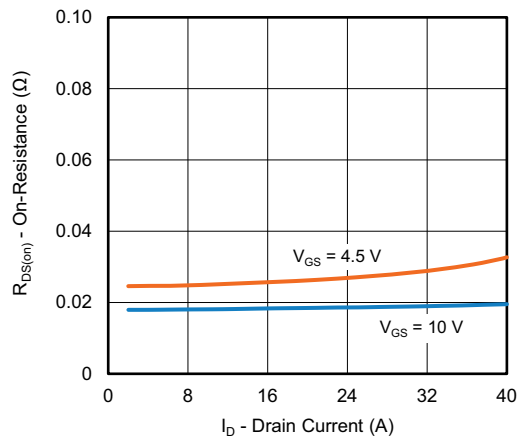
Output Characteristics



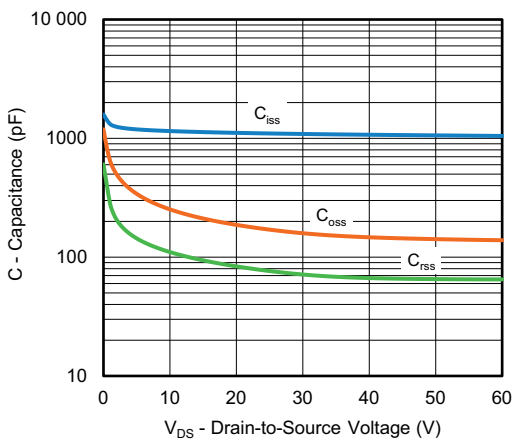
Transfer Characteristics



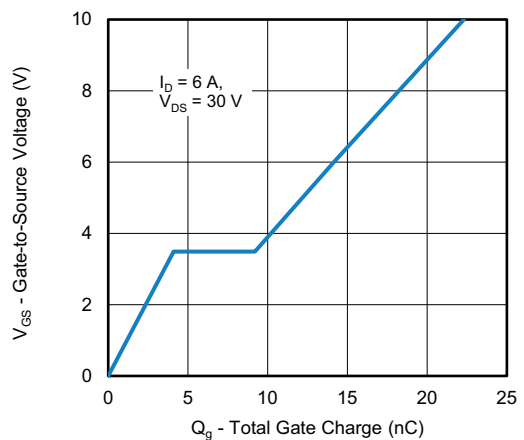
Transconductance



On-Resistance vs. Drain Current



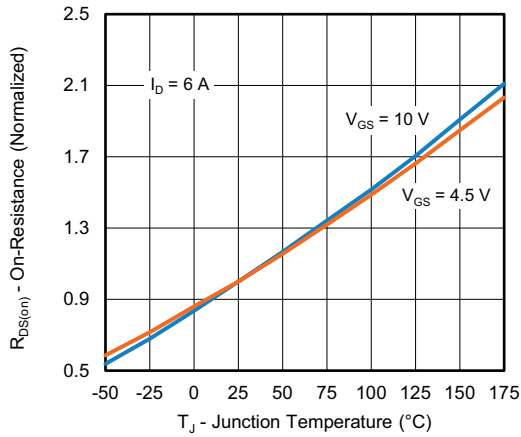
Capacitance



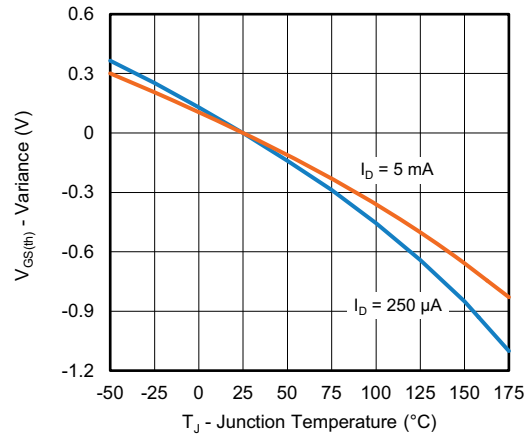
Gate Charge



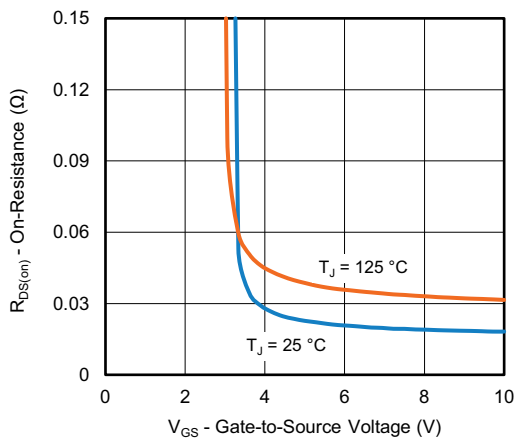
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



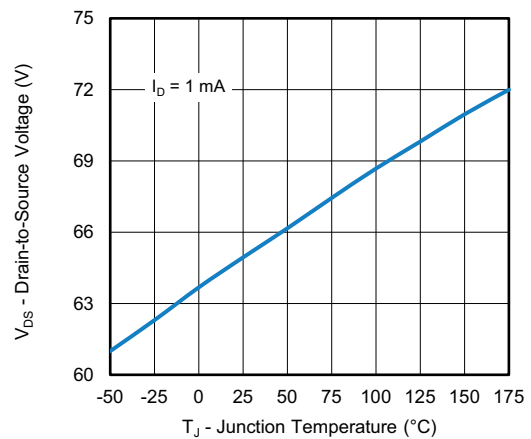
On-Resistance vs. Junction Temperature



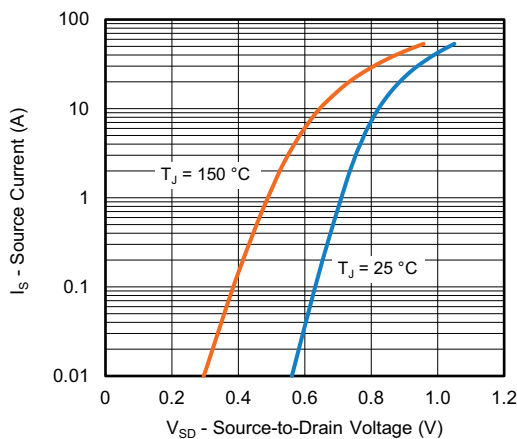
Threshold Voltage



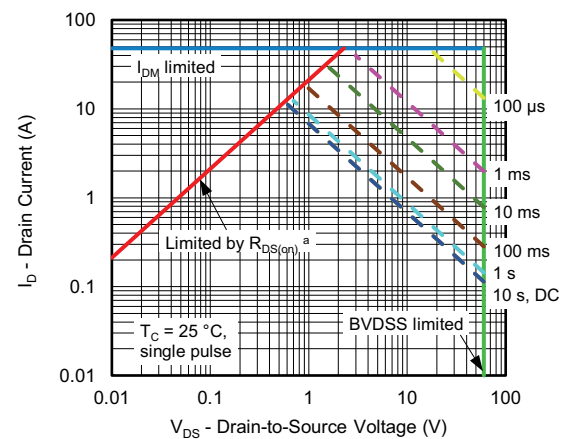
On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature



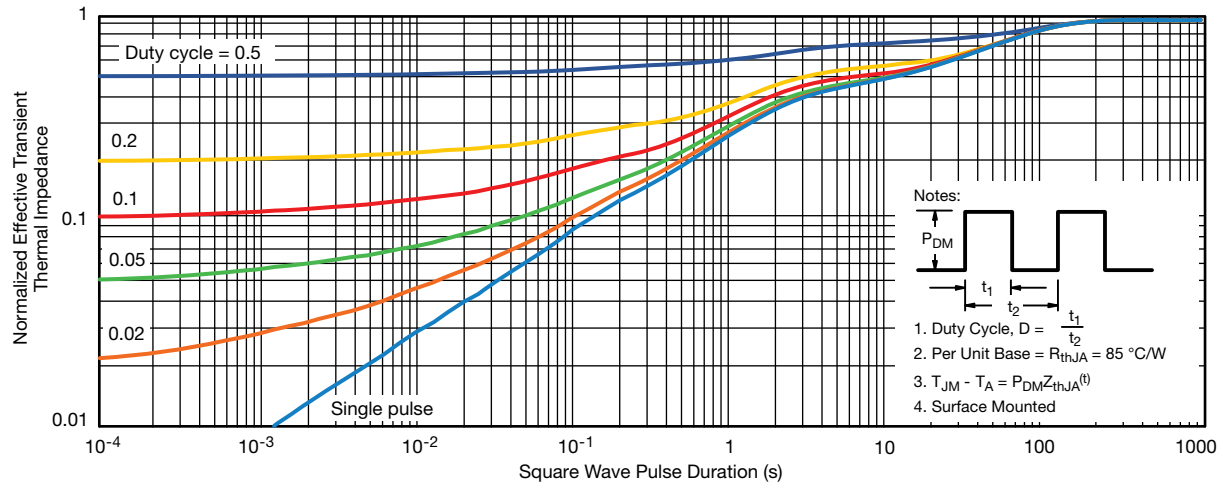
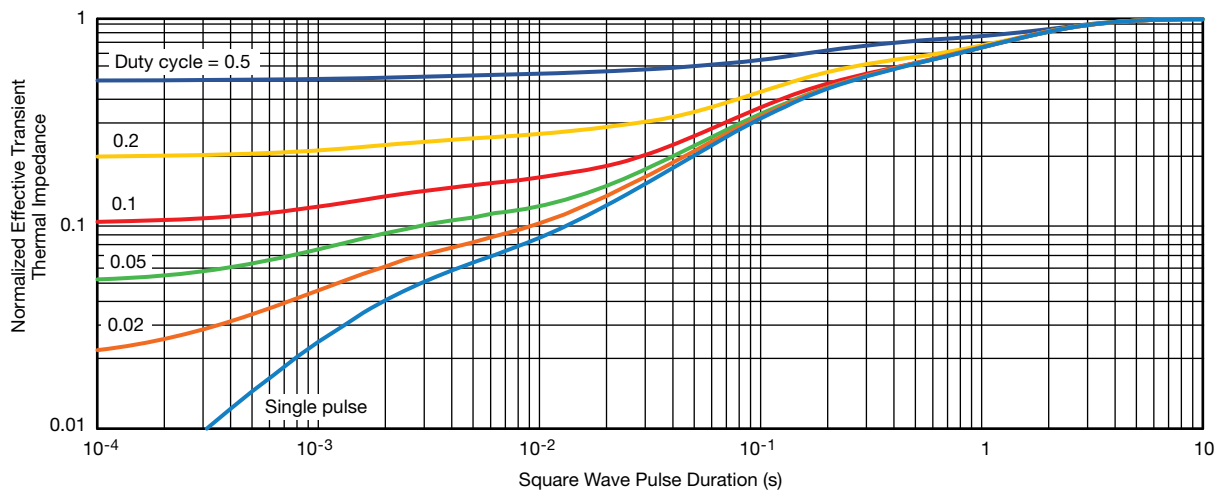
Source Drain Diode Forward Voltage



Safe Operating Area

Note

a. $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

**THERMAL RATINGS** ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)**Normalized Thermal Transient Impedance, Junction-to-Ambient****Normalized Thermal Transient Impedance, Junction-to-Foot****Note**

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient ($25\text{ }^{\circ}\text{C}$)
 - Normalized Transient Thermal Impedance Junction-to-Foot ($25\text{ }^{\circ}\text{C}$)
 are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?62018.