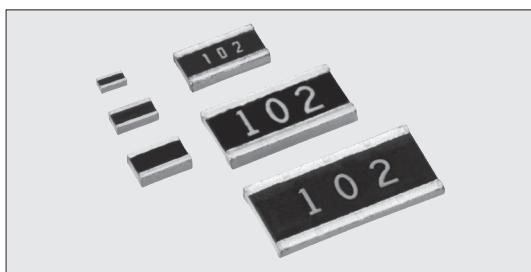


THICK FILM (WIDE TERMINAL TYPE)

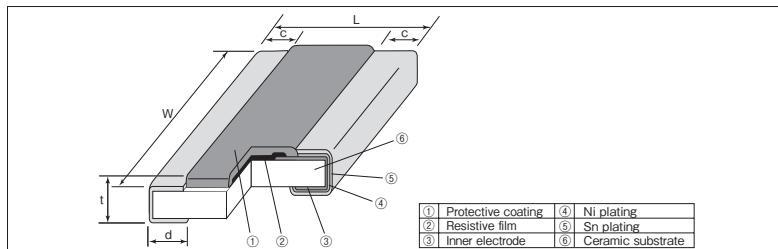


WK73R | Wide Terminal Type Flat Chip Resistors



Coating color : Black

■Construction



(1)	Protective coating	(4)	Ni plating
(2)	Resistive film	(5)	Sn plating
(3)	Inner electrode	(6)	Ceramic substrate

■Features

- Flat chip resistors of wide terminal type.
- High reliability and performance with T.C.R. $\pm 100 \times 10^{-6}/K$, resistance tolerance $\pm 0.5\%$.
- Suitable for both reflow and flow solderings.
- Products meet EU-RoHS requirements.
EU-RoHS regulation is not intended for Pb-glass contained in electrode, resistor element and glass.
- AEC-Q200 Tested.

■Applications

- Power supply, ECU etc.

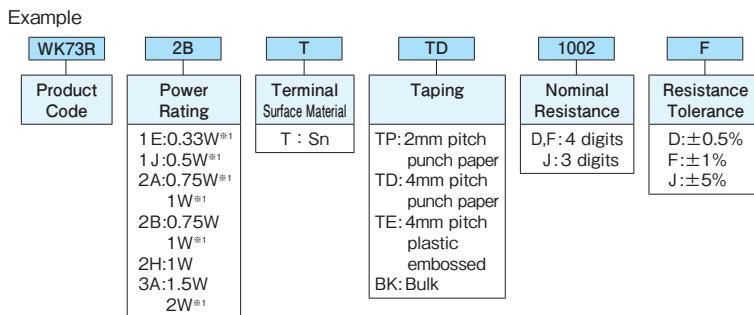
■Reference Standards

IEC 60115-8
JIS C 5201-8
EIAJ RC-2134C

■Dimensions

Type (Inch Size Code)	Dimensions (mm)					Weight(g) (1000pcs)
	L	W	c	d	t	
1E (0204)	0.5 \pm 0.05	1.0 \pm 0.05	0.15 \pm 0.05	0.15 \pm 0.05	0.35 \pm 0.05	0.65
1J (0306)	0.8 \pm 0.1	1.6 \pm 0.1	0.15 \pm 0.1	0.2 \pm 0.1	0.45 \pm 0.1	2.13
2A (0508)	1.25 \pm 0.15	2.0 \pm 0.15	0.3 \pm 0.2	0.35 \pm 0.2	0.55 \pm 0.1	4.93
2B (0612)	1.6 \pm 0.15	3.2 \pm 0.2	0.3 \pm 0.2	0.45 \pm 0.15		12.0
2H (1020)	2.5 \pm 0.15	5.0 \pm 0.15	0.4 \pm 0.2		0.75 \pm 0.15	30.2
3A (1225)	3.1 \pm 0.15	6.3 \pm 0.15	0.45 \pm 0.2			45.6

■Type Designation



Contact us when you have control request for environmental hazardous material other than the substance specified by EU-RoHS.

For further information on taping, please refer to APPENDIX C on the back pages.

■Ratings

Type	Power Rating	Rated Ambient Temp.	Rated Terminal Part Temp.	T.C.R. (x 10 ⁻⁶ /K)	Resistance Range (Ω)			Max. Working Voltage	Max. Overload Voltage	Taping & Q' ty/Reel (pcs)		
					D: $\pm 0.5\%$ E24 · E96	F: $\pm 1\%$ E24 · E96	J: $\pm 5\%$ E24			TP	TD	TE
WK73R1E	0.33W ^{※1}	70°C	125°C	± 100	—	10~1M	10~1M	75V	100V	10,000	—	—
WK73R1J	0.5W ^{※1}	70°C	125°C	± 100	—	10~1M	10~1M	150V	200V	—	5,000	—
WK73R2A	0.75W ^{※1}	70°C	125°C	± 100	—	20.5k~1M	22k~1M			—	5,000	—
	1W ^{※1}	70°C	125°C	± 100	—	10~20k	10~20k			—	5,000	—
WK73R2B	0.75W	70°C	125°C	± 100	10~1M	10~1M	10~1M			—	5,000	—
	1W ^{※1}	70°C	115°C	± 100	10~9.76k	10~9.76k	10~9.1k			—	5,000	—
WK73R2H	1W	70°C	125°C	± 100	—	10~430k	10~430k			—	—	4,000
				± 200	—	432k~1M	470k~1M			—	—	4,000
WK73R3A	1.5W	70°C	125°C	± 100	—	10~330k	10~330k			—	—	4,000
	2W ^{※1}	70°C	115°C	± 100	—	332k~1M	360k~1M			—	—	4,000
				± 200	—	332k~1M	360k~1M			—	—	4,000

Operating Temperature Range : $-55^{\circ}\text{C} \sim +155^{\circ}\text{C}$

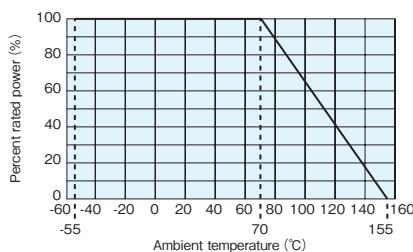
Rated voltage = $\sqrt{\text{Power Rating} \times \text{Resistance value or Max. working voltage}}$, whichever is lower.

※1 If you use at the rated power, please keep the condition that the terminal of the resistor is below the rated terminal part temperature. Please refer to the derating curves based on the terminal temperature of right side on the next page.

If any questions arise whether to use the "Rated Ambient Temperature" or the "Rated Terminal Part Temperature" in your usage conditions, please give priority to the "Rated Terminal Part Temperature". For more details, please refer to "Introduction of the derating curves based on the terminal part temperature" on the beginning of our catalog.

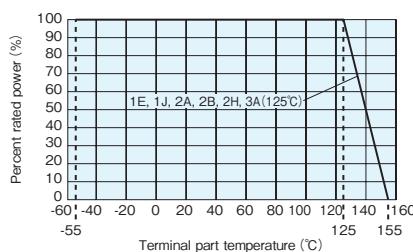
■ Derating Curve

Ambient temperature

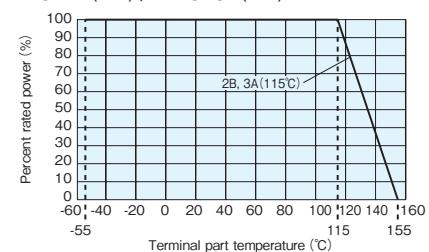


For resistors operated at an ambient temperature of 70°C or higher, the power shall be derated in accordance with the derating curve.

Terminal part temperature



Terminal part temperature
WK73R2B(1W), WK73R3A(2W)

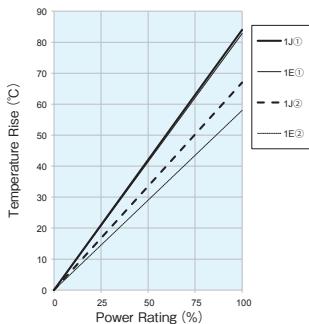


When the terminal part temperature of the resistor exceeds the rated terminal part temperature shown above, the power shall be derated according to the derating curve.

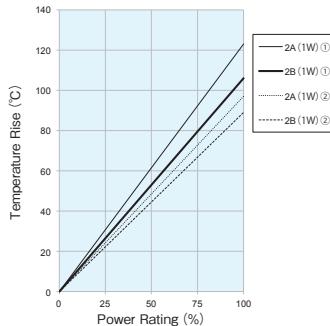
Please refer to "Introduction of the derating curves based on the terminal part temperature" on the beginning of our catalog before use.

■ Temperature Rise

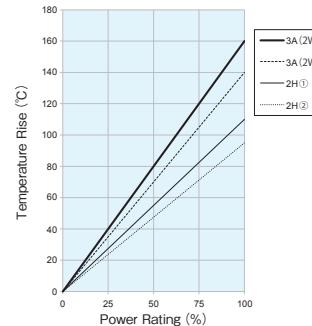
WK73R 1E-1J



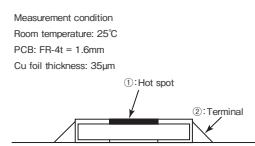
WK73R 2A-2B



WK73R 2H-3A

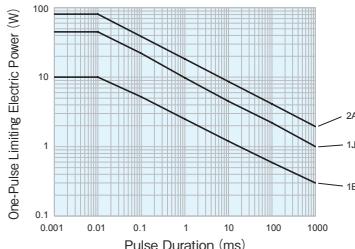


Regarding the temperature rise, the value of the temperature varies per conditions and board for use since the temperature is measured under our measuring conditions.

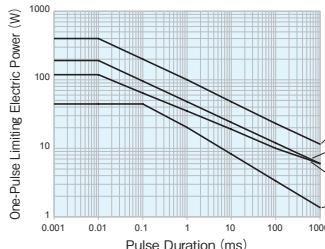


■ One-Pulse Limiting Electric Power

WK73R 1E-2A



WK73R 2B-3A



The maximum applicable voltage is equal to the max. overload voltage. Please ask us about the resistance characteristic of continuous applied pulse. The pulse endurance values are not assured values, so be sure to check the products on actual equipment when you use them.

■ Performance

Test Items	Performance Requirements $\Delta R \pm (\% + 0.005\Omega)$		Test Methods
	Limit	Typical	
Resistance	Within specified tolerance	—	25°C
T.C.R.	Within specified T.C.R	—	+25°C / -55°C and +25°C / +125°C
Overload (Short time)	2	0.2	Rated voltage $\times 2.5$ for 5s (WK73R1E(0.33W), WK73R2A(0.75W, 1W), WK73R2B(1W), WK73R3A(2W)) Rated voltage $\times 2.0$ for 5s
Resistance to soldering heat	1	0.2	260°C ± 5 °C, 10s ± 1 s
Bending test	1	0.1	Holding point 90mm, Bending 1time. Bending 5mm
Rapid change of temperature	2	1	-55°C (30min.) / +125°C (30min.) 1000 cycles
Moisture resistance	3 : 1E 2 : others	1 : 1E 0.2 : others	40°C ± 2 °C, 90%~95%RH, 1000h 1.5h ON/0.5h OFF cycle
Endurance at 70°C or rated terminal part temperature	3 : 1E 2 : others	1 : 1E 0.2 : others	70°C ± 2 °C or rated terminal part temperature ± 2 °C 1000h 1.5h ON/0.5h OFF cycle
High temperature exposure	1	0.2	+155°C, 1000h

■ Precautions for Use

- The substrate of chip resistors is alumina. Cracks may occur at the connection of solder (solder fillet portion) due to the difference of the coefficient of thermal expansion from a mounting board when heat stress like heat cycle, etc. are repeatedly given to them. Care should be taken to the occurrence of the cracks when the change in ambient temperature or ON/OFF of load is repeated, especially when WK73 series which have self-heating. The occurrence of the crack by heat stress may be influenced by the size of a pad, solder volume, heat radiation of mounting board etc., so please pay careful attention to designing when a big change in ambient temperature and conditions for use like ON/OFF of load can be assumed.