

CMOS Digital Integrated Circuits Silicon Monolithic

## TC7WZ07FU

#### 1. Functional Description

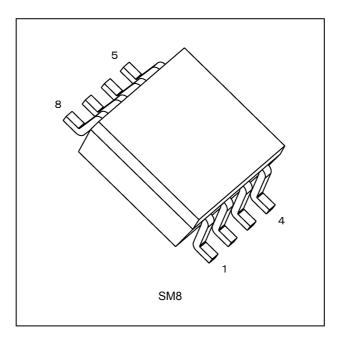
• Triple Non-Inverter (Open Drain)

## 2. Features

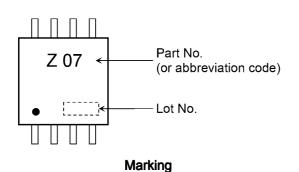
- (1) Wide operating temperature range:  $T_{opr} = -40$  to 125 °C (Note 1)
- (2) High output current:  $\pm 24$  mA (min) at  $V_{CC} = 3.0$  V
- (3) Super high speed operation:  $t_{PZL} = 2.3$  ns (typ.) at  $V_{CC} = 5.0$  V,  $C_L = 50$  pF
- (4) Operation voltage range:  $V_{CC} = 1.65$  to 5.5 V
- (5) 5.5 V tolerant inputs
- (6) 5.5 V power down protection output
- (7) Matches the performance of TC74LCX series when operated at  $3.3~V~V_{\rm CC}$

Note 1: For devices with the ordering part number ending in J(CT.  $T_{opr}$  = -40 to 85 °C for the other devices.

## 3. Packaging



#### 4. Marking and Pin Assignment



2A 3 6 3A GND 4 5 2Y

Pin Assignment (Top view)

Start of commercial production

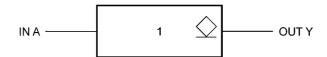
2020-01

Vcc

1Y



## 5. IEC Logic Symbol



#### 6. Truth Table

А	Y
L	L
Н	Z

Z: High impedance

## 7. Absolute Maximum Ratings (Note) (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V <sub>CC</sub>		-0.5 to 6.0	V
Input voltage	V <sub>IN</sub>		-0.5 to 6.0	٧
DC output voltage	V <sub>OUT</sub>	(Note 1)	-0.5 to 6.0	٧
Input diode current	I <sub>IK</sub>		-20	mA
Output diode current	lok	(Note 2)	-20	mA
DC output current	I <sub>OUT</sub>		50	mA
V <sub>CC</sub> /ground current	I <sub>CC</sub>		±50	mA
Power dissipation	$P_D$		300	mW
Storage temperature	T <sub>stg</sub>		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: I<sub>OUT</sub> absolute maximum rating must be observed.

Note 2: V<sub>OUT</sub> < GND

## 8. Operating Ranges (Note)

Characteristics	Symbol	Note	Test Condition	Rating	Unit
Supply voltage	V <sub>CC</sub>		_	1.65 to 5.5	V
		(Note 1)	_	1.5 to 5.5	
Input voltage	V <sub>IN</sub>		_	0 to 5.5	V
Output voltage	V <sub>OUT</sub>		_	0 to 5.5	V
Operating temperature	T <sub>opr</sub>	(Note 2)	_	-40 to 125	°C
		(Note 3)	_	-40 to 85	
Input rise and fall time	dt/dv		$V_{CC}$ = 1.8 ± 0.15 V, 2.5 ± 0.2 V	0 to 20	ns/V
			V <sub>CC</sub> = 3.3 ± 0.3 V	0 to 10	
			$V_{CC} = 5.0 \pm 0.5 \text{ V}$	0 to 5	

Note: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either V<sub>CC</sub> or GND.

Note 1: Data retention only

Note 2: For devices with the ordering part number ending in J(CT.

Note 3: For devices except those with the ordering part number ending in J(CT.



#### 9. Electrical Characteristics

## 9.1. DC Characteristics (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Тур.	Max	Unit
High-level input voltage	V <sub>IH</sub>	_		1.65 to 1.95	V <sub>CC</sub> × 0.75	_	_	V
				2.3 to 5.5	V <sub>CC</sub> × 0.7	_	_	
Low-level input voltage	V <sub>IL</sub>	_		1.65 to 1.95		l	V <sub>CC</sub> × 0.25	V
				2.3 to 5.5	_	ı	$V_{CC} \times 0.3$	
Low-level output voltage	$V_{OL}$	$V_{IN} = V_{IL}$	I <sub>OL</sub> = 100 μA	1.65	_	0.0	0.1	V
				2.3	_	0.0	0.1	
				3.0	_	0.0	0.1	
				4.5	_	0.0	0.1	
			I <sub>OL</sub> = 4 mA	1.65	_	0.08	0.24	
			I <sub>OL</sub> = 8 mA	2.3	_	0.1	0.3	
			I <sub>OL</sub> = 16 mA	3.0	_	0.15	0.4	
			I <sub>OL</sub> = 24 mA	3.0	_	0.22	0.55	
			I <sub>OL</sub> = 32 mA	4.5	_	0.22	0.55	
3-state output OFF-state leakage current	I <sub>OZ</sub>	$V_{IN} = V_{IH}$ $V_{OUT} = V_{CC}$ or GND		5.5	_		±5.0	μА
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5	_	_	±1.0	μΑ
Power-OFF leakage current	I <sub>OFF</sub>	V <sub>IN</sub> or V <sub>OUT</sub> = 5.5 V		0		_	1	μА
Quiescent supply current	Icc	V <sub>IN</sub> = 5.5 V or GND		1.65 to 5.5		-	1	μΑ

## 9.2. DC Characteristics (Unless otherwise specified, T<sub>a</sub> = -40 to 85 °C)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit	
High-level input voltage	V <sub>IH</sub>	_		1.65 to 1.95	V <sub>CC</sub> × 0.75	_	V
				2.3 to 5.5	$V_{CC} \times 0.7$	_	
Low-level input voltage	V <sub>IL</sub>	_		1.65 to 1.95	_	V <sub>CC</sub> × 0.25	V
				2.3 to 5.5	_	$V_{CC} \times 0.3$	
Low-level output voltage	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IL</sub>	I <sub>OL</sub> = 100 μA	1.65	_	0.1	V
				2.3	_	0.1	
				3.0	_	0.1	
				4.5	_	0.1	
			I <sub>OL</sub> = 4 mA	1.65	_	0.24	
			I <sub>OL</sub> = 8 mA	2.3	_	0.3	
			I <sub>OL</sub> = 16 mA	3.0	_	0.4	
			I <sub>OL</sub> = 24 mA	3.0	_	0.55	
			I <sub>OL</sub> = 32 mA	4.5	_	0.55	
3-state output OFF-state leakage current	I <sub>OZ</sub>	$V_{IN} = V_{IH}$ $V_{OUT} = V_{CC}$ or GND		5.5	_	±10.0	μА
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5	_	±10.0	μА
Power-OFF leakage current	I <sub>OFF</sub>	V <sub>IN</sub> or V <sub>OUT</sub> = 5.5 V		0	_	10	μΑ
Quiescent supply current	I <sub>CC</sub>	V <sub>IN</sub> = 5.5 V or GND		1.65 to 5.5	_	10	μΑ



## 9.3. DC Characteristics (Note) (Unless otherwise specified, Ta = -40 to 125 °C)

Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Max	Unit
High-level input voltage	V <sub>IH</sub>	_		1.65 to 1.95	V <sub>CC</sub> × 0.75	_	V
				2.3 to 5.5	$V_{CC} \times 0.7$	_	
Low-level input voltage	V <sub>IL</sub>	_		1.65 to 1.95	_	V <sub>CC</sub> × 0.25	V
				2.3 to 5.5	_	V <sub>CC</sub> × 0.3	
Low-level output voltage	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IL</sub>	I <sub>OL</sub> = 100 μA	1.65	_	0.1	V
				2.3	_	0.1	
				3.0	_	0.1	
				4.5	_	0.1	
			I <sub>OL</sub> = 4 mA	1.65	_	0.7	
			I <sub>OL</sub> = 8 mA	2.3	_	0.45	
			I <sub>OL</sub> = 16 mA	3.0	_	0.6	
			I <sub>OL</sub> = 24 mA	3.0	_	0.8	
			I <sub>OL</sub> = 32 mA	4.5	_	0.8	
3-state output OFF-state leakage current	I <sub>OZ</sub>	$V_{IN} = V_{IH}$ $V_{OUT} = V_{CC}$ or GND		5.5	_	±20.0	μА
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5	_	±20.0	μΑ
Power-OFF leakage current	I <sub>OFF</sub>	V <sub>IN</sub> or V <sub>OUT</sub> = 5.5 V		0		100	μΑ
Quiescent supply current	I <sub>CC</sub>	V <sub>IN</sub> = 5.5 V or GND		1.65 to 5.5	_	100	μА

Note: For devices with the ordering part number ending in J(CT.

## 9.4. AC Characteristics (Unless otherwise specified, $T_a = 25$ °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Note	Test Condition	V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Тур.	Max	Unit
Propagation delay time	t <sub>PZL</sub>		$R_L$ = 500 $\Omega$	1.8 ± 0.15	50	1.8	5.5	9.5	ns
				2.5 ± 0.2		1.2	3.7	5.8	
				$3.3\pm0.3$		0.8	2.9	4.4	
				5.0 ± 0.5		0.5	2.3	3.5	
	t <sub>PLZ</sub>		$R_L$ = 500 $\Omega$	$1.8 \pm 0.15$	50	1.8	4.3	9.5	ns
				$2.5 \pm 0.2$		1.2	2.8	5.8	
				$3.3 \pm 0.3$		0.8	2.1	4.4	
				5.0 ± 0.5		0.5	1.4	3.5	
Input capacitance	C <sub>IN</sub>		_	0 to 5.5	_	_	3	_	pF
Output capacitance	C <sub>OUT</sub>		_	0 to 5.5	_	1	3		pF
Power dissipation	C <sub>PD</sub>	(Note 1)	_	3.3	_		5		pF
capacitance				5.5		_	8	_	

Note 1:  $C_{PD}$  is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.  $I_{CC(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$  (per 1 gate)



# 9.5. AC Characteristics (Unless otherwise specified, $T_a$ = -40 to 85 °C, Input: $t_r$ = $t_f$ = 3 ns)

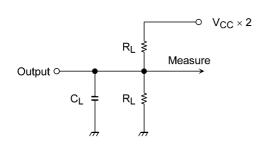
Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Max	Unit
Propagation delay time	t <sub>PZL</sub>	$R_L$ = 500 $\Omega$	1.8 ± 0.15	50	1.8	10.5	ns
			$2.5 \pm 0.2$		1.2	6.4	
			$3.3 \pm 0.3$		0.8	4.8	
			$5.0 \pm 0.5$		0.5	3.9	
	t <sub>PLZ</sub>	$R_L$ = 500 $\Omega$	$1.8 \pm 0.15$	50	1.8	10.5	ns
			$2.5 \pm 0.2$		1.2	6.4	
			$3.3 \pm 0.3$		0.8	4.8	
			$5.0 \pm 0.5$		0.5	3.9	

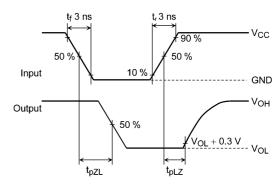
## 9.6. AC Characteristics (Note) (Unless otherwise specified, $T_a$ = -40 to 125 °C, Input: $t_r$ = $t_f$ = 3 ns)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Max	Unit
Propagation delay time	t <sub>PZL</sub>	$R_L$ = 500 $\Omega$	$1.8 \pm 0.15$	50	1.8	12.0	ns
			$2.5 \pm 0.2$		1.2	7.5	
			$3.3 \pm 0.3$		0.8	5.5	
			$5.0 \pm 0.5$		0.5	4.5	
	t <sub>PLZ</sub>	R <sub>L</sub> = 500 Ω	1.8 ± 0.15	50	1.8	12.0	ns
			$2.5 \pm 0.2$		1.2	7.5	
			$3.3 \pm 0.3$		0.8	5.5	
			5.0 ± 0.5		0.5	4.5	

Note: For devices with the ordering part number ending in J(CT.

## 9.7. AC Characteristics Measurement Circuit and AC Waveform





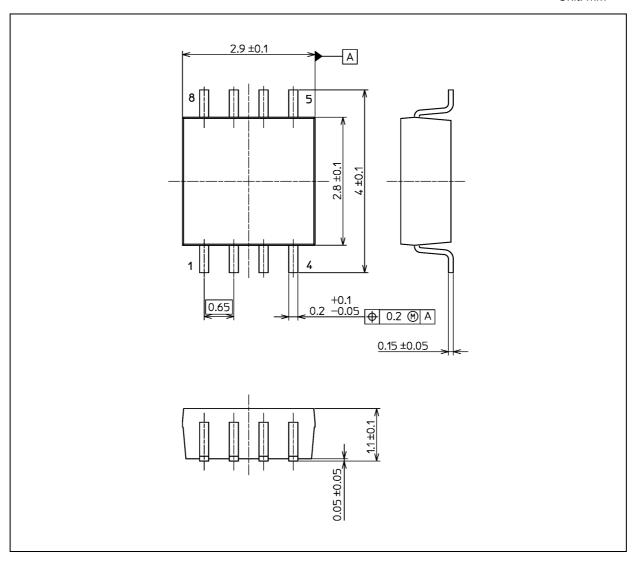
**AC Characteristics Measurement Circuit** 

**AC Waveform** 



## **Package Dimensions**

Unit: mm



Weight: 21 mg (typ.)

	Package Name(s)
JEDEC: SOT-505	
Nickname: SM8	



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