

NP20P06YLG

MOS FIELD EFFECT TRANSISTOR

R07DS0706EJ0100 Rev.1.00 Apr 17, 2012

Description

The NP20P06YLG is P-channel MOS Field Effect Transistor designed for high current switching applications.

Features

• Low on-state resistance

 $R_{DS(on)} = 47 \text{ m}\Omega \text{ MAX.} (V_{GS} = -10 \text{ V}, I_D = -10 \text{ A})$

 $R_{DS(on)} = 64 \text{ m}\Omega \text{ MAX.} (V_{GS} = -5 \text{ V}, I_D = -10 \text{ A})$

 $R_{\rm DS(on)} = 70 \ m\Omega \ MAX. \ (V_{\rm GS} = -4.5 \ V, \ I_{\rm D} = -10 \ A)$

- Logic level drive type
- Gate to Source ESD protection diode built in
- Designed for automotive application and AEC-Q101 qualified

Ordering Information

Part No.	Lead Plating	Pac	Package	
NP20P06YLG-E1-AY *1	Pure Sn (Tin)	Tape 2500 p/reel	Taping (E1 type)	8-pin HSON
NP20P06YLG-E2-AY *1			Taping (E2 type)	

Note: *1 Pb-free (This product does not contain Pb in the external electrode)

Absolute Maximum Ratings $(T_A = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to Source Voltage (V _{GS} = 0 V)	V_{DSS}	-60	V
Gate to Source Voltage (V _{DS} = 0 V)	V_{GSS}	∓20	V
Drain Current (DC) (T _C = 25°C)	I _{D(DC)}	∓20	А
Drain Current (pulse) *1	I _{D(pulse)}	∓60	А
Total Power Dissipation (T _C = 25°C)	P _{T1}	57	W
Total Power Dissipation (T _A = 25°C) *2	P _{T2}	1.0	W
Channel Temperature	T _{ch}	175	°C
Storage Temperature	T _{stg}	−55 to +175	°C
Single Avalanche Current *3	I _{AS}	17	A
Single Avalanche Energy *3	E _{AS}	29	mJ

Thermal Resistance

Notes: *1 $T_C = 25$ °C, $P_W \le 10 \mu s$, Duty Cycle ≤ 1 %

*2 Mounted on glass epoxy substrate of 40 mm \times 40 mm \times 1.6 mmt with 4% copper area (35 $\mu\text{m})$

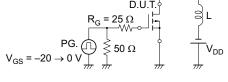
*3 $T_{ch(start)}$ = 25°C, V_{DD} = -30 V, R_G = 25 Ω , L = 100 μ H, V_{GS} = -20 V \rightarrow 0 V

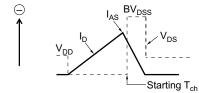
Electrical Characteristics (T_A = 25°C)

Item	Symbol	MIN.	TYP.	MAX.	Unit	Test Conditions
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-1	μΑ	$V_{DS} = -60 \text{ V}, V_{GS} = 0 \text{ V}$
Gate Leakage Current	I _{GSS}	_	_	∓10	μΑ	$V_{GS} = \mp 20 \text{ V}, V_{DS} = 0 \text{ V}$
Gate to Source Threshold Voltage	V _{GS(th)}	-1.0	-1.7	-2.5	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Forward Transfer Admittance *1	y _{fs}	9	20	_	S	$V_{DS} = -5 \text{ V}, I_{D} = -10 \text{ A}$
Drain to Source On-state Resistance *1	R _{DS(on)1}	_	37	47	mΩ	$V_{GS} = -10 \text{ V}, I_D = -10 \text{ A}$
	R _{DS(on)2}	_	41	64	mΩ	$V_{GS} = -5 \text{ V}, I_{D} = -10 \text{ A}$
	R _{DS(on)3}	_	43	70	mΩ	$V_{GS} = -4.5 \text{ V}, I_D = -10 \text{ A}$
Input Capacitance	C _{iss}	_	1605	2407	pF	V _{DS} = -25 V
Output Capacitance	Coss	_	150	225	pF	$V_{GS} = 0 V$
Reverse Transfer Capacitance	C _{rss}	_	96	173	pF	f = 1 MHz
Turn-on Delay Time	t _{d(on)}	_	8	16	ns	$V_{DD} = -30 \text{ V}, I_D = -10 \text{ A}$
Rise Time	t _r	_	8	20	ns	$V_{GS} = -10 \text{ V}$
Turn-off Delay Time	t _{d(off)}	_	160	320	ns	$R_G = 0 \Omega$
Fall Time	t _f	_	80	200	ns	
Total Gate Charge	Q_G	_	34	51	nC	$V_{DD} = -48 \text{ V}$
Gate to Source Charge	Q _{GS}	_	4	_	nC	$V_{GS} = -10 \text{ V}$
Gate to Drain Charge	Q_{GD}	_	9	_	nC	$I_D = -20 \text{ A}$
Body Diode Forward Voltage *1	$V_{F(S-D)}$	_	0.95	1.5	V	I _F = 20 A, V _{GS} = 0 V
Reverse Recovery Time	t _{rr}	_	38	_	ns	I _F = 20 A, V _{GS} = 0 V
Reverse Recovery Charge	Q _{rr}	_	50	_	nC	di/dt = 100 A/μs

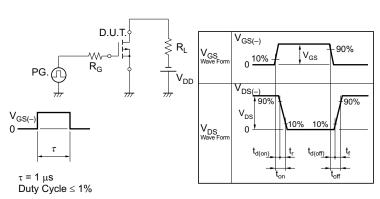
Note: *1 Pulsed test

TEST CIRCUIT 1 AVALANCHE CAPABILITY





TEST CIRCUIT 2 SWITCHING TIME

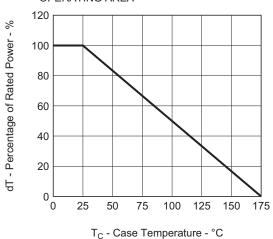


TEST CIRCUIT 3 GATE CHARGE

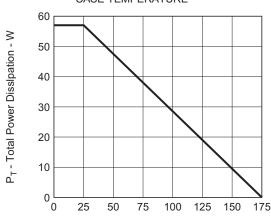
$$\begin{array}{c|c} \text{D.U.T.} \\ \text{I}_{G} = -2 \text{ mA} \\ \text{W} \\ \end{array} \begin{array}{c} \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \\ \\$$

Typical Characteristics $(T_A = 25^{\circ}C)$

DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA

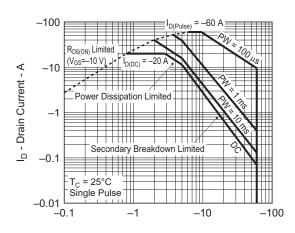


TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



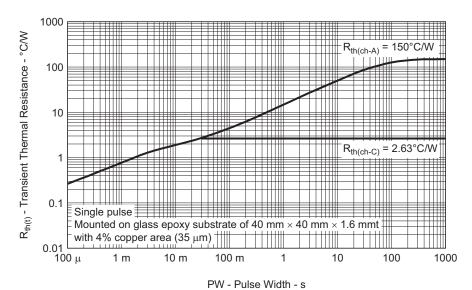
T_C - Case Temperature - °C

FORWARD BIAS SAFE OPERATING AREA

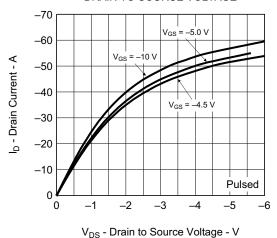


 V_{DS} - Drain to Source Voltage - V

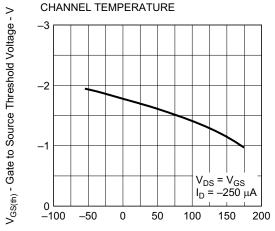
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE

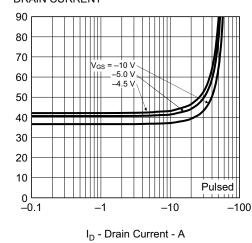


GATE TO SOURCE THRESHOLD VOLTAGE vs.

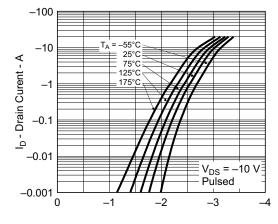


T_{ch} - Channel Temperature - °C

DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

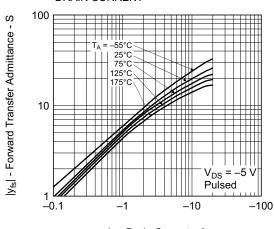


FORWARD TRANSFER CHARACTERISTICS



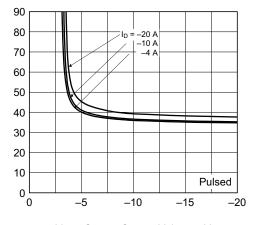
V_{GS} - Gate to Source Voltage - V

FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



I_D - Drain Current - A

DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



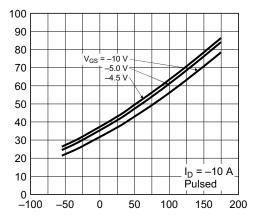
V_{GS} - Gate to Source Voltage - V

 $R_{DS(on)}$ - Drain to Source On-State Resistance - $m\Omega$

 $R_{\text{DS(on)}}$ - Drain to Source On-State Resistance - $m\Omega$

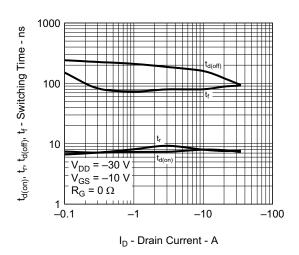
 $R_{DS(on)}$ - Drain to Source On-State Resistance - $m\Omega$

DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE

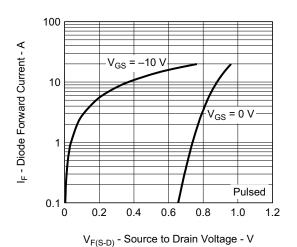


T_{ch} - Channel Temperature - °C

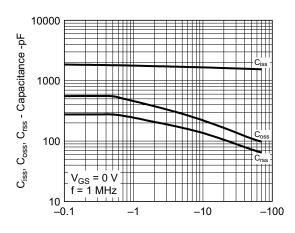
SWITCHING CHARACTERISTICS



SOURCE TO DRAIN DIODE FORWARD VOLTAGE

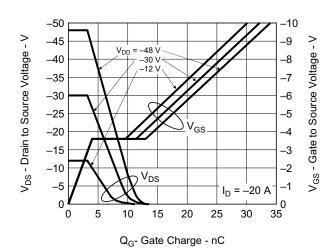


CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE

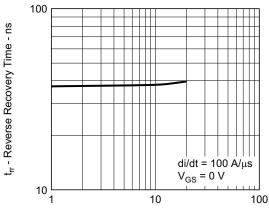


V_{DS} - Drain to Source Voltage - V

DYNAMIC INPUT/OUTPUT CHARACTERISTICS



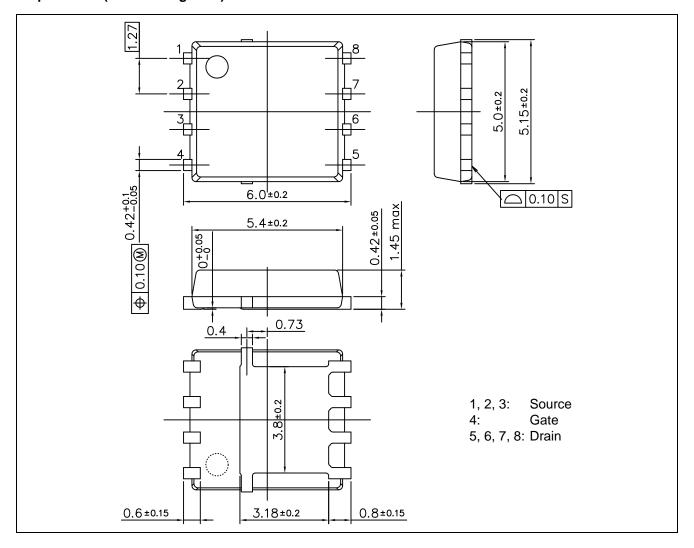
REVERSE RECOVERY TIME vs. DRAIN CURRENT



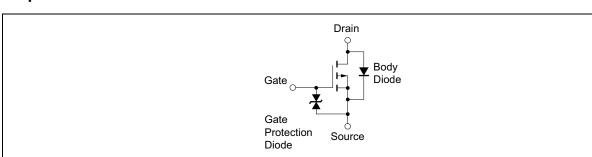
I_F - Drain Current - A

Package Drawings (Unit: mm)

8-pin HSON (Mass: 0.13 g TYP.)



Equivalent Circuit



Remark: The diode connected betweeen the gate and source of the transisor serves as a protector against EDS. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

Revision History

NP20P06YLG Data Sheet

		Description		
Rev.	Date	Page	Summary	
1.00	Apr 17, 2012	_	First Edition Issued	

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