

RJF0613JSP

60 V - 10 A - N Channel MOS FET Power Switching

R07DS0874EJ0100 Rev.1.00 Aug 29, 2012

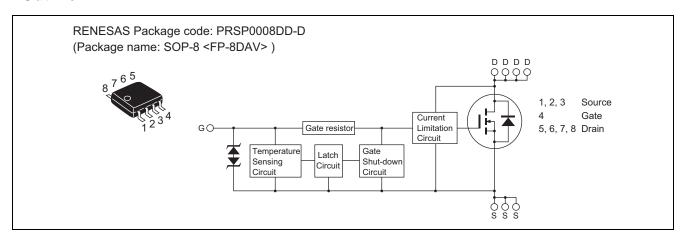
Description

This FET has the over temperature shut-down capability sensing to the junction temperature. This FET has the built-in over temperature shut-down circuit in the gate area. And this circuit operation to shut-down the gate voltage in case of high junction temperature like applying over power consumption, over current etc..

Features

- Logic level operation (4 V Gate drive).
- Built-in the over temperature shut-down circuit.
- High endurance capability against to the short circuit.
- Latch type shut down operation (need 0 voltage recovery).
- Built-in the current limitation circuit.
- High density mounting
- Power supply voltage applies 12 V and 24 V.
- AEC-Q101 Compliant

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

| Item | Symbol | Ratings | Unit |
|--|------------------------|-------------|------|
| Drain to source voltage | V_{DSS} | 60 | V |
| Gate to source voltage | V_{GSS} | 16 | V |
| Gate to source voltage | V_{GSS} | -2.5 | V |
| Drain current | I _D Note3 | 10 | A |
| Body-drain diode reverse drain current | I _{DR} | 10 | Α |
| Avalanche current | I _{AP} Note 2 | 4.7 | A |
| Avalanche energy | E _{AR} Note 2 | 94.7 | mJ |
| Channel dissipation | Pch Note 1 | 2.5 | W |
| Channel temperature | Tch | 150 | °C |
| Storage temperature | Tstg | -55 to +150 | °C |

Notes: 1. When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW \leq 10s

- 2. Tch = 25°C, Rg \geq 50 Ω
- 3. It provides by the current limitation lower bound value.

Typical Operation Characteristics

 $(Ta = 25^{\circ}C)$

| Item | Symbol | Min | Тур | Max | Unit | Test Conditions |
|----------------------------|----------------------|-----|------|-----|------|---|
| Input voltage | V_{IH} | 3.5 | _ | _ | V | |
| | V_{IL} | | _ | 1.2 | V | |
| Input current | I _{IH1} | l | _ | 100 | μΑ | Vi = 8 V, V _{DS} = 0 |
| (Gate non shut down) | I _{IH2} | l | _ | 50 | μΑ | Vi = 3.5 V, V _{DS} = 0 |
| | I _{IL} | l | _ | 1 | μΑ | Vi = 1.2 V, V _{DS} = 0 |
| Input current | I _{IH(sd)1} | l | 0.8 | _ | mA | Vi = 8 V, V _{DS} = 0 |
| (Gate shut down) | I _{IH(sd)2} | l | 0.35 | _ | mA | Vi = 3.5 V, V _{DS} = 0 |
| Shut down temperature | Tsd | l | 175 | _ | °C | Channel temperature |
| Gate operation voltage | Vop | 3.5 | _ | 12 | V | |
| Drain current | I _{D limt} | 10 | _ | _ | Α | $V_{GS} = 5 \text{ V}, V_{DS} = 10 \text{ V}^{\text{Note 4}}$ |
| (Current limitation value) | | | | | | |

Note: 4. Pulse test

Electrical Characteristics

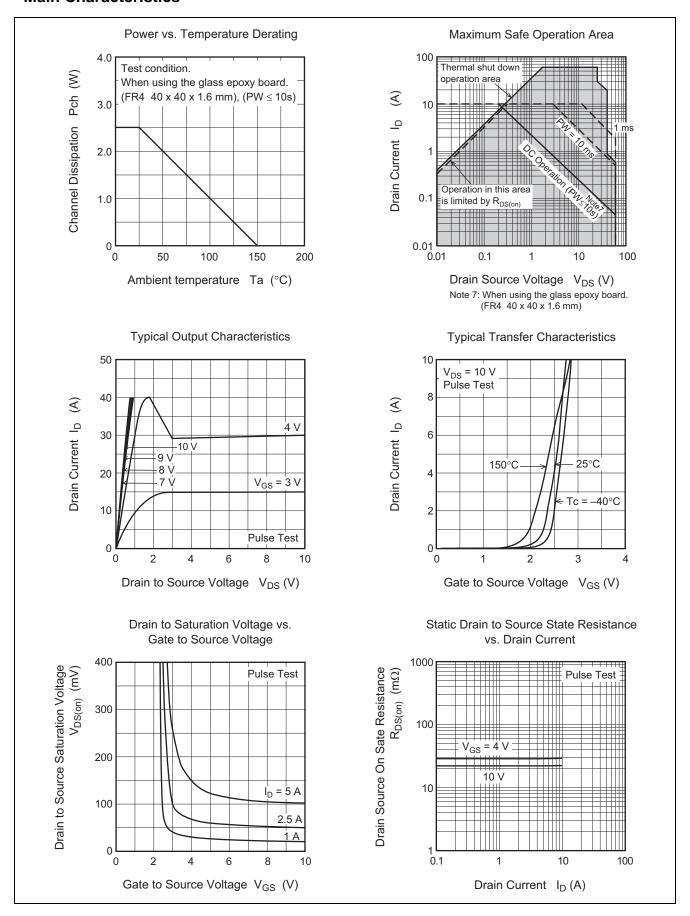
 $(Ta = 25^{\circ}C)$

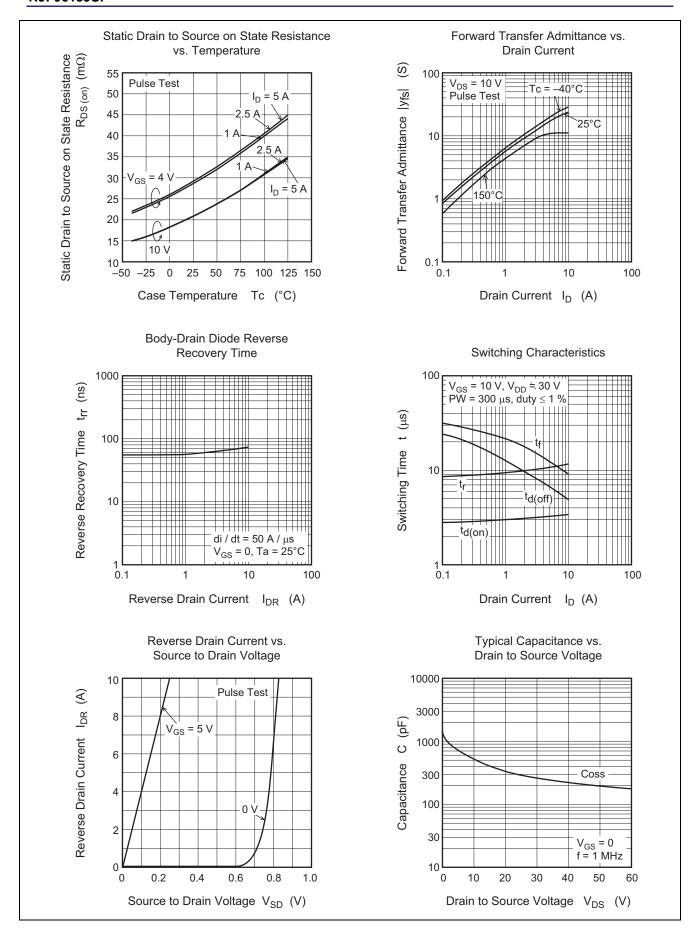
| Item | Symbol | Min | Тур | Max | Unit | Test Conditions |
|--|----------------------|------|------|------|------|--|
| Drain current | I _{D1} | _ | _ | 45 | Α | $V_{GS} = 3.5 \text{ V}, V_{DS} = 10 \text{ V}^{\text{Note 5}}$ |
| | I _{D2} | _ | _ | 10 | mA | V _{GS} = 1.2 V, V _{DS} = 10 V |
| | I _{D3} | 10 | _ | _ | Α | $V_{GS} = 5 \text{ V}, V_{DS} = 10 \text{ V}^{\text{Note 5}}$ |
| Drain to source breakdown voltage | V _{(BR)DSS} | 60 | _ | _ | V | I _D = 10 mA, V _{GS} = 0 |
| Gate to source breakdown | $V_{(BR)GSS}$ | 16 | _ | _ | V | $I_G = 800 \mu A, V_{DS} = 0$ |
| voltage | V _{(BR)GSS} | -2.5 | _ | _ | V | $I_G = -100 \mu A, V_{DS} = 0$ |
| Gate to source leak current | I _{GSS1} | _ | _ | 100 | μА | V _{GS} = 8 V, V _{DS} = 0 |
| | I _{GSS2} | _ | _ | 50 | μΑ | $V_{GS} = 3.5 \text{ V}, V_{DS} = 0$ |
| | I _{GSS3} | _ | _ | 1 | μΑ | V _{GS} = 1.2 V, V _{DS} = 0 |
| | I _{GSS4} | _ | _ | -100 | μА | $V_{GS} = -2.4 \text{ V}, V_{DS} = 0$ |
| Input current (shut down) | I _{GS(OP)1} | _ | 8.0 | _ | mA | V _{GS} = 8 V, V _{DS} = 0 |
| | I _{GS(OP)2} | _ | 0.35 | _ | mA | $V_{GS} = 3.5 \text{ V}, V_{DS} = 0$ |
| Zero gate voltage drain current | I _{DSS} | _ | _ | 10 | μА | V _{DS} = 32 V, V _{GS} = 0, Ta = 125°C |
| Gate to source cutoff voltage | $V_{GS(off)}$ | 1.1 | _ | 2.1 | V | V _{DS} = 10 V, I _D = 1 mA |
| Forward transfer admittance | y _{fs} | 12 | 17 | _ | S | I _D = 5 A, V _{DS} = 10 V Note 5 |
| Static drain to source on state | R _{DS(on)} | _ | 30 | 40 | mΩ | $I_D = 5 \text{ A}, V_{GS} = 4 \text{ V}^{\text{Note 5}}$ |
| resistance | R _{DS(on)} | _ | 21 | 30 | mΩ | $I_D = 5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note 5}}$ |
| Output capacitance | Coss | _ | 520 | _ | pF | V _{DS} = 10 V, V _{GS} = 0, f = 1MHz |
| Turn-on delay time | t _{d(on)} | _ | 3.5 | _ | μS | V_{GS} = 10 V, I_{D} = 5 A, R_{L} = 2 Ω |
| Rise time | t _r | _ | 11 | _ | μS | |
| Turn-off delay time | t _{d(off)} | _ | 7 | _ | μS | |
| Fall time | t _f | _ | 12 | _ | μS | |
| Body-drain diode forward voltage | V_{DF} | _ | 0.9 | _ | V | I _F = 10 A, V _{GS} = 0 |
| Body-drain diode reverse recovery time | t _{rr} | _ | 63 | _ | ns | $I_F = 10 \text{ A}, V_{GS} = 0$ $di_F/dt = 50 \text{ A}/\mu\text{s}$ |
| Over load shut down | t _{os1} | _ | 0.4 | _ | ms | V _{GS} = 5 V, V _{DD} = 16 V |
| operation time Note 6 | t _{os2} | _ | 0.25 | _ | ms | V _{GS} = 5 V, V _{DD} = 24 V |

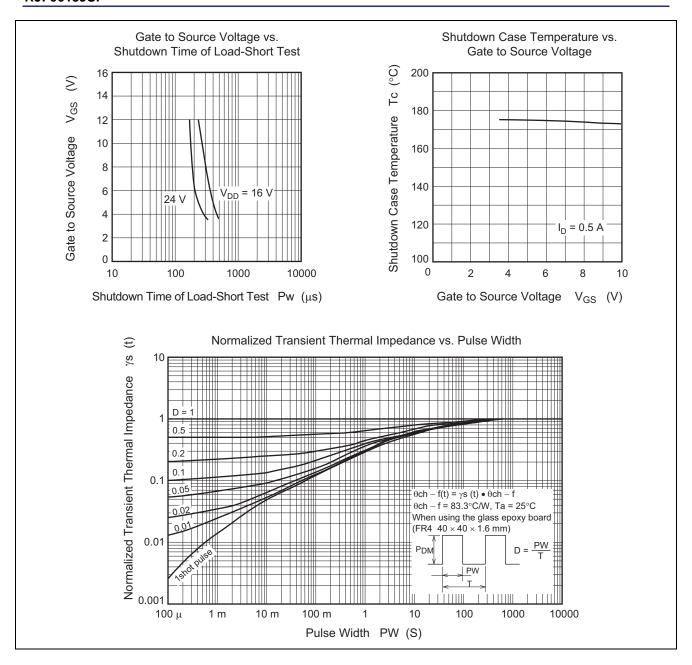
Notes: 5. Pulse test

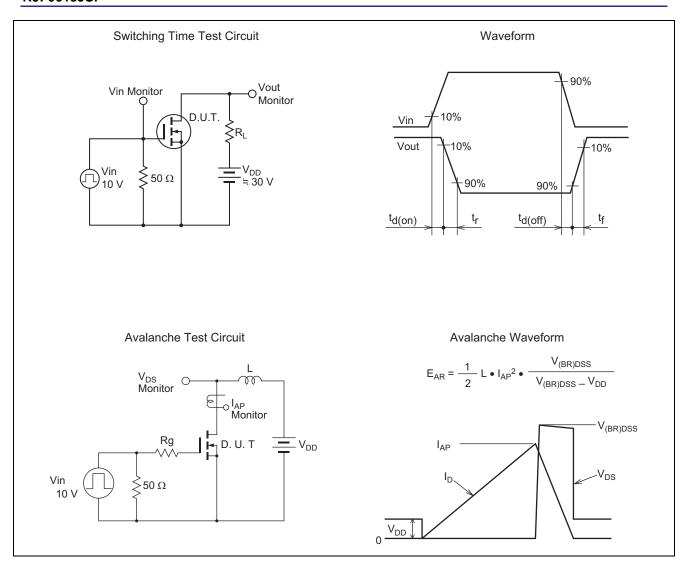
6. Including the junction temperature rise of the over loaded condition.

Main Characteristics

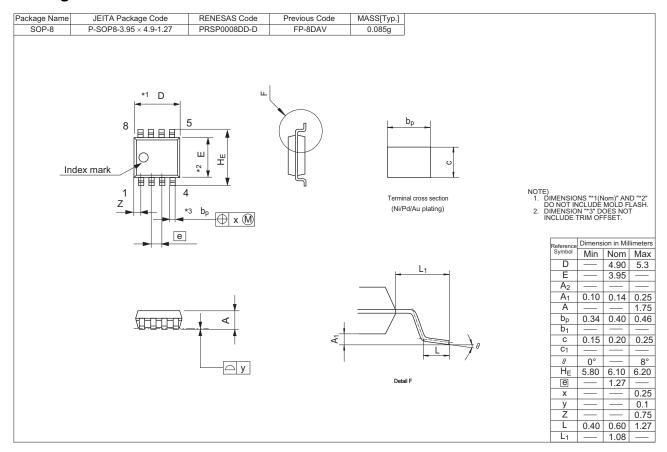








Package Dimensions



Ordering Information

| Orderable Part Number | Quantity | Shipping Container |
|-----------------------|----------|--------------------|
| RJF0613JSP-00-J0 | 2500 pcs | Taping |

Note: The symbol of 2nd "-" is occasionally presented as "#".

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