# RGS50NL65DHRBTL

### 650V 25A Field Stop Trench IGBT

Datasheet

V <sub>CES</sub>	650V
I <sub>C</sub>	25A
V <sub>CE(sat) (Typ.)</sub>	1.65V
$P_D$	206W

# Outline LPDL (TO-263L) (1) (3)

### Features

- 1) Qualified to AEC-Q101
- 2) Low Collector Emitter Saturation Voltage
- 3) Short Circuit Withstand Time 8µs
- 4) Built in Very Fast & Soft Recovery FRD
- 5) Pb free Lead Plating; RoHS Compliant

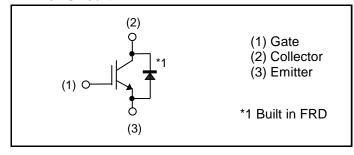
### Application

General Inverter

for Automotive and Industrial Use

Heater for Automotive

### ●Inner Circuit



Packaging Specifications

Type	Packaging	Taping
	Reel Size (mm)	330
	Tape Width (mm)	24
	Basic Ordering Unit (pcs)	1,000
	Packing Code	TL
	Marking	RGS50NL65D

## ● Absolute Maximum Ratings (at T<sub>C</sub> = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V <sub>CES</sub>	650	V
Gate - Emitter Voltage		$V_{GES}$	±30	V
Collector Current	T <sub>C</sub> = 25°C	I <sub>C</sub>	50	А
Collector Current	T <sub>C</sub> = 100°C	I <sub>C</sub>	34	А
Pulsed Collector Current		I <sub>CP</sub> *1	75	А
Diede Fermand Ourmant	T <sub>C</sub> = 25°C	I <sub>F</sub>	43	А
Diode Forward Current	T <sub>C</sub> = 100°C	I <sub>F</sub>	25	А
Diode Pulsed Forward Current		I <sub>FP</sub> *1	75	А
Dower Discinction	T <sub>C</sub> = 25°C	P <sub>D</sub>	206	W
Power Dissipation	T <sub>C</sub> = 100°C	P <sub>D</sub>	103	W
Operating Junction Temperature		T <sub>j</sub>	-40 to +175	°C
Storage Temperature		T <sub>stg</sub>	-55 to +175	°C

<sup>\*1</sup> Pulse width limited by T<sub>imax</sub>.

### ●Thermal Resistance

Parameter	Symbol	Values			Unit
raiailletei	Symbol	Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	ı	ı	0.73	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j-c)}$	ı	ı	1.55	°C/W

# ●IGBT Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
raiailletei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Collector - Emitter Breakdown Voltage	BV <sub>CES</sub>	$I_{C} = 10 \mu A, V_{GE} = 0 V$	650	ı	-	V
		$V_{CE} = 650V, V_{GE} = 0V,$				
Collector Cut - off Current	I <sub>CES</sub>	$T_j = 25^{\circ}C$	-	-	10	μΑ
		Tj = 175°C	-	0.1	-	mA
Gate - Emitter Leakage Current	I <sub>GES</sub>	$V_{GE} = \pm 30V$ , $V_{CE} = 0V$	-	-	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5V, I_{C} = 1.25mA$	5.0	6.0	7.0	V
		$I_C = 25A, V_{GE} = 15V,$				
Collector - Emitter Saturation Voltage	V <sub>CE(sat)</sub>	$T_j = 25^{\circ}C$	-	1.65	2.10	V
		T <sub>j</sub> = 175°C	-	2.15	-	V

# ●IGBT Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Conditions		Unit		
			Min.	Тур.	Max.	Offic
Input Capacitance	$C_{ies}$	$V_{CE} = 30V$ ,	-	968	-	
Output Capacitance	C <sub>oes</sub>	$V_{GE} = 0V$ ,	-	66	-	pF
Reverse transfer Capacitance	$C_{res}$	f = 1MHz	ı	9	ı	
Total Gate Charge	$Q_g$	V <sub>CE</sub> = 400V,	-	31	ı	
Gate - Emitter Charge	$Q_ge$	I <sub>C</sub> = 25A,	1	9	ı	nC
Gate - Collector Charge	$Q_{gc}$	$V_{GE} = 15V$	ı	13	ı	
Turn - on Delay Time	t <sub>d(on)</sub>		ı	28	ı	
Rise Time	t <sub>r</sub>	$I_C = 25A, V_{CC} = 400V,$ $V_{GE} = 15V, R_G = 10\Omega,$	ı	15	ı	nc
Turn - off Delay Time	$t_{d(off)}$	$T_i = 25^{\circ}C$	1	91	ı	ns
Fall Time	t <sub>f</sub>	Inductive Load  *E <sub>on</sub> include diode reverse recovery	-	97	-	
Turn - on Switching Loss	E <sub>on</sub>		ı	0.81	ı	- mJ
Turn - off Switching Loss	$E_{off}$	·	1	0.65	ı	
Turn - on Delay Time	$t_{d(on)}$		-	28	-	ns
Rise Time	t <sub>r</sub>	$I_C = 25A, V_{CC} = 400V,$ $V_{GE} = 15V, R_G = 10\Omega,$ $T_i = 175^{\circ}C$	1	18	ı	
Turn - off Delay Time	$t_{d(off)}$		-	109	ı	
Fall Time	t <sub>f</sub>	Inductive Load	ı	129	ı	
Turn - on Switching Loss	E <sub>on</sub>	*E <sub>on</sub> include diode reverse recovery	ı	0.86	ı	m l
Turn - off Switching Loss	$E_{off}$	•	-	0.87	-	mJ
		$I_C = 75A, V_{CC} = 520V,$				-
Reverse Bias Safe Operating Area	RBSOA	$V_P = 650V, V_{GE} = 15V,$	FULL SQUARE			
date operating Area		$R_G = 50\Omega, T_j = 175^{\circ}C$				
Short Circuit Withstand Time	t <sub>sc</sub>	$V_{CC} \le 360V$ , $V_{GE} = 15V$ , $T_j = 25^{\circ}C$	8	-	-	μs
Short Circuit Withstand Time	t <sub>sc</sub> *2	$V_{CC} \le 360V$ , $V_{GE} = 15V$ , $T_j = 150$ °C	6	-	-	μs

\*2 Design assurance without measurement

# **•FRD Electrical Characteristics** (at $T_j = 25$ °C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Тур.	Max.	Offic
		I <sub>F</sub> = 25A,				
Diode Forward Voltage	$V_{F}$	$T_j = 25^{\circ}C$	-	1.5	1.95	V
		T <sub>j</sub> = 175°C	-	1.6	-	
Diode Reverse Recovery Time	t <sub>rr</sub>	$I_F = 25A$ , $V_{CC} = 400V$ , $di_F/dt = 200A/\mu s$ , $T_j = 25^{\circ}C$	-	95	1	ns
Diode Peak Reverse Recovery Current	I <sub>rr</sub>		-	6.9	ı	А
Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	0.37	ı	μC
Diode Reverse Recovery Energy	E <sub>rr</sub>		-	16	ı	μJ
Diode Reverse Recovery Time	t <sub>rr</sub>	$I_F = 25A,$ $V_{CC} = 400V,$ $di_F/dt = 200A/\mu s,$ $T_j = 175^{\circ}C$	-	127	-	ns
Diode Peak Reverse Recovery Current	I <sub>rr</sub>		-	8.3	ı	А
Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	0.64	-	μC
Diode Reverse Recovery Energy	E <sub>rr</sub>		-	34	-	μJ

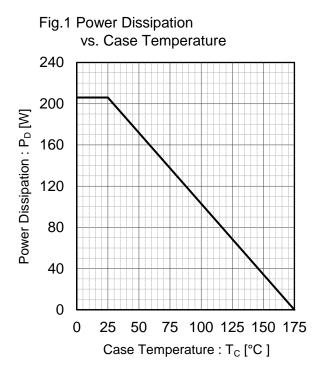


Fig.2 Collector Current vs. Case Temperature 60 50 Collector Current : Ic [A] 40 30 20 10 T<sub>i</sub> ≤ 175°C <sup>j</sup><sub>GE</sub> ≥ 15V 0 25 50 75 100 125 150 175 0 Case Temperature : T<sub>C</sub> [°C]

Fig.3 Forward Bias Safe Operating Area

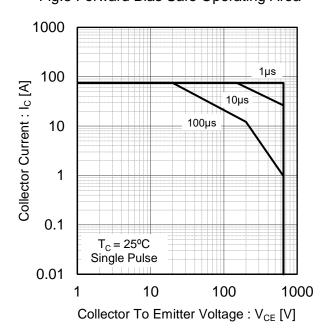
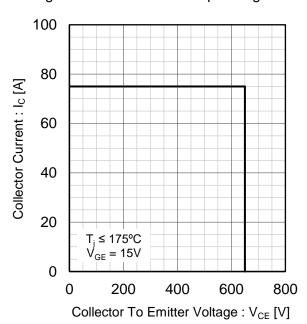


Fig.4 Reverse Bias Safe Operating Area



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Fig.5 Typical Output Characteristics

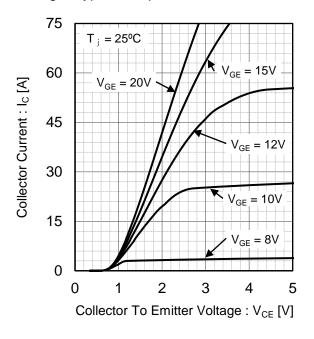


Fig.6 Typical Output Characteristics

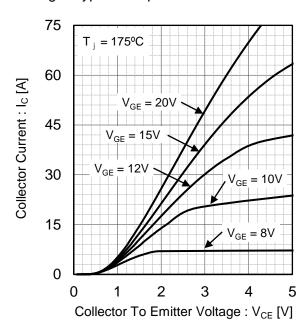


Fig.7 Typical Transfer Characteristics

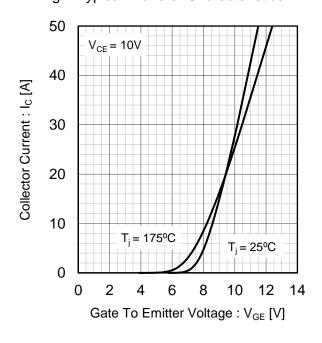
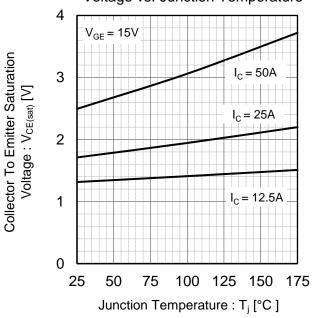


Fig.8 Typical Collector to Emitter Saturation Voltage vs. Junction Temperature



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Fig.9 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

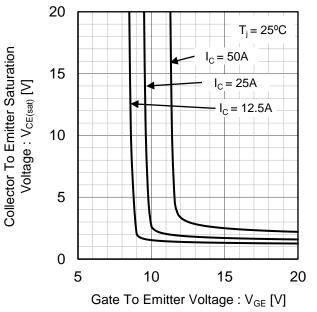


Fig.10 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

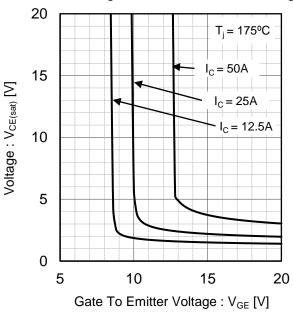


Fig.11 Typical Capacitance vs. Collector to Emitter Voltage

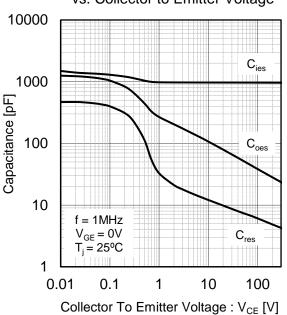
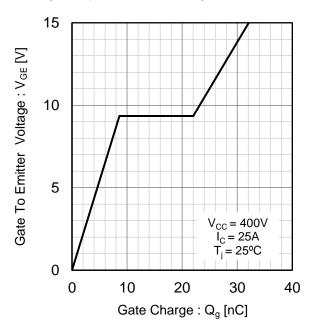
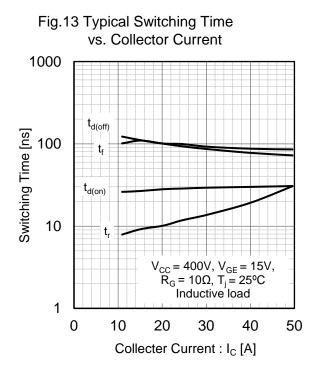


Fig.12 Typical Gate Charge



Collector To Emitter Saturation



vs. Gate Resistance 1000 Switching Time [ns] 100  $t_{d(off)}$ d(on) 10  $I_{C} = 400 \text{V}, V_{GE} = 15 \text{V},$   $I_{C} = 25 \text{A}, T_{j} = 25 ^{\circ} \text{C}$ Inductive load 1 0 10 20 30 40 50 Gate Resistance :  $R_G[\Omega]$ 

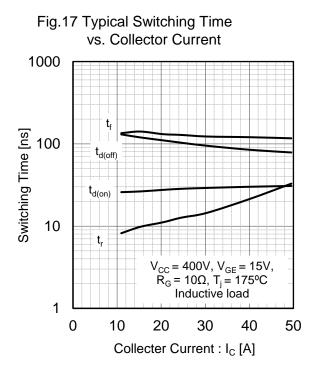
Fig.14 Typical Switching Time

Fig.15 Typical Switching Energy Losses vs. Collector Current 10 Switching Energy Losses [mJ] 1  $\mathsf{E}_{\mathsf{off}}$ 0.1  $\mathsf{E}_{\mathsf{on}}$  $V_{CC}$  = 400V,  $V_{GE}$  = 15V,  $R_{G}$  = 10 $\Omega$ ,  $T_{j}$  = 25°C Inductive load 0.01 0 10 20 30 40 50 Collecter Current : I<sub>C</sub> [A]

vs. Gate Resistance 10 Switching Energy Losses [mJ]  $\mathsf{E}_{\mathsf{off}}$ 1  $\mathsf{E}_{\mathsf{on}}$ 0.1  $V_{CC}$  = 400V,  $V_{GE}$  = 15V,  $I_{C}$  = 25A,  $T_{j}$  = 25°C Inductive load 0.01 0 10 20 30 40 50 Gate Resistance :  $R_G[\Omega]$ 

Fig.16 Typical Switching Energy Losses

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vs. Gate Resistance 1000 Switching Time [ns] 100  $t_{d(off)}$  $t_{d(on)}$ 10  $C_{CC} = 400 \text{V}, V_{GE} = 15 \text{V},$   $I_{C} = 25 \text{A}, T_{j} = 175 ^{\circ}\text{C}$ Inductive load 1 0 10 20 30 40 50 Gate Resistance :  $R_G[\Omega]$ 

Fig.18 Typical Switching Time

Fig.19 Typical Switching Energy Losses vs. Collector Current 10 Switching Energy Losses [mJ] 1  $\mathsf{E}_{\mathsf{off}}$  $\mathsf{E}_{\mathsf{on}}$ 0.1  $V_{CC} = 400V, V_{GE} = 15V,$   $R_G = 10\Omega, T_j = 175^{\circ}C$ Inductive load 0.01 0 10 20 30 40 50 Collecter Current : I<sub>C</sub> [A]

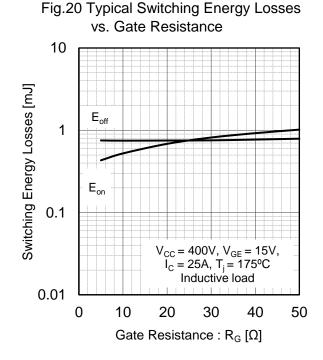


Fig.21 Typical Diode Forward Current vs. Forward Voltage

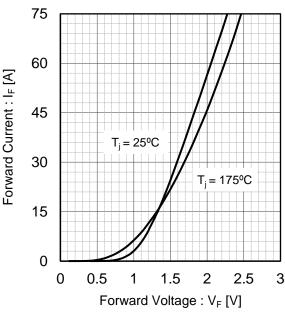


Fig.22 Typical Diode Revese Recovery Time vs. Forward Current

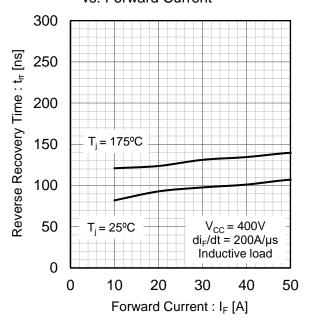


Fig.23 Typical Diode Reverse Recovery Current vs. Forward Current

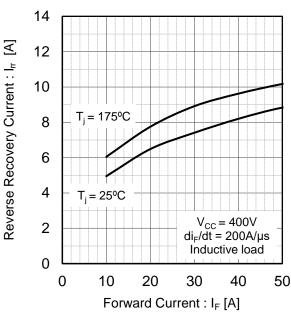
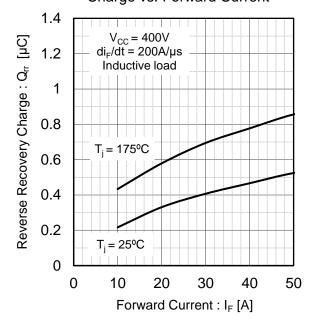


Fig.24 Typical Diode Rrverse Recovery Charge vs. Forward Current



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Fig.25 Typical IGBT Transient Thermal Impedance

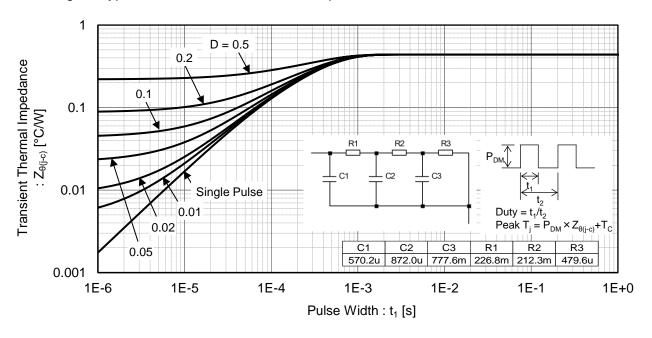
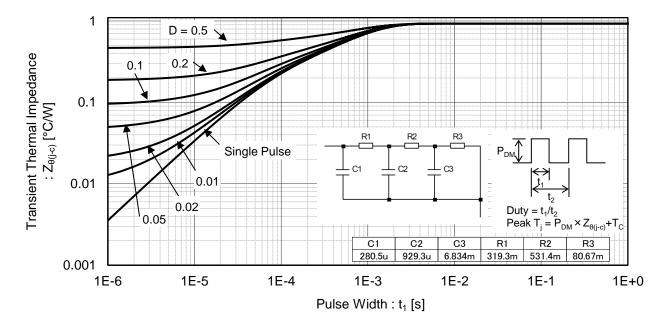


Fig.26 Typical Diode Transient Thermal Impedance



### Inductive Load Switching Circuit and Waveform

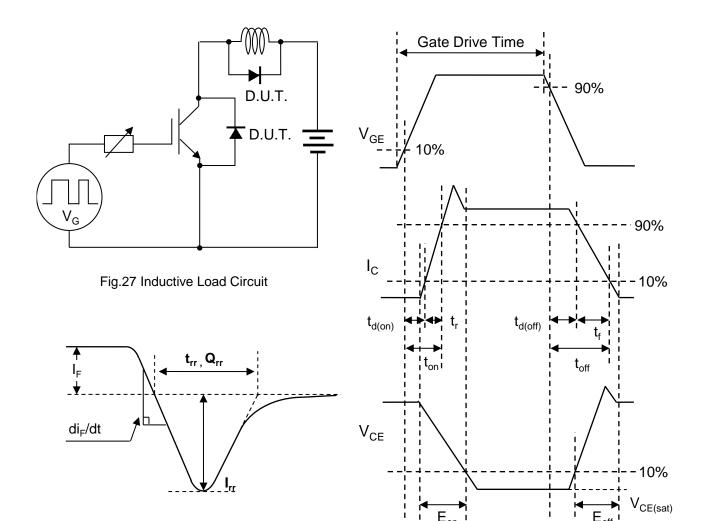


Fig.29 Diode Reverse Recovery Waveform

Fig.28 Inductive Load Waveform

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