# VSMY385011X02



**Vishay Semiconductors** 

# High Speed Infrared Emitting Diode, 850 nm, Surface Emitter Technology



# LINKS TO ADDITIONAL RESOURCES



### DESCRIPTION

The VSMY385011X02 is an infrared, 850 nm emitting diode based on surface emitter technology with high radiant intensity, high optical power and high speed, molded in a PLCC-2 package for surface mounting (SMD).

# FEATURES

- Package type: surface-mount
- Package form: PLCC-2
- Dimensions (L x W x H in mm): 3.5 x 2.8 x 1.75
- AEC-Q102 qualified
- Peak wavelength:  $\lambda_p = 850 \text{ nm}$
- Angle of half intensity:  $\phi = \pm 60^{\circ}$
- Floor life: 168 h, MSL 3, according to J-STD-020
- Lead (Pb)-free reflow soldering
- Material categorization:
  for definitions of compliance please see
  www.vishay.com/doc?99912

### **RELEASED FOR APPLICATIONS**

- Infrared radiation source for operation with CMOS cameras (illumination)
- High speed IR data transmission
- Automotive sensors
- Light curtain

# PRODUCT SUMMARY

	JOT COMMANY					
COMPONENT	l <sub>e</sub> (mW/sr) at I <sub>F</sub> = 100 mA	φ <b>(°)</b>	λ <sub>P</sub> (nm)	t <sub>r</sub> (ns)		
VSMY385011X02	12	± 60	850	25		

#### Note

Test conditions see table "Basic Characteristics"

#### **ORDERING INFORMATION**

ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM
VSMY385011X02-GS08	Tape and reel	MOQ: 7500 pcs, 1500 pcs/reel	PLCC-2
VSMY385011X02-GS18	Tape and reel	MOQ: 8000 pcs, 8000 pcs/reel	PLCC-2

#### Note

• MOQ: minimum order quantity

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
Reverse voltage		V <sub>R</sub>	5	V			
Forward current <sup>(1)</sup>		I <sub>F</sub>	100	mA			
Pulse peak forward current	$t_p/T = 0.03, t_p = 100 \ \mu s$	I <sub>FM</sub>	200	mA			
Surge forward current	t <sub>p</sub> = 100 μs	I <sub>FSM</sub>	500	mA			
Power dissipation		Pv	180	mW			
Junction temperature		Тj	110	°C			
Operating temperature range		T <sub>amb</sub>	-40 to +105	°C			
Storage temperature range		T <sub>stg</sub>	-40 to +110	°C			
Soldering temperature	According to Fig. 7, J-STD-020	T <sub>sd</sub>	260	°C			
Thermal resistance junction to ambient	J-STD-051, soldered on PCB	R <sub>thJA</sub>	240	K/W			

#### Note

<sup>(1)</sup> Minimum forward current  $I_{F min.} = 5 \text{ mA}$ 

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RoHS

COMPLIANT

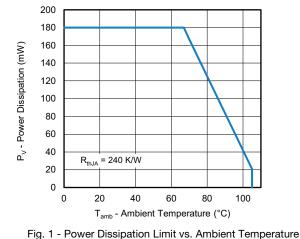
HALOGEN

FREE



VSMY385011X02

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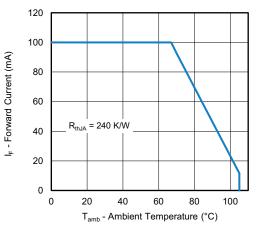


Fig. 2 - Forward Current Limit vs. Ambient Temperature

<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Forward voltage	l <sub>F</sub> = 100 mA, t <sub>p</sub> = 20 ms	V <sub>F</sub>	-	1.6	-	V	
	$I_F = 0.5 \text{ A}, t_p = 300 \ \mu \text{s}$	V <sub>F</sub>	-	2.1	-	V	
Temperature coefficient of V <sub>F</sub>	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	TK <sub>VF</sub>	-	-1.5	-	mV/K	
Reverse current		I <sub>R</sub>	Not designed for reverse operation		μA		
Junction capacitance	V <sub>R</sub> = 0 V, f = 1 MHz, E = 0	Cj	-	15	-	pF	
Radiant power	l <sub>F</sub> = 100 mA, t <sub>p</sub> = 20 ms	φ <sub>e</sub>	-	35	-	mW	
Temperature coefficient of $\phi_{e}$	I <sub>F</sub> = 100 mA	TKφ <sub>e</sub>	-	-0.4	-	%/K	
Angle of half intensity		φ	-	± 60	-	0	
Peak wavelength	I <sub>F</sub> = 100 mA	λp	-	850	-	nm	
Spectral bandwidth	I <sub>F</sub> = 100 mA	Δλ	-	27	-	nm	
Temperature coefficient of $\lambda_p$	I <sub>F</sub> = 100 mA	ΤΚλρ	-	0.3	-	nm/K	
Rise time	I <sub>F</sub> = 30 mA	t <sub>r</sub>	-	25	-	ns	
Fall time	I <sub>F</sub> = 30 mA	t <sub>f</sub>	-	25	-	ns	

<b>TYPE DEDICATED CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	BINNED GROUP	SYMBOL	MIN.	MAX.	UNIT	
Radiant intensity I <sub>F</sub> =		А		9.4	11.1	UNIT mW/sr	
	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	В	l <sub>e</sub>	10.9	12.6	mW/sr	
		С		12.4	14.1		

Note

• Each 1500 reel will contain a single group. The label on the bag will indicate which binned group is the bag. A specific group cannot be ordered. Production shipment containing multiple bags will likely include multiple groups. Please design accordingly.





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# **BASIC CHARACTERISTICS** ( $T_{amb}$ = 25 °C, unless otherwise specified)

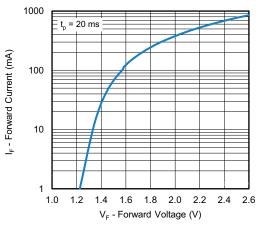
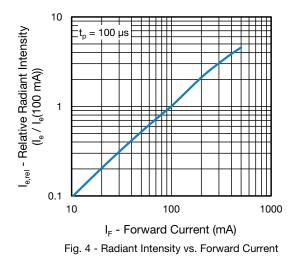


Fig. 3 - Forward Current vs. Forward Voltage



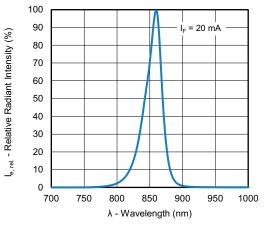


Fig. 5 - Relative Radiant Power vs. Wavelength

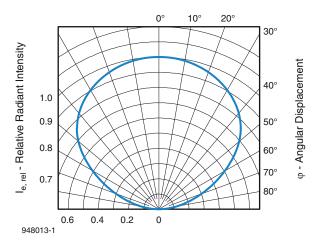
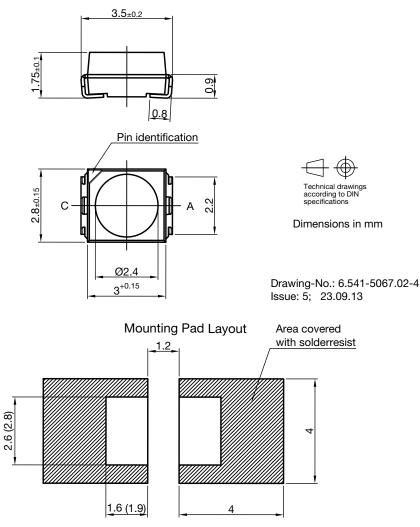


Fig. 6 - Relative Radiant Intensity vs. Angular Displacement



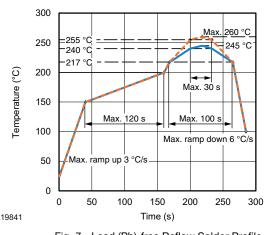
# **Vishay Semiconductors**

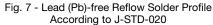
## **PACKAGE DIMENSIONS** in millimeters



Dimensions: Reflow and vapor phase (wave soldering)

# SOLDER PROFILE





## DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

# **FLOOR LIFE**

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 168 h

Conditions: T<sub>amb</sub> < 30 °C, RH < 60 %

Moisture sensitivity level 3, according to J-STD-033D

### DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.

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# TAPE AND REEL

PLCC-2 components are packed in antistatic blister tape (DIN IEC (CO) 564) for automatic component insertion. Cavities of blister tape are covered with adhesive tape.

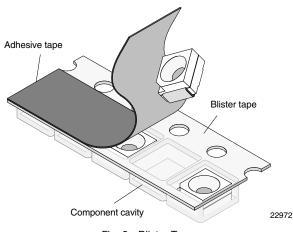


Fig. 8 - Blister Tape

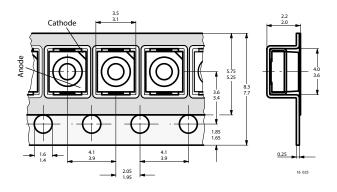
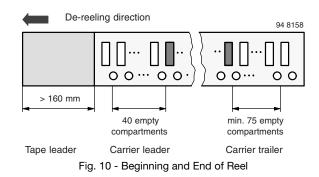


Fig. 9 - Tape Dimensions in mm for PLCC-2

# **MISSING DEVICES**

A maximum of 0.5 % of the total number of components per reel may be missing, exclusively missing components at the beginning and at the end of the reel. A maximum of three consecutive components may be missing, provided this gap is followed by six consecutive components.



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The tape leader is at least 160 mm and is followed by a carrier tape leader with at least 40 empty compartments. The tape leader may include the carrier tape as long as the cover tape is not connected to the carrier tape. The least component is followed by a carrier tape trailer with a least 75 empty compartments and sealed with cover tape.

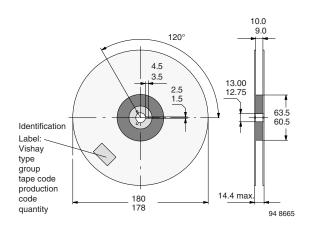


Fig. 11 - Dimensions of Reel-GS08

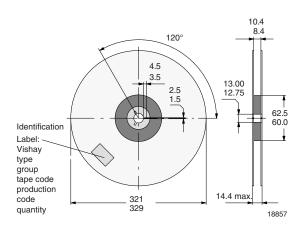


Fig. 12 - Dimensions of Reel-GS18

# **COVER TAPE REMOVAL FORCE**

The removal force lies between 0.1 N and 1.0 N at a removal speed of 5 mm/s. In order to prevent components from popping out of the blisters, the cover tape must be pulled off at an angle of 180° with regard to the feed direction.

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