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MPC8555 Configurable Development System

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The CDS system is designed to aid hardware and software developers of the PowerQUICC[®]™ III family by providing an evaluation platform for MPC8555E, and MPC8541E derivatives in the 783-pin FC-PBGA package.

The CDS system is preloaded with U-boot and Linux BSP, it has 256MB DDR SDRAM DIMM module, 2 banks of Flash 64Mb each. The on-board interfaces include JTAG/COP, USB, Dual UART, Dual PCI, 10/100/1000 Base-T Fast Ethernet interface, and a 155 Mbps ATM UNI with optical interface.

Using the CDS System on-board resources and an associated debugger, developers are able to download and run code, set breakpoints, display memory and registers, and connect proprietary hardware via the expansion connectors. That code can then be incorporated into a desired system with the MPC85xx processor.

Who should order this product?

Developers using an MPC8555E, or MPC8541E

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Documentation

Quick reference to our documentation types (/design/documentation/nxp-technical-documentation:NXP-TECHNICAL-DOCUMENTATION).

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REFERENCE MANUAL

MPC8555E Configurable Development System Reference Manual (/docs/en/reference-manual/MPC8555CDSX3RM.pdf)

PDF Rev 1 Nov 9, 2006 7.9 MB MPC8555CDSX3RM English