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MDT0700A0OSR-PAR	800 x 480	Parallel Interface	TFT Module						
(MCT070WCA0TW800480LML) Specification									
Version: 3 Date: 10/10/2016									
		Revision							
1 (	)9/03/2016 F	First issue.							
2 /	1/08/2016 N	Modify Vibration test.							
3 (	08/10/2016 N	Modify Summary.							

Display F	eatures		
Display Size	7.0"		
Resolution	800 x 480		
Orientation	Landscape		
Appearance	RGB		
Logic Voltage	3.1V		oHS ompliant
Interface	Parallel	IVR	$(0) \vdash S$
Brightness	250 cd/m <sup>2</sup>	/ 4 23	mpliant
Touchscreen	SPRTP	, 00	mpnant
Module Size	165.00 x 100.00 x 13.00mm		10.54
Operating Temperature	-20°C ~ +70°C		
Pinout	36 Way Connector	Box Quantity	Weight / Display
Pitch			

\* - For full design functionality, please use this specification in conjunction with the SSD1963 specification.(Provided Separately)

Display Accessories								
Part Number	Description							
MDIB-RPI	The MDIB-RPI is a Raspberry Pi interface board designed to provide connectivity and compatibility to a range of MIDAS TFT displays.							

Optional Variants									
Appearances	Voltage								

### **Summary**

TFT 7.0"is a TN transmissive type color active matrix TFT liquid crystal display that use amorphous silicon TFT as switching devices. This module is a composed of a TFT\_LCD module, It is usually designed for industrial application and this module follows RoHs.

### 3. General Specifications

■ Resolution: 800 x RGB x 480(TFT)

■ Module dimension: 165.0 x 100.0 x 13.0mm

Active Area : 154.08 x 85.92 mm

■ Dot pitch: 0.0642 x 0.179 mm

■ LCD type: TFT, Normally White, Transmissive

■ View direction: 12 o'clock

■ Gray Scale Inversion Direction: 6 o'clock

Backlight Type: LED, Normally White

Controller IC: SSD1963 ANUFACTURE • SUPPLY

■ Interface: Digital 8080 family MPU 8bit/16bit

■ With /Without TP: With RTP

Surface: Anti-Glare

Color tone slight changed by temperature and driving voltage.

# Interface

### 1. LCM PIN Definition (CON2)

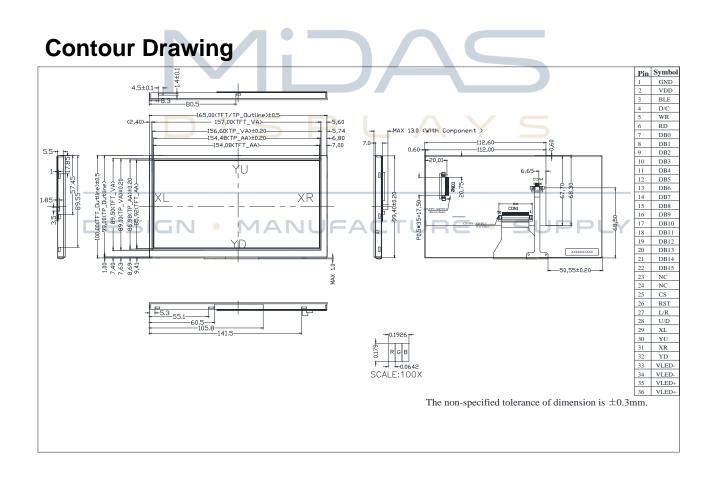
System ground pin of the IC .   Connect to system ground.	Pin	Symbol	Function	Remark
Connect to system ground.  2 VDD Power Supply: +3.3V  3 BLE Backlight control signal , H: On \ L: Off  4 D/C Data/Command select  5 WR Write strobe signal  6 RD Read strobe signal  7 DB0 Data bus  8 DB1 Data bus  9 DB2 Data bus  10 DB3 Data bus  11 DB4 Data bus  12 DB5 Data bus  13 DB6 Data bus  14 DB7 Data bus  15 DB8 Data bus (When select 8bits Mode, this pin is NC) Note1  16 DB9 Data bus (When select 8bits Mode, this pin is NC) Note1  17 DB10 Data bus (When select 8bits Mode, this pin is NC) Note1  18 DB11 Data bus (When select 8bits Mode, this pin is NC) Note1  19 DB12 Data bus (When select 8bits Mode, this pin is NC) Note1  19 DB12 Data bus (When select 8bits Mode, this pin is NC) Note1  20 DB13 Data bus (When select 8bits Mode, this pin is NC) Note1  21 DB14 Data bus (When select 8bits Mode, this pin is NC) Note1  22 DB15 Data bus (When select 8bits Mode, this pin is NC) Note1  23 NC No connect  24 NC No connect  25 CS Chip select  26 RST Hardware reset  27 L/R Left / right selection; Default L/R=H Note 2,3  28 U/D Up/down selectrode  30 YU Top electrode  31 XR Right electrode  32 YD Bottom electrode  33 VLED- Power for LED Driver IC(GND)	1		System ground pin of the IC.	
BLE			· · · · · · · · · · · · · · · · · · ·	
4 D/C Data/Command select 5 WR Write strobe signal 6 RD Read strobe signal 7 DB0 Data bus 8 DB1 Data bus 9 DB2 Data bus 10 DB3 Data bus 11 DB4 Data bus 12 DB5 Data bus 13 DB6 Data bus 14 DB7 Data bus 15 DB8 Data bus 16 DB9 Data bus (When select 8bits Mode, this pin is NC) Note1 17 DB10 Data bus (When select 8bits Mode, this pin is NC) Note1 18 DB11 Data bus (When select 8bits Mode, this pin is NC) Note1 19 DB12 Data bus (When select 8bits Mode, this pin is NC) Note1 19 DB13 Data bus (When select 8bits Mode, this pin is NC) Note1 19 DB14 Data bus (When select 8bits Mode, this pin is NC) Note1 20 DB13 Data bus (When select 8bits Mode, this pin is NC) Note1 21 DB14 Data bus (When select 8bits Mode, this pin is NC) Note1 22 DB15 Data bus (When select 8bits Mode, this pin is NC) Note1 23 NC No connect 24 NC No connect 25 CS Chip select 26 RST Hardware reset DFA LED Note2,3 28 U/D Up/down selection; Default L/R=H Note 2,3 29 XL Left electrode 30 YU Top electrode 31 XR Right electrode 32 YD Bottom electrode 33 VLED- Power for LED Driver IC(GND)	2	VDD	Power Supply: +3.3V	
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DB12 Data bus (When select 8bits Mode, this pin is NC) DB13 Data bus (When select 8bits Mode, this pin is NC) Note1 DB14 Data bus (When select 8bits Mode, this pin is NC) DB15 Data bus (When select 8bits Mode, this pin is NC) Note1 DB16 NC No connect NC No connect CS CS Chip select CS Chip select CS Chip select DIAR Left / right selection; Default L/R=H Note 2,3 DYD Up/down selection; Default U/D=L Note 2,3 DYU Top electrode TYU Top electrode TYU Top electrode TYD Bottom electrode  TYD Bottom electrode  TYD Power for LED Driver IC(GND)  TOTAL DETAIL SHOP IN NOTE IN NO	17	DB10	Data bus (When select 8bits Mode, this pin is NC)	Note1
DB13 Data bus (When select 8bits Mode, this pin is NC) Note1 DB14 Data bus (When select 8bits Mode, this pin is NC) Note1 DB15 Data bus (When select 8bits Mode, this pin is NC) Note1 NC No connect NC No connect CS CS Chip select CS Chip select DHardware reset CHACLE SOLUTION DIPOLED Note 2,3  No	18	DB11	Data bus (When select 8bits Mode, this pin is NC)	Note1
DB14 Data bus (When select 8bits Mode, this pin is NC) DB15 Data bus (When select 8bits Mode, this pin is NC) Note1 NC No connect NC No connect CS Chip select CS Chip select CHardware reset CHARLEH Note 2,3  L/R Left / right selection; Default L/R=H Note 2,3  WLED- Power for LED Driver IC(GND)  Note1 Note2 Note	19	DB12	Data bus (When select 8bits Mode, this pin is NC)	Note1
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23 NC No connect 24 NC No connect 25 CS Chip select 26 RST Hardware reset Provided Hardware reset Prov	21	DB14	Data bus (When select 8bits Mode, this pin is NC)	Note1
24 NC No connect 25 CS Chip select  26 RST Hardware reset 27 L/R Left / right selection; Default L/R=H Note 2,3 28 U/D Up/down selection; Default U/D=L Note 2,3 29 XL Left electrode 30 YU Top electrode 31 XR Right electrode 32 YD Bottom electrode 33 VLED- Power for LED Driver IC(GND) 34 VLED- Power for LED Driver IC(GND)	22	DB15	Data bus (When select 8bits Mode, this pin is NC)	Note1
25 CS Chip select 26 RST Hardware reset 27 L/R Left / right selection; Default L/R=H Note 2,3 28 U/D Up/down selection; Default U/D=L Note 2,3 29 XL Left electrode 30 YU Top electrode 31 XR Right electrode 32 YD Bottom electrode 33 VLED- Power for LED Driver IC(GND) 34 VLED- Power for LED Driver IC(GND)	23	NC	No connect	
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28 U/D Up/down selection; ; Default U/D=L Note 2,3 29 XL Left electrode 30 YU Top electrode 31 XR Right electrode 32 YD Bottom electrode 33 VLED- Power for LED Driver IC(GND) 34 VLED- Power for LED Driver IC(GND)	26	RST	Hardware reset NUFACTURE • 5U	PPLY
29     XL     Left electrode       30     YU     Top electrode       31     XR     Right electrode       32     YD     Bottom electrode       33     VLED-     Power for LED Driver IC(GND)       34     VLED-     Power for LED Driver IC(GND)	27	L/R	Left / right selection; Default L/R=H	Note 2,3
30 YU Top electrode 31 XR Right electrode 32 YD Bottom electrode 33 VLED- Power for LED Driver IC(GND) 34 VLED- Power for LED Driver IC(GND)	28	U/D	Up/down selection; ; Default U/D=L	Note 2,3
31 XR Right electrode 32 YD Bottom electrode 33 VLED- Power for LED Driver IC(GND) 34 VLED- Power for LED Driver IC(GND)	29	XL	Left electrode	
32 YD Bottom electrode 33 VLED- Power for LED Driver IC(GND) 34 VLED- Power for LED Driver IC(GND)	30	YU	Top electrode	
33 VLED- Power for LED Driver IC(GND) 34 VLED- Power for LED Driver IC(GND)	31	XR	Right electrode	
34 VLED- Power for LED Driver IC(GND)	32	YD	Bottom electrode	
` '	33	VLED-	Power for LED Driver IC(GND)	
	34	VLED-	Power for LED Driver IC(GND)	
	35	VLED+	Power for LED Driver IC(+5V)	
36 VLED+ Power for LED Driver IC(+5V)				

Note1: When select 8bit mode, DB0~DB7 be used, DB8~DB15 no connect When select 16bit mode, DB0~DB15 be used

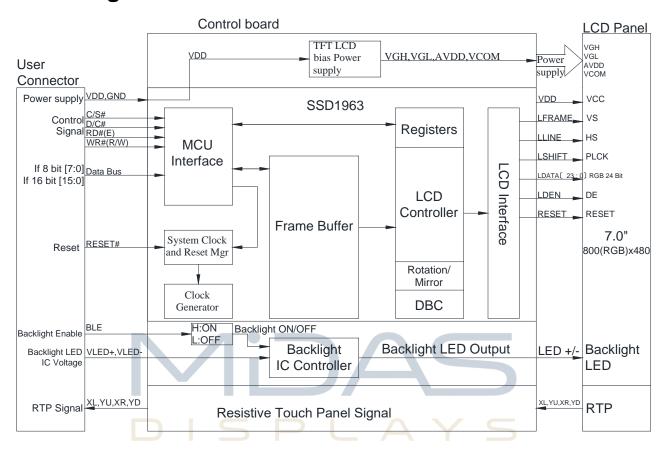
Note 2: Selection of scanning mode

Setting of scar	n control input	Scanning direction					
U/D	L/R						
GND	VDD	Up to down, left to right					
VDD	GND	Down to up, right to left					
GND	GND	Up to down, right to left					
VDD	VDD	Down to up, left to right					

Note 3: Definition of scanning direction. Refer to the figure as below:



## **Block Diagram**



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## **Absolute Maximum Ratings**

Item	Symbol	Min	Тур	Max	Unit
Operating Temperature	TOP	-20	_	+70	°C
Storage Temperature	TST	-30	_	+80	°C

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

 Temp. ≤60°C, 90% RH MAX. Temp. >60°C, Absolute humidity shall be less than 90% RH at 60°C

### **Electrical Characteristics**

1. Operating conditions: (CON2.Pin1=GND, Pin2=VDD)

Item	Symbol	Condition	Min	Тур	Max	Unit	Remark
Supply Voltage For LCM	VDD	_	3.0	3.1	3.3	V	_
Supply Current For LCM	IDD	_	_	300	450	mA	Note1

Note 1 : This value is test for VDD=3.3V , Ta=25°C only

2. Backlight driving conditions (CON2.Pin33,34=VLED-, Pin35,36=VLED+)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Operation Current For LED Driver	VLED=5V	400	_	600	mA	Note 1,2
Power Consumption	VLED=5V	2000	_	3000	mW	Note 1,2
Supply Voltage For LED Driver	VLED+	_	5	_	V	_
LED Life Time	_	_	50,000	_	Hr	Note 2,3,4

Note 1: Base on VLED= 5V for the back light driver IC specification

Note 2 : Ta = 25 °C

Note 3: Brightness to be decreased to 50% of the initial value

Note 4: The single LED lamp case

DISPLAYS

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#### DC CHARATERISTICS

Parameter	Symbol		Rating	Unit	Condition	
1 drameter	Cymbol	Min	Тур	Max	Ome	Oonanion
Low level input voltage	VIL	0	-	0.3VDD	V	
High level input voltage	VIH	0.7VDD	-	VDD	V	

# Interface timing

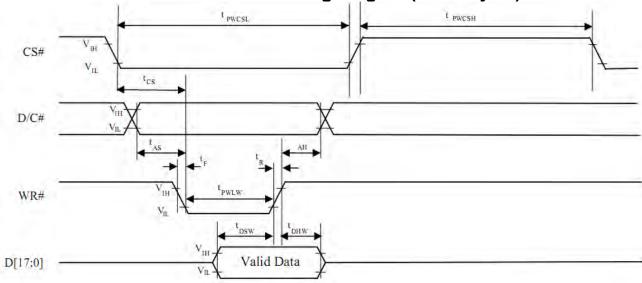
#### 1.8080 Mode

The 8080 mode MCU interface consist of CS#, D/C#, RD#, WR#, Data Bus. This interface use WR# to define a write cycle and RD# for read cycle. If the WR# goes low when the CS# signal is low, the data or command will be latched into the system at the rising edge of WR#. Similarly, the read cycle will start when RD# goes low and end at the rising edge of RD#.

2. 8080 Mode Write Cycle

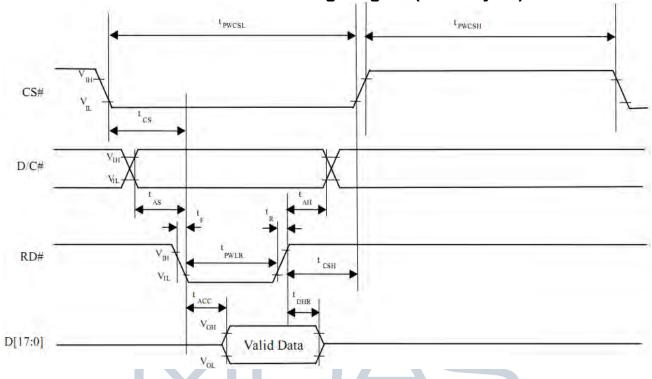
Symbol	Parameter Parameter	Min	Тур	Max	Unit
fMCLK	System Clock Frequency	1	-	110	MHz
tMCLK	System Clock Period	1/ fMCLK	-	-	ns
tPWCSH	Control Pulse High Width Write Read	13 30	1.5* tMCLK 3.5* tMCLK	-	ns
tPWCSL	Control Pulse Low Width Write (next write cycle) Write (next read cycle) Read	13 80 80	1.5* tMCLK 9* tMCLK 9* tMCLK	-	ns
tAS	Address Setup Time	1	-	-	ns
tAH	Address Hold Time	2	-	-	ns
tDSW	Write Data Setup Time	4			ns
tDHW	Write Data Hold Time	1	-	-	ns
tPWLW	Write Low Time	12			ns
tDHR	Read Data Hold Time	1	-	-	ns
tACC	Access Time	32			ns
tPWLR	Read Low Time	36	\ \ -	-	ns
tR	Rise Time	-		0.5	ns
tF	Fall Time	-	-	0.5	ns
tCS	Chip select setup time	2		_	ns
tCSH	Chip select hold time to read signal	3	-	-	ns

### 3. Parallel 8080-series Interface Timing Diagram(Write Cycle)



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## 4. Parallel 8080-series Interface Timing Diagram(Read Cycle)



### 5. Pixel Data Format

Interface	Cycle	D[15]	D[14]	D[13]	D[12]	D[11]	D[10]	D[9]	D[8]	D[7]	D[6]	D[5]	D[4]	D[3]	D[2]	D[1]	D[0]
16 bits (565 format)	1 <sup>st</sup>	R5	R4	R3	R2	R1	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1
	1 <sup>st</sup>	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0
16 bits	2 <sup>nd</sup>	B7	B6	B5	B4	В3	B2	B1	В0	R7	R6	R5	R4	R3	R2	R1	R0
	3 <sup>rd</sup>	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	ВЗ	B2	B1	B0
8 bits	1 <sup>st</sup>				-					R7	R6	R5	R4	R3	R2	R1	R0
	2 <sup>nd</sup>									G7	G6	G5	G4	G3	G2	G1	G0
	3 <sup>rd</sup>									B7	B6	B5	B4	ВЗ	B2	B1	В0

# **Optical Characteristics**

Item		Symbol	Condition.	Min	Тур.	Max.	Unit	Remark
Dognongo timo		Tr	θ=0°、Ф=0°	-	10	20	.ms	Note 3
Response time		Tf		-	15	30	.ms	Note 3
Contrast ratio		CR	At optimized viewing angle	400	500	ı	ı	Note 4
Color	I White	Wx	θ=0°、Ф=0	0.26	0.31	0.36		Note 2,5,6
Chromaticity		Wy		0.28	0.33	0.38		
Viewing angle	Hor.	ΘR	CR≧10	-	75	-	Deg.	Note 1
(Gray Scale	пог.	ΘL		-	75	-		
Inversion	sion Ver	ΦТ		-	75	-		
Direction)		ΦВ		-	75	ı		
Brightness		-	-	150	250	-	cd/m <sup>2</sup>	Center of display

Ta=25±2°C, VLED / ILED= 5V / 400mA

Note 1: Definition of viewing angle range

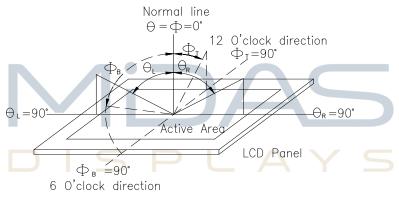


Fig.11.1. Definition of viewing angle

Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7 or BM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.

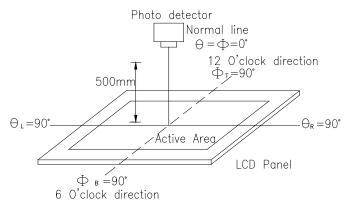
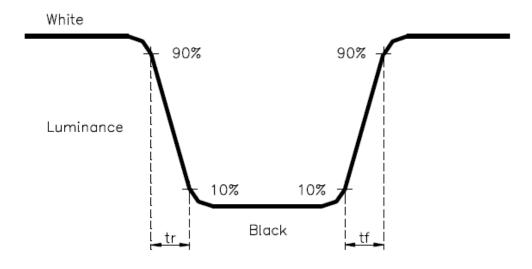


Fig. Optical measurement system setup

Note 3: Definition of Response time:

Definition of response time: The response time is defined as the time interval between the 10% and 90% amplitudes.



Note 4: Definition of contrast ratio:

The contrast ratio is defined as the following expression.

Contrast ratio (CR) = Luminance measured when LCD on the "White" state

Luminance measured when LCD on the "Black" state

Note 5: White  $Vi = Vi50 \pm 1.5V$ 

Black  $Vi = Vi50 \pm 2.0V$ 

"±" means that the analog input signal swings in phase with VCOM signal.

"±" means that the analog input signal swings out of phase with VCOM signal.

The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

Note 6: Definition of color chromaticity (CIE 1931)

Color coordinates measured at the center point of LCD

Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

## Reliability

Content of Reliability Test (Wide temperature, -20°C~70°C)

Environmental Test			
Test Item	Content of Test	Test Condition	Note
High Temperature	Endurance test applying the high storage temperature	80°C	2
storage	for a long time.	200hrs	
Low Temperature	Endurance test applying the low storage temperature	-30°C	1,2
storage	for a long time.	200hrs	
High Temperature	Endurance test applying the electric stress (Voltage &	70°C	
Operation	Current) and the thermal stress to the element for a	200hrs	
	long time.		
Low Temperature	Endurance test applying the electric stress under low	-20°C	1
Operation	temperature for a long time.	200hrs	
High Temperature/	The module should be allowed to stand at	60°C,90%RH	1,2
Humidity Operation	60°C,90%RH max	96hrs	
Thermal shock	The sample should be allowed stand the following 10	-20°C/70°C	
resistance	cycles of	10 cycles	
	operation		
	-20°C 25°C 70°C		
	30min 5min 30min 1 cycle		
Vibration test	Endurance test applying the vibration during	Total fixed amplitude :	3
	transportation and using.	1.5mm	
		Vibration Frequency :	
		10~55Hz	
	DISPLAY	One cycle 60	
		seconds to 3	
		directions of X,Y,Z for	
Otatia alaatuiaitu te-t	Englishers to the multiple the collection stress to the	Each 15 minutes	
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±600V(contact)	
	lemma.	,±800v(air),	
DESIC	N • MANUFACTURE	RS=330Ω	Y
		CS=150pF	
		10 times	

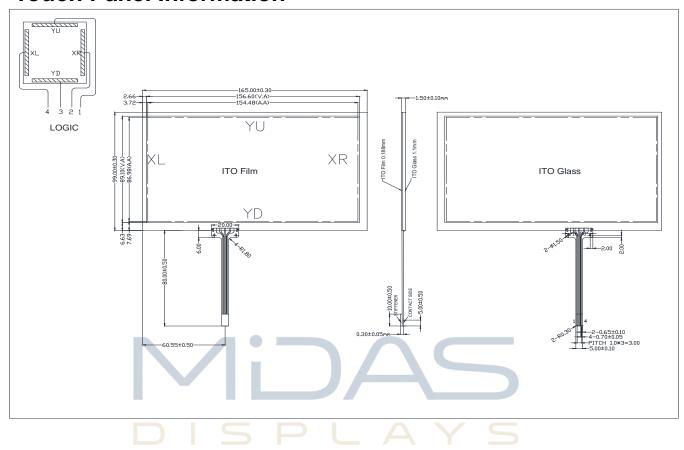
Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal

Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

# **Touch Panel Information**



#### **DESIGN • MANUFACTURE • SUPPLY**

**Resistance Touch Panel General Specifications** 

Item	Description		
Driving condition	DC3~7V		
Operating force	30~80g		
Linearity max	≤±1.5%		
Insulating resistance	$>$ 10M $\Omega$ , 25V(DC)		
Light transparence	70%		
Structure type	ITO Film/ITO Glass(F/G)		
Surface Hardness	3H typ		
Pen Hitting Durability (with the silicon rubber)	>1000,000 times		
X Axis resistance	430~910Ω		
Y Axis resistance	150~530Ω		

#### Initial Code For Reference void Initial SSD1963() Write\_Command(0x01); Delay\_ms(10); Write Command(0xe0); //START PLL Write Parameter(0x01); Delay\_ms(50); Write\_Command(0xe0); //START PLL Write Parameter(0x03): Delay\_ms(5); Write\_Command(0xb0); Write\_Parameter(0x20); Write Parameter(0x80); Write Parameter(0x03); Write Parameter(0x1f); Write Parameter(0x01); Write Parameter(0xdf): Write\_Parameter(0x00); Write\_Command(0xf0); Write\_Parameter(0x03); //pixel data format, 0x03 is 16bit(565 format);0x00 is for 8-bit //Set the MN of PLL Write Command(0xe2); Write\_Parameter(0x1d); Write Parameter(0x02); Write Parameter(0x54); MANUFACTURE • SUPPLY DESIGN Write\_Command(0xe6); Write Parameter(0x04): Write\_Parameter(0x6f); Write\_Parameter(0x47); //Set front porch and back porch Write Command(0xb4); Write Parameter(0x04); Write Parameter(0x20); Write\_Parameter(0x00); Write Parameter(0x2e); Write Parameter(0xd2): Write Parameter(0x00): Write Parameter(0x00): Write Parameter(0x00); Write Command(0xb6); Write Parameter(0x02); Write Parameter(0x0d);

```
Write_Parameter(0x00);
 Write_Parameter(0x17);
 Write_Parameter(0x16);
 Write_Parameter(0x00);
 Write_Parameter(0x00);
Write Command(0x2a);
Write_Parameter(0x00);
Write_Parameter(0x00);
Write_Parameter(0x03);
Write_Parameter(0x1f);
Write_Command(0x2b);
Write_Parameter(0x00);
Write_Parameter(0x00);
Write Parameter(0x01);
Write_Parameter(0x1f);
 Write_Command(0xb8);
 Write_Parameter(0x0f);
 Write_Parameter(0x01);
 Write_Command(0xba);
 Write_Parameter(0x01);
 Write Command(0x29);
 Write_Command(0x2c);
```

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