# VS-VSUD510CW60

**Vishay Semiconductors** 



FRED Pt<sup>®</sup>, Ultrafast Soft Recovery Diode Module, 500 A



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub> 500 A					
V <sub>R</sub>	600 V				
Q <sub>rr</sub> (typical)	1770 nC				
t <sub>rr</sub>	270 ns				
Туре	Modules - diode, FRED Pt®				
Package	TO-244				
Circuit configuration	Two diodes common cathode				

### **FEATURES**

- Ultrafast recovery
- · Designed for industrial level
- UL approved file E222165
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **BENEFITS**

- Reduced RFI and EMI
- Higher frequency operation
- Reduced snubbing
- · Reduced parts count

### **DESCRIPTION / APPLICATIONS**

FRED Pt® diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for HF welding, power converters and other applications where switching losses are significant portion of the total losses.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V <sub>R</sub>		600	V	
	I <sub>F(DC)</sub>	T <sub>C</sub> = 25 °C	619		
Continuous forward current per diode		T <sub>C</sub> = 85 °C	438		
		T <sub>C</sub> = 134 °C	250	A	
Single pulse forward current per diode	I <sub>FSM</sub>	T <sub>C</sub> = 150 °C	4000		
Maximum power dissipation	PD	T <sub>C</sub> = 25 °C	937	w	
		T <sub>C</sub> = 134 °C	253		
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-40 to +175	°C	

<b>ELECTRICAL SPECIFICATIONS PER LEG</b> ( $T_J$ = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	YMBOL TEST CONDITIONS		TYP.	MAX.	UNITS	
Breakdown voltage	V <sub>BR</sub>	V <sub>BR</sub> I <sub>R</sub> = 200 μA		-	-		
Forward voltage	V <sub>FM</sub>	I <sub>F</sub> = 250 A	-	0.97	1.170		
		I <sub>F</sub> = 500 A	-	1.08	-	V	
		$I_F = 250 \text{ A}, T_J = 175 \text{ °C}$ - 0.		0.82	-		
		I <sub>F</sub> = 500 A, T <sub>J</sub> = 175 °C	-	0.97	-		
Reverse leakage current	I <sub>RM</sub>	$T_J = 175 \text{ °C}, V_R = V_R \text{ rated}$	-	0.33	-	mA	

Revision: 24-Jul-2023 Document Number: 97093 1 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000



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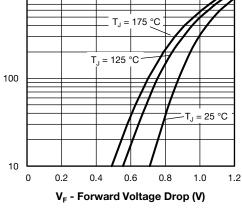
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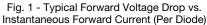
<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS			TYP.	MAX.	UNITS
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 50 A, dI <sub>F</sub> /dt = 50 A/μs, V <sub>B</sub> = 400 V	-	270	-	ns
		T <sub>J</sub> = 150 °C		-	590	-	
Peak recovery current	I <sub>RRM</sub>	$T_J = 25 \ ^{\circ}C$		-	9	-	A
		T <sub>J</sub> = 150 °C		-	22	-	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	1770	-	nC
		T <sub>J</sub> = 150 °C		-	7820	-	

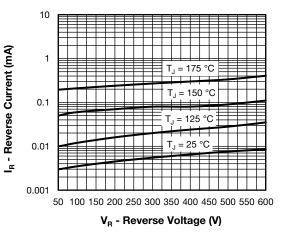
THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS
Thermal resistance,	per leg		-	-	0.16	
junction to case	per module	R <sub>thJC</sub>	-	-	0.08	°C/W
Thermal resistance, case to heatsink		R <sub>thCS</sub>	-	0.1	-	
Weight			-	68	-	g
			-	2.4	-	oz.
Mounting torque			30 (3.4)	-	40 (4.6)	
Mounting torque center hole Terminal torque			12 (1.4)	-	18 (2.1)	lbf · in (N · m)
			30 (3.4)	-	40 (4.6)	(14 11)
Vertical pull 2" lever pull			-	-	80	lhf in
			-	-	35	lbf ∙ in
Case style			TO-244			

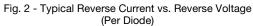


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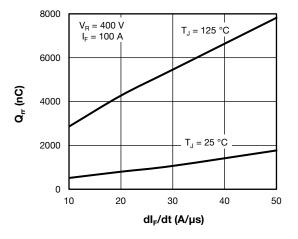


Fig. 3 - Typical Reverse Recovery Charge vs dI<sub>F</sub>/dt (Per Diode)

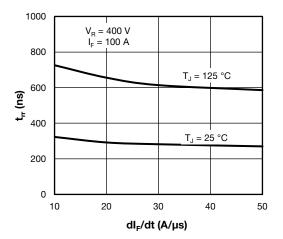


Fig. 4 - Typical Reverse Recovery Time vs dI<sub>F</sub>/dt (Per Diode)

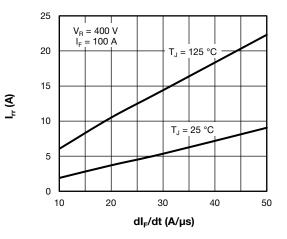
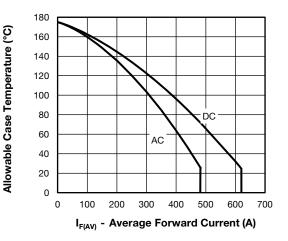
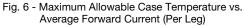
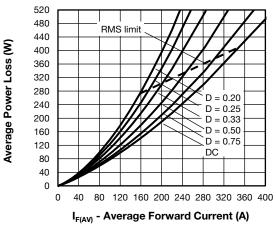
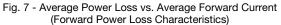


Fig. 5 - Typical Reverse Recovery Current vs. dl<sub>F</sub>/dt (Per Diode)









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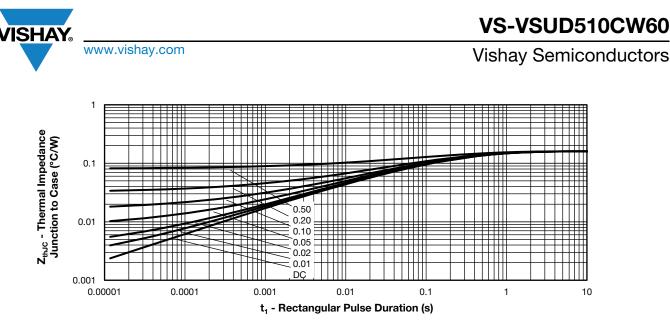


Fig. 8 - ZthJC - Maximum Thermal Impedance Junction to Case vs. t1 Rectangular Pulse Duration

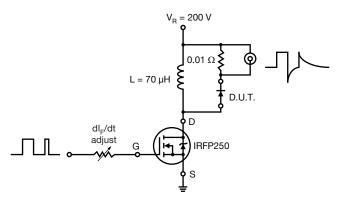
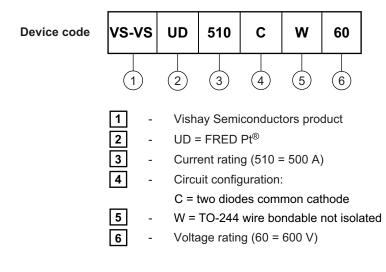


Fig. 9 - Reverse Recovery Parameter Test Circuit

#### **ORDERING INFORMATION TABLE**



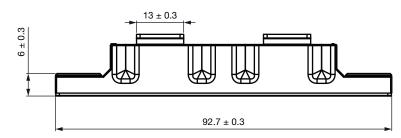


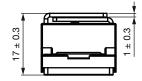


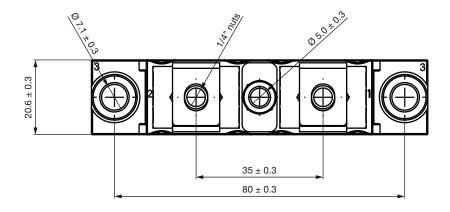
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CIRCUIT CONFIGURATION				
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING		
Two diodes common cathode	С	Lug Lug terminal anode 1 anode 2 Base common cathode		

#### **DIMENSIONS** in millimeters (inches)









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