

EVALUATION BOARD BASIC INFORMATION ⁽²⁾

Evaluation Board PN	Typical VCC Supply Voltage (V)	Rated Primary Current (A)	Typical Sensitivity (mV/A)	/OCD Trigger Point (A)
EVCS1805-S-305-B-00A	3.3	±5	264	±5
EVCS1805-S-320-B-00A	3.3	±20	66	±20
EVCS1805-S-330-B-00A	3.3	±30	44	±30
EVCS1805-S-340-B-00A	3.3	±40	33	±40
EVCS1805-S-350-B-00A	3.3	±50	26.4	±50
EVCS1805-S-510-B-00A	5	±10	200	±10
EVCS1805-S-520-B-00A	5	±20	100	±20
EVCS1805-S-540-B-00A	5	±40	50	±40

Note:

2) Contact an MPS FAE for additional variants.

QUICK START GUIDE

1. Preset the DC power supply to 3.3V or 5V, then turn the power supply off.
2. Connect the DC power supply terminals to:
 - a. Positive (+): VCC
 - b. Negative (-): GND
3. Connect the current source load terminals to:
 - a. Positive (+): IP+
 - b. Negative (-): IP-
4. Turn on the DC power supply and current source, then measure the output result via the VOUT pin.
5. If over-current detection (OCD) is required, measure the /OCD signal via the /OCD pin.

EVALUATION BOARD SCHEMATIC

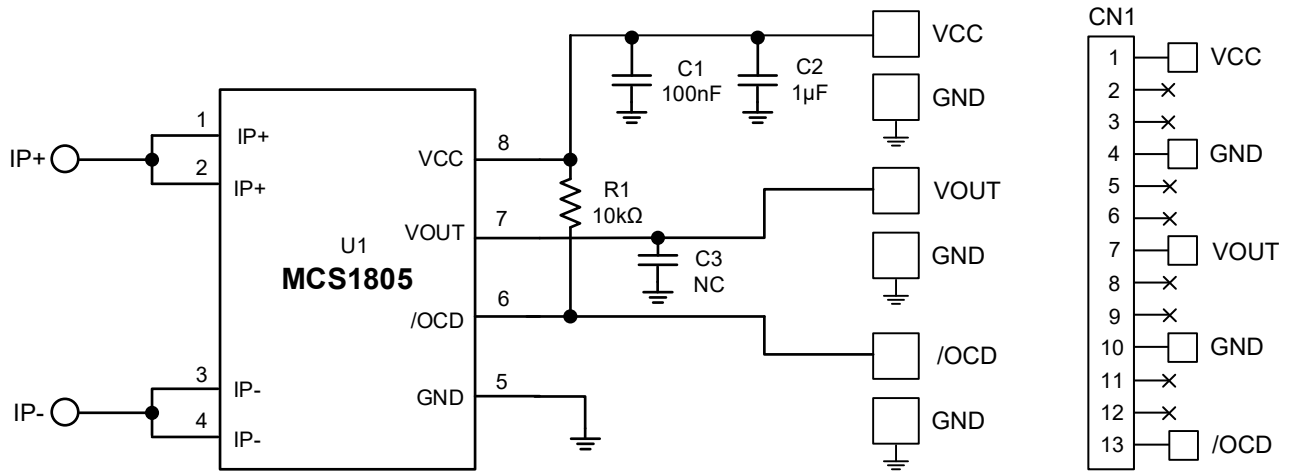


Figure 1: Evaluation Board Schematic

EVCS1805-S-00A BILL OF MATERIALS

Qty	Ref	Value	Description	Package	Manufacturer	Manufacturer PN
1	C1	100nF	VCC ceramic decoupling capacitor, 16V, X7R	0603	Murata	GRM188R71C104KA01D
1	C2	1 μ F	VCC ceramic decoupling capacitor, 16V, X7R	0805	Murata	GRM21BR71C105KA01L
1	C3	NC				
1	R1	10k Ω	/OCD pull-up resistor	0603	Yageo	RC0603FR-0710KL
1	CN1	2.54mm	Male pin header, 13-pin	DIP	Custom	
1	U1	MCS1805	Linear Hall-effect current sensor with OCD	SOIC-8	MPS	MCS1805GS-ABB-CDDD

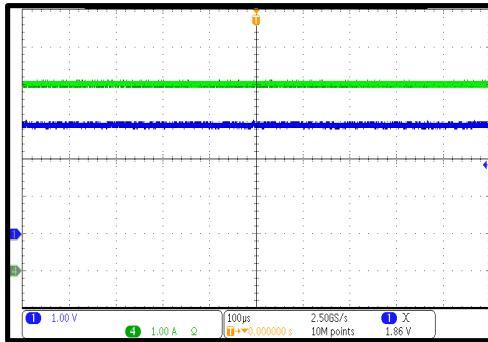
EVB TEST RESULTS

Performance waveforms are tested on the EVCS1805-S-305-B-00A evaluation board (see the Evaluation Board Basic Information section on page 2) with an /OCD trigger point at 5A. $V_{CC} = 3.3V$, C3 is open, $T_A = 25^\circ C$, unless otherwise noted.

DC Current Status

$I_P = 5A$

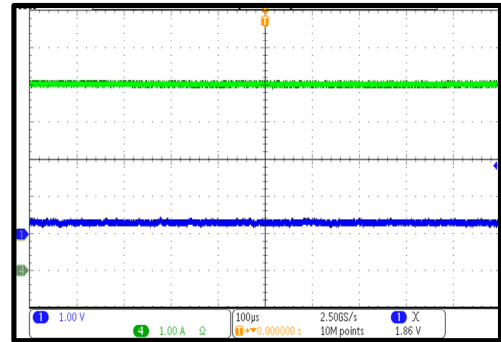
CH1: V_{OUT}
CH4: I_P



DC Current Status

$I_P = -5A$

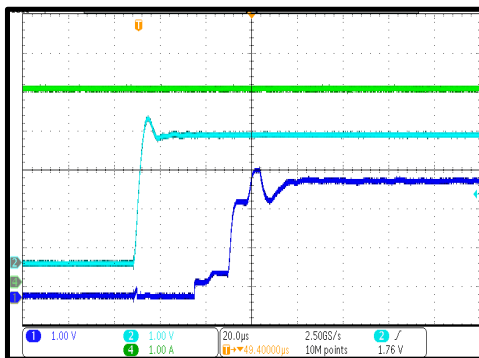
CH1: V_{OUT}
CH4: I_P



Start-Up through VCC

$I_P = 5A$

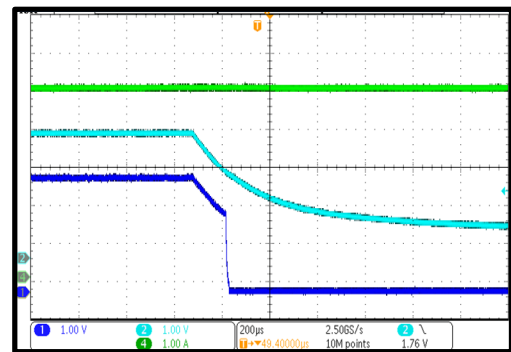
CH2: V_{CC}
CH4: I_P
CH1: V_{OUT}



Shutdown through VCC

$I_P = 5A$

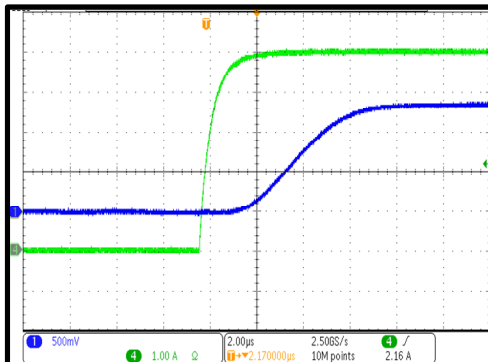
CH2: V_{CC}
CH4: I_P
CH1: V_{OUT}



Step-Up Current

$I_P = 5A$

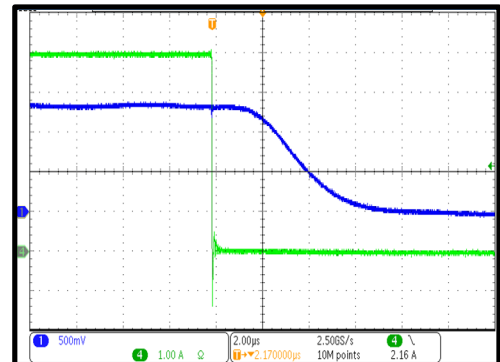
CH1: $V_{OUT}/$
1.65V offset
CH4: I_P



Step-Down Current

$I_P = 5A$

CH1: $V_{OUT}/$
1.65V offset
CH4: I_P

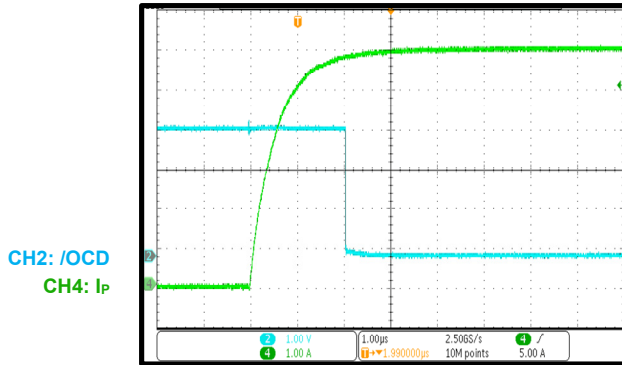


EVB TEST RESULTS *(continued)*

Performance waveforms are tested on the EVCS1805-S-305-B-00A evaluation board (see the Evaluation Board Basic Information section on page 2) with an /OCD trigger point at 5A. $V_{CC} = 3.3V$, C3 is open, $T_A = 25^{\circ}C$, unless otherwise noted.

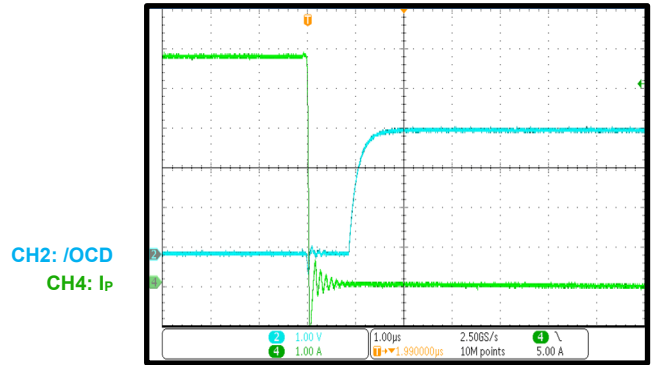
/OCD Response

$I_P = 20\%$ above /OCD trigger point



/OCD Recover

$I_P = 20\%$ above /OCD trigger point



PCB LAYOUT

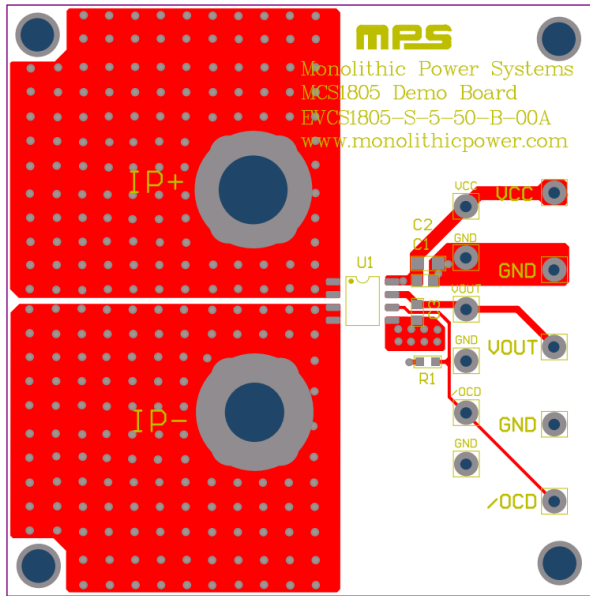


Figure 2: Top Silk and Top Layer

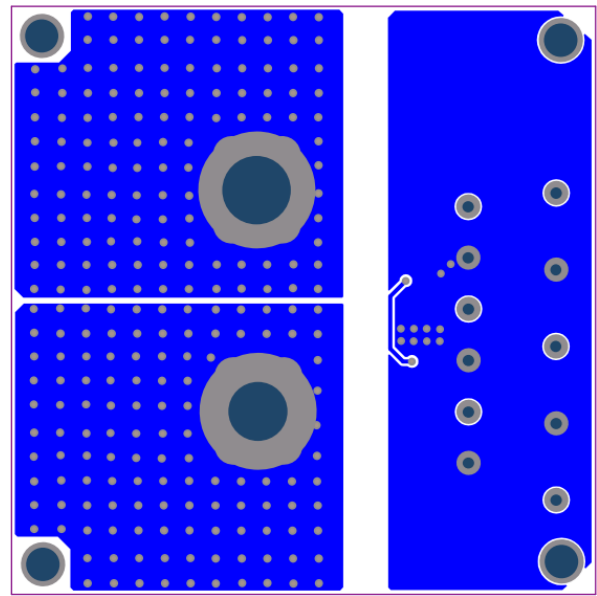


Figure 3: Bottom Layer



REVISION HISTORY

Revision #	Revision Date	Description	Pages Updated
1.0	7/8/2024	Initial Release	-

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