# Low Quiescent Current LDO







#### **Description**

This series is a high accuracy, high input voltage low quiescent current, high speed, and low dropout Liner regulator with high ripple rejection. The device is manufactured with Bi-CMOS process.

This offers over-current limit and over temperature protection to ensure the device working in well conditions

#### **Specification**

Supply Voltage : 4.75V to 40V
Output Range : 1.8V to 10V
Output Accuracy : <±2%

Output Current : 250mA (Up to 500mA Typ.)

PSRR : 50dB @ 100Hz

Dropout Voltage :  $850 \text{mV} \otimes \text{Iout} = 250 \text{mA}$ Quiescent Current :  $6 \mu \text{A} \otimes \text{Vin} = 7 \text{V} (\text{Typ.})$ 

Recommend Capacitor : 10µF

### **Absolute Maximum Ratings Ta = 25°C**

Parameter	Rating
Power Dissipation	Internal limited (mW)
V <sub>IN</sub> Range	-0.3V to 45V
Vouт Range	-0.3V to 10V
Lead Temperature Range	260°C
Storage Temperature Range	-55°C to 150°C
Operating Junction Temperature Range	125°C
ESD MM	400V
ESD HBM (V)	4K

## Recommended Operating Conditions Ta = 25°C

Parameter	Rating
Operating Supply Voltage	4.75V to 40V
Operating Temperature Range	-40°C to +85°C
Thermal Resistance(On PCB),R JA	43.5°C/W
Power Dissipation	1000mW

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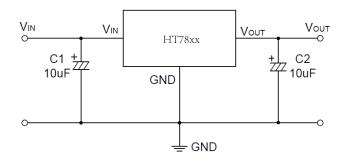
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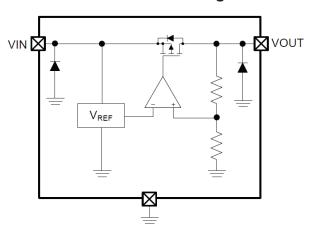
#### Electrical Characteristics (Ta=25 C,VIN=12V,CIN=COUT=10uF,unless otherwise noted)

Parameter Name	Symbol	Test Conditions		Min	Тур	Max	Unit	
Input Range	Vin	Ιουτ=10mA		4.75		40		
Output Voltage	Vоит	VIN=12V, IOUT=10mA	HT7830	2.94	3	3.06	V	
			HT7833	3.234	3.3	3.366		
			HT7850	4.9	5	5.1		
Maximum Output Current	Іоит_рк	VIN=12V, RL=1Ω			500		mA	
Quiescent Current	ΙQ	V <sub>IN</sub> =7V, No load			6	8	μА	
		V <sub>IN</sub> =24V, No load			7.5	10		
		V <sub>IN</sub> =40V, No load			10	15		
Dropout Voltage		Іоит=100mA			2	12		
	VDROP	Iоит=100mA			300	400	mV	
		lv=250mA			850	1200		
Line Degulation	V <sub>1</sub> =7~24V,V <sub>0</sub> u <sub>T</sub> =5V, l <sub>0</sub> u <sub>T</sub> =1mA		1	υт <b>=1mA</b>		0.02		0/ /\/
Line Regulation	Lnr	VIN=7~45V, VOUT=5V,IC	VIN=7~45V, VOUT=5V,IOUT=1mA		0.1		%/V	
Load Damidation	1	V <sub>IN</sub> =12V, I <sub>OUT</sub> =1~100mA			0.6		%	
Load Regulation	LDR	VIN=7V, IOUT=1~250mA	V <sub>IN</sub> =7V, I <sub>OUT</sub> =1~250mA		2			
Output Noise	eno	Iouт=10mA		-100		100	μV	
Ripple Rejection	PSRR	V <sub>IN</sub> =10V V <sub>PP</sub> =0.5V I <sub>OUT</sub> =1mA	f=100Hz		50		dB	
			f=1KHz		40			
			f=10KHz		30			
Thermal Protection	Tsp	Vin=12V, Iout=1mA		ĺ	155		°.	
Thermal Protection Hys	Tsd_Hys	VIN=12V, IOUT=1mA		ĺ	30		°C	
Temperature Coefficient	ΔVο/ΔΤ	VIN=12V, IOUT=1mA			±0.1		mV/°C	

### **Typical Application**



### **Functional Block Diagram**



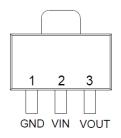
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#### **Pin Configuration**



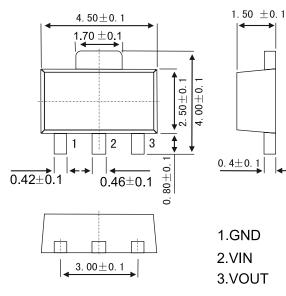
#### **Pin Description**

Pin Number	Pin Name Function Descrip	
1	GND	Ground
2	Vin	Voltage Input
3	Vout	Voltage Output

#### Marking

HT7830	H4VR
HT7833	H4YR
HT7850	H4PR

#### Diagram



#### **Part Number Table**

Description	Part Number
Low Quiescent Current LDO, 3V, SOT-89	HT7830
Low Quiescent Current LDO, 3.3V, SOT-89	HT7833
Low Quiescent Current LDO, 5V, SOT-89	HT7850

**Dimensions: Millimetres** 

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