

Product Change Notification: MFOL-14IYNS822

Date:

19-Mar-2025

Product Category:

8-Bit Microcontrollers

Notification Subject:

CCB 7234 Initial Notice: Qualification of MTAI as an additional final test site for selected ATMEGA3250, ATMEGA3250P, ATMEGA3290P, ATMEGA3290P, ATMEGA6450, and ATMEGA6490 device families available in 100L TQFP (14x14x1mm) package.

Affected CPNs:

MFOL-14IYNS822_Affected_CPN_03192025.pdf MFOL-14IYNS822_Affected_CPN_03192025.csv

PCN Status: Initial Notification

PCN Type: Manufacturing Change

Microchip Parts Affected: Please open one of the files found in the Affected CPNs section. Note: For your convenience Microchip includes identical files in two formats (.pdf and .xls)

Description of Change: Qualification of MTAI as an additional final test site for selected ATMEGA3250, ATMEGA3250P, ATMEGA3290P, ATMEGA3290P, ATMEGA6450, and ATMEGA6490 device families available in 100L TQFP (14x14x1mm) package.

Pre and Post Summary Changes:

		Pre Change	Post Change				
Final Test Site		Orient Semiconductor Electronics, Ltd (OSE)	Orient Semiconductor Electronics, Ltd (OSE)	Microchip Technology Thailand (HQ) (MTAI)			
Pin 1 Orientation		No change.					
1/K	BQM	No change.	No change.				

Carrier TapeNo changeCover TapeWith minor		No change.
		With minor dimensional change. See Pre and Post summary for comparison.
	Reel	With dimensional change. See Pre and Post change for comparison.
Trav	Pin 1 Orientation	No change.
Iray	BQM	No change.
Packing Method		See Pre and Post change summary for comparison.

Impacts to Datasheet: None

Change Impact: None

Reason for Change: To improve manufacturability by qualifying MTAI as an additional final test site.

Change Implementation Status: In Progress

Estimated Qualification Completion Date: June 2025

Note: Please be advised the qualification completion times may be extended because of unforeseen business conditions however implementation will not occur until after qualification has completed and a final PCN has been issued. The final PCN will include the qualification report and estimated first ship date. Also note that after the estimated first ship date guided in the final PCN customers may receive pre and post change parts.

Timetable Summary:

	March 2025			>	June	2025					
Work Week	09	10	11	12	13		23	24	25	26	27
Initial PCN Issue Date				Х							
Qual Report Availability							X				
Final PCN Issue Date							X				

Method to Identify Change: Traceability Code

Qualification Plan: Please open the attachments included with this PCN labeled as PCN_#_Qual_Plan.

Revision History: March 19, 2025: Issued initial notification.

Note: The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable product.

Attachments:

PCN_MFOL-14IYNS822_Pre and Post_Change_Summary.pdf PCN_MFOL-14IYNS822_Qual Plan.pdf

Please contact your local Microchip sales office with questions or concerns regarding this notification.

Terms and Conditions:

If you wish to <u>receive Microchip PCNs via email</u> please register for our PCN email service at our <u>PCN</u> <u>home page</u> select register then fill in the required fields. You will find instructions about registering for Microchips PCN email service in the <u>PCN FAQ</u> section.

If you wish to <u>change your PCN profile</u>, <u>including opt out</u>, please go to the <u>PCN home page</u> select login and sign into your myMicrochip account. Select a profile option from the left navigation bar and make the applicable selections.

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Affected Catalog Part Numbers (CPN)

ATMEGA3250V-8AUR

ATMEGA3290V-8AU

ATMEGA6490-16AU

ATMEGA3290P-20AUR

ATMEGA3250-16AUR

ATMEGA3250PV-10AUR

ATMEGA6450V-8AUR

ATMEGA3290PV-10AUR

ATMEGA6450-16AU

ATMEGA3250P-20AU

ATMEGA6490V-8AU

ATMEGA3290-16AU

ATMEGA3290PV-10AU

ATMEGA3290PV-10AUA0

ATMEGA6450-16AUR

ATMEGA3250V-8AU

ATMEGA3290-16AUR

ATMEGA3250-16AU

ATMEGA3250PV-10AU

ATMEGA3290V-8AUR

ATMEGA6490-16AUR

ATMEGA6450V-8AU

ATMEGA3290P-20AU

ATMEGA6490V-8AUR

ATMEGA3250P-20AUR



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Tray – BQM and Pin 1 Orientation





Tray - Drawing





Tray - Packing Method

	OSE	MTAI		
Antistatic Shielding Bag / Moisture Barrier Bag	Image: Non-Width Length Width 280 mm	MSL 1LengthWidth560 mm280 mm	Image: Market of the systemMSL 3LengthKidth560 mm280 mm	
Desiccant	EXCREMENT AND USE THE OWNER AN	N/A MSL 1	MUSAWAG De Galaxy of Heat SECTIALISA VILO - SARET TETE IS SECTIALISA VILO - SARET TETE IS CONTENTS - INFORMATION DISTORTING - INFORMATION DISTORT - INFOR	



Tray - Packing Method





Tape and Reel – BQM and Pin-1 Orientation

	OSE	ΜΤΑΙ
Base Quantity Multiple (BQM)	1500	1500
Pin 1 orientation	Quadrant 1	I I <td< th=""></td<>



Tape and Reel – Carrier Tape



Plant	W (mm.) ±0.3 or Specific	P (mm.) ±0.1 or Specific	A0 ±0.1 or Specific	B0 ±0.1 or Specific	K0 ±0.1 or Specific	K1 ±0.1 or Specific
OSE	24 mm	20 mm	16.5 mm	16.5 mm	1.90 mm	1.55 mm
MTAI	24 mm	20 mm	16.5 mm	16.5 mm	1.90 mm	1.55 mm



Tape and Reel – Cover Tape



Plant	Width	Thickness	Sealing Method
OSE	21 mm	0.048 mm	Heat Seal
MTAI	21.0 ± 0.1mm	0.050±0.010mm	Heat Seal



Tape and Reel – Plastic Reel

OSE		MTAI		
				30
Disustan	11-1	14/4	14/0	Oslar

Plant	Diameter	Hub	W1	W2	Color
OSE	330 mm	102 mm	8.4 mm 16.4 mm	11.1 mm 19.1 mm	Blue
MTAI	330 mm	100 mm	24.40 mm	30.40 mm	Dark Blue



Tape and Reel - Packing Method

	OSE	MTAI		
Desiccant	Two units, 33g	N/A MSL 1	With Reference of the second of the secon	
Antistatic Shielding Bag / Moisture Barrier Bag	Herstein Herstein Length Width 455 mm 430 mm	LengthWidth420 mm370 mm	LengthWidth 370 mmMSL 3	



Tape and Reel - Packing Method







QUALIFICATION PLAN SUMMARY

PCN #: MFOL-14IYNS822

Date: March 10, 2025

Qualification of MTAI as an additional final test site for selected ATMEGA3250, ATMEGA3250P, ATMEGA3290, ATMEGA3290P, ATMEGA6450, and ATMEGA6490 device families available in 100L TQFP (14x14x1mm) package.



Purpose: Qualification of MTAI as an additional final test site for selected ATMEGA3250, ATMEGA3250P, ATMEGA3290, ATMEGA3290P, ATMEGA6450, and ATMEGA6490 device families available in 100L TQFP (14x14x1mm) package.

CCB No.: 7234

Test / Evaluation	Test Condition / Parameters	
Datalog Bin Comparison	 Compare test numbers, test names, test sequence bin assignments & pass/fail results Accept if all match or justify the differences 	
Site by Site Verification	 Verifies the channel map has the correct site assignments and tester/handler communications work correctly 	
Correlation Lot Report	Yield comparison between systems and test sites.Accept on yield match within 0.1%	
Test Stability	 50 loop test performed with no datalog delays Accept on 0 fails 	
Parametric Test Stability Verification	Use Real Time Statistics software	
Unit to unit parametric correlation	 A full assembly strip characterized on both systems and graphed vs each other & the data sheet limits 	