



🏠 > Adaptive SoCs and FPGAs > Evaluation Boards > Kintex™ UltraScale™ FPGA KCU1250

# AMD Kintex™ UltraScale™ FPGA KCU1250 Characterization Kit

by: [AMD](#)



The KCU1250 Characterization Kit provides everything you need to evaluate the 20 GTH 16.3Gbps transceivers available on the UltraScale™ XCKU040-FFVA1156 FPGA. Access to both the Integrated Bit Error Ratio Test (IBERT) demonstration and the Vivado™ Design Suite enables

Feedback



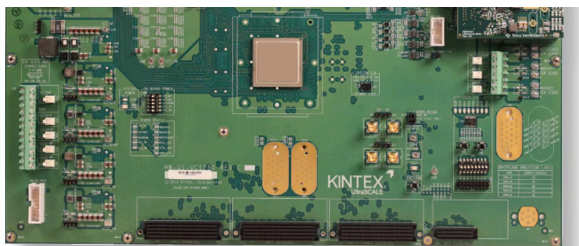


 Image Zoom

quick evaluation of the industry leading GTH transceivers. Each GTH Quad and its associated reference clock are routed from the FPGA to the BullsEye connector pad. This enables users to connect to a broad range of evaluation platforms, from backplanes and optical evaluation boards to high speed test equipment.

**Part Number:**

CK-U1-KCU1250-G

**Lead Time:** Discontinued

**Device Support:** Kintex UltraScale



**Discontinued:** This development kit has been discontinued per PDN advisory [XCN21011](#) and is no longer offered for sale. The solutions targeted for this product will not be updated moving forward with limited support available from AMD.

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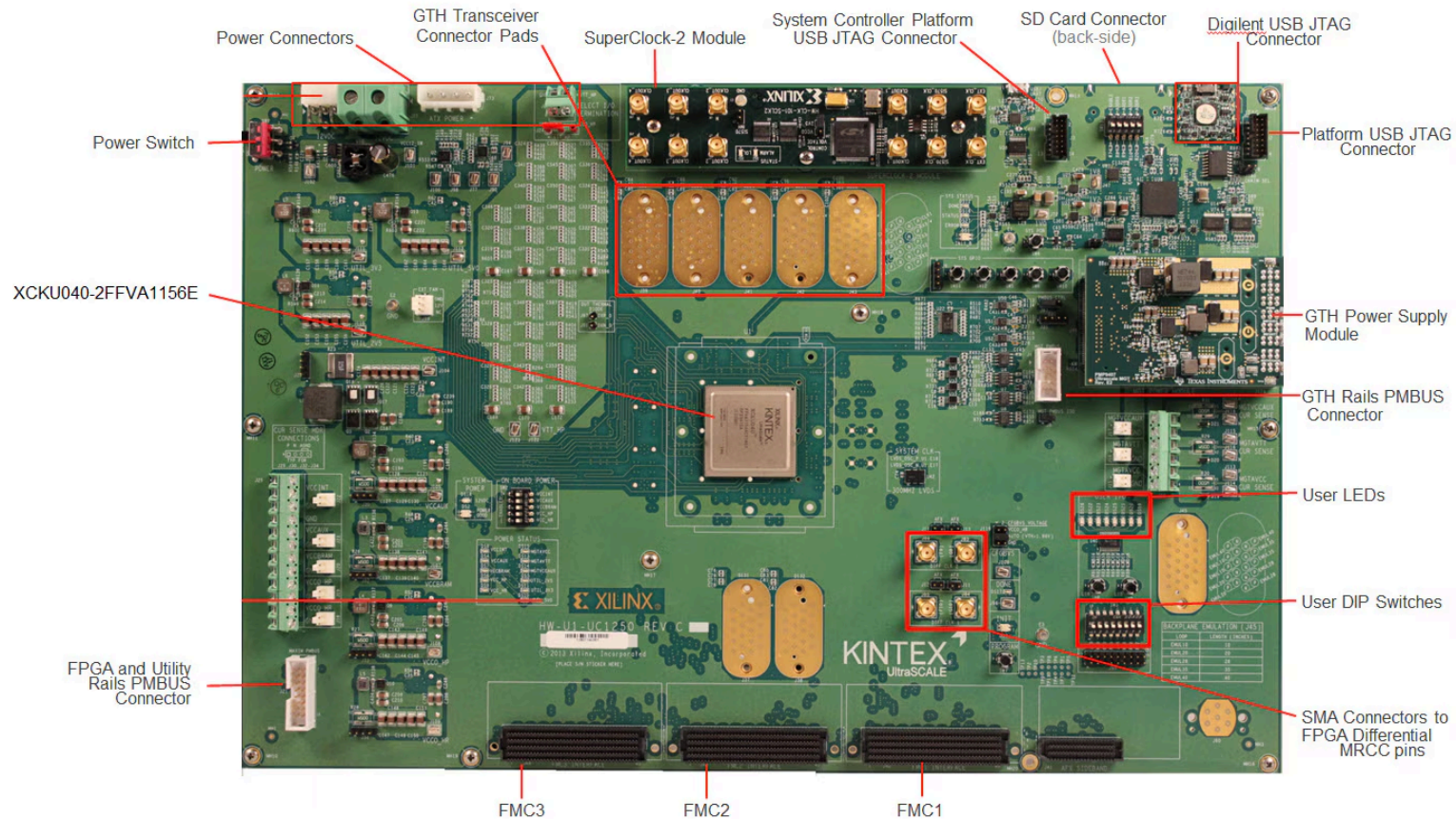
Features &amp; Devices

**Specifications**

What's Inside

## Board Features

### Featuring the Kintex UltraScale KCU1250 Characterization Board



**Communication  
& Networking**

**Clocking**

**Display**

**Expansion  
Connectors**



- Five Samtec BullsEye connector pads for interfacing to the 20 GTH transceivers and their associated reference clocks
- Two pairs of differential MRCC inputs with SMA connectors
- USB-to-UART bridge
- Fixed, 200 MHz 2.5V LVDS oscillator wired to multi-region clock capable (MRCC) inputs
- SuperClock-2 module supporting multiple frequencies
- Power status LEDs
- General purpose DIP switches, LEDs, push buttons, and test I/O
- Three VITA 57.1 FPGA mezzanine card (FMC) high pin count (HPC) connectors

## Memory

- SD controller

## Control & I/O

- I<sup>2</sup>C Bus

## Power

- PMBus connectivity to on-board digital power supplies

