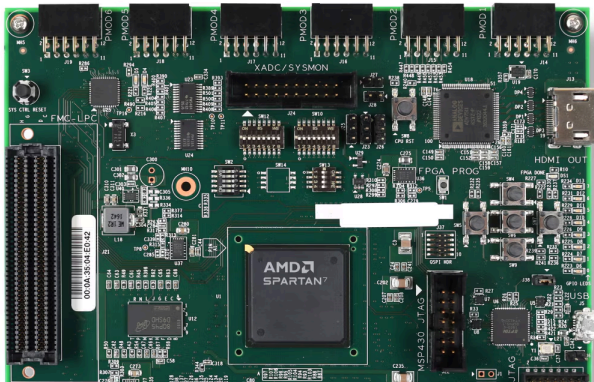




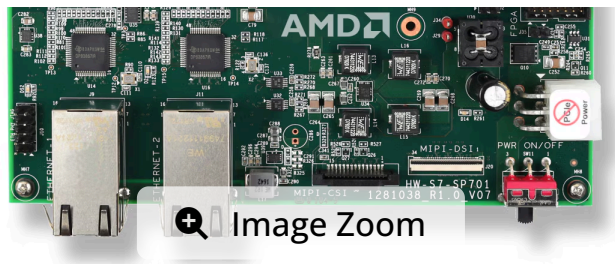
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# AMD Spartan™ 7 SP701 FPGA Evaluation Kit

by: [AMD](#)



The SP701 Evaluation Kit, equipped with the best-in-class performance-per-watt AMD Spartan™ 7 FPGA, is built for designs requiring sensor fusion such as industrial networking, embedded vision, and automotive applications. The SP701 features high I/O availability and I/O expansion capability via Pmods and FMC connectors, making it the largest IP development canvas for Spartan 7



FPGA users. The included XC7S100 FPGA is the highest density device of the Spartan 7 family. With 102K logic cells and 400 I/O pins, the XC7S100 pairs expandable connectivity with high processing capability, all in a form factor ideal for edge applications.

**Part Number:**

EK-S7-SP701-G

**Lead Time:** 6 weeks**Device Support:** Spartan 7[Add to Cart](#) or buy from: [Authorized Distributors](#)

## Product Information



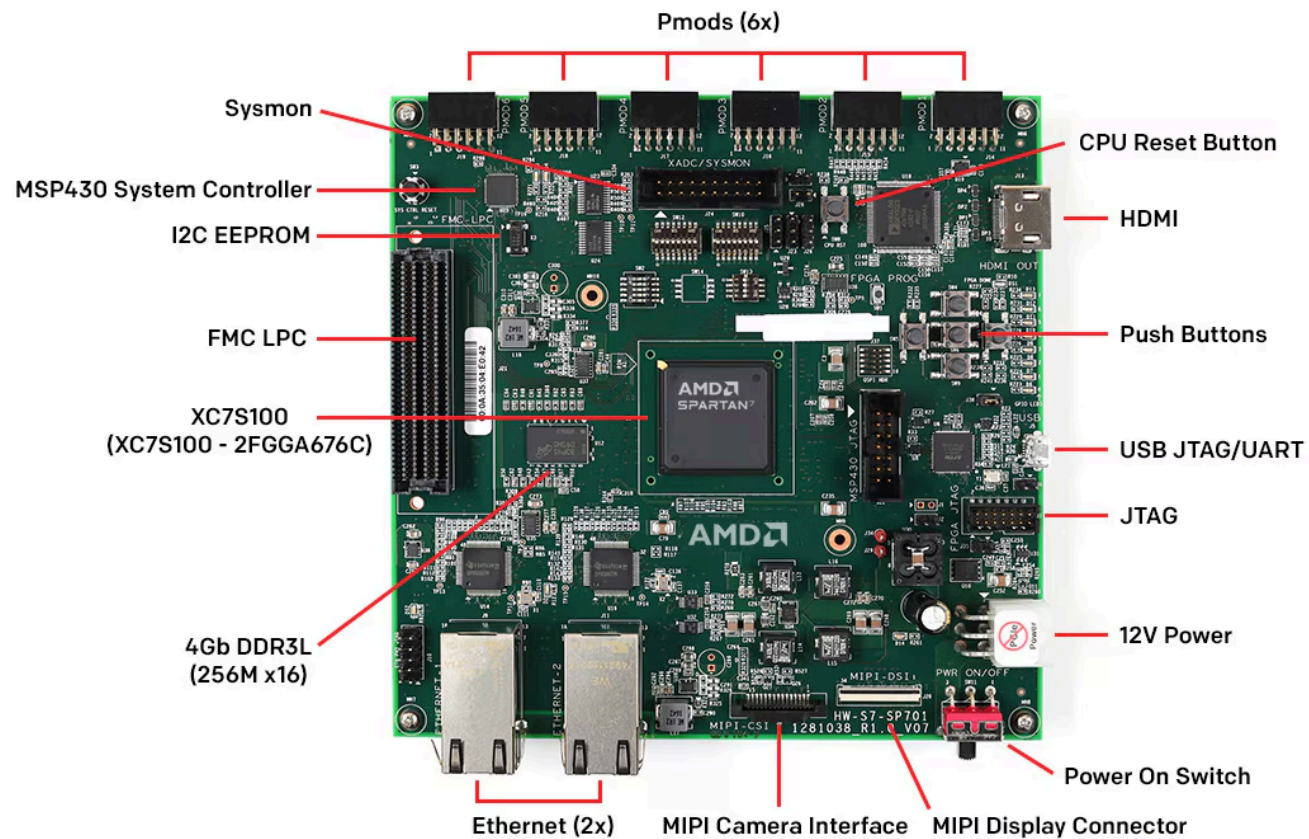
Features &amp; Devices

**Specifications**

What's Inside

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## Featuring the Spartan™ 7 XC7S100 FPGA

**Board Specifications****Value**

Length	6 inch (152.4 mm)
Height	6 inch (152.4 mm)
Thickness (+/-5%)	0.088 inch (2.24 mm)
Operating Environmental Temperature	0°C to +45°C
Storage Environmental Temperature	-25°C to +60°C

### Configuration

JTAG	Yes
SPI	Yes
QSPI	Yes

### Memory

DDR3L	4Gb (256M x 16)
Quad-SPI Flash	1Gb
I2C EEPROM	32Kb

### Control & I/O

User pushbuttons	5
DIP switches	16
User LEDs	8



System Controller	Yes
PMBUS	Yes

### Expansion Connectors

VITA 57.1 FMC LPC	1
Pmod Headers	6

### Communication & Networking

UART to USB Bridge	1
RJ-45 Tri-Speed Ethernet Connectors	2

### Display

HDMI Video Output	Yes
MIPI-CSI Camera Interface	Yes
MIPI-DSI Display Interface	Yes

### Clocking

I2C Programmable Clock	Yes
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### Power

12V Wall Adapter	Yes
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