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AMD Kintex™ 7 FPGA Embedded Kit

by: [AMD](#)

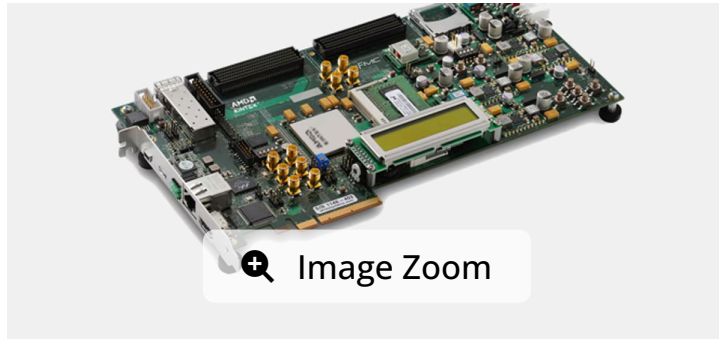


The Kintex™ 7 Embedded Kit includes the components of the Kintex 7 KC705 Base Evaluation Kit plus all additional soft content that embedded designers need to quickly design their high-performance embedded systems. This

Feedback

ON THIS PAGE





Part Number:

DK-K7-EMBD-G

Lead Time: Discontinued

Device Support: Kintex 7

i **Discontinued:** This evaluation kit has been discontinued and is no longer offered for sale. The solutions targeted for this product will not be updated moving forward with limited support available from AMD.

Product Information

Features & Devices

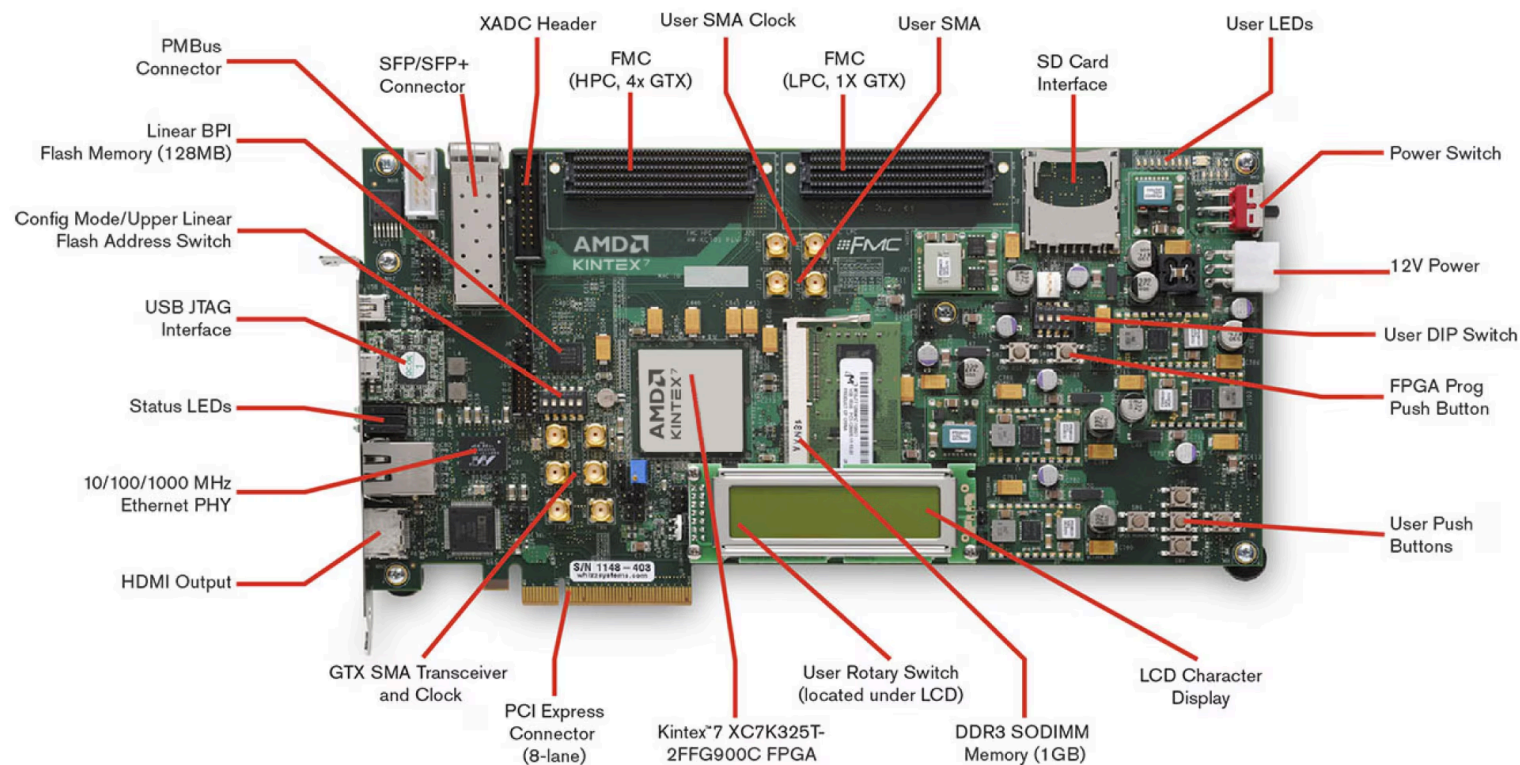
Specifications

What's Inside

Board Features

Featuring the Kintex 7 Embedded Board





Communication & Networking

- Gigabit Ethernet GMII, RGMII and SGMII
- SFP / SFP+ cage
- GTX port (TX, RX) with four SMA connectors

Clocking

Used to support CPRI/OBSAI applications that perform clock recovery from a user-supplied SFP/SFP+ module

Display

- HDMI video output
- External Phy/codec device driving an HDMI connector
- 2x16 LCD display
- 8x LEDs

Expansion Connectors

- FMC-HPC (partial population) connector (4 GTX transceiver, 116 single-ended, 58 differential)



- UART to USB bridge
- PCI Express x8 edge connector
- Fixed Oscillator with differential 200MHz output used as the “system” clock for the FPGA
- Programmable Oscillator with 156.250 MHz as the default output and frequency targeted for Ethernet applications but oscillator is programmable for many end uses
- Differential SMA clock input
- Differential SMA GTX reference clock input
- Jitter attenuated clock used to support CPRI/OBSAI LA & 24 HA) user defined signals)
- FMC-LPC connector (1 GTX transceiver, 68 single-ended or 34 differential user defined signals)
- Vadj can support 1.8V, 2.5V, or 3.3V
- IIC



applications that
perform clock
recovery from a
user-supplied
SFP/SFP+ module

Configuration

- Onboard JTAG configuration circuitry to enable configuration over USB
- JTAG header provided for use with AMD download cables such as the Platform Cable USB II
- 128MB (1024Mb) Linear BPI Flash for PCIe Configuration
- 16MB (128Mb) Quad SPI Flash

Memory

- 1GB DDR3 SODIMM 800MHz / 1600Mbps
- 128MB (1024Mb) Linear BPI Flash for PCIe® Configuration
- 16MB (128Mb) Quad SPI Flash
- 8Kb IIC EEPROM
- SD card slot

Control & I/O

- 5X push buttons
- 4X DIP switches
- Diff pair I/O (1 SMA pair)
- AMS FAN header (2 I/O)
- 7 I/O pins available through LCD header

Power

- 12V wall adapter or ATX
- Voltage and current measurement capability of 2.5V, 1.5V, and 1.2V, 1.0V supplies (IIC path to FPGA)

Analog

- XADC header

