2507071655 SiWG917 SoC Data Sheet v1.1 and Errata v0.3 Release

PCN Issue Date: Jul 07, 2025   **Effective Date:** Oct 13, 2025

PCN Type: Datasheet; Errata

Description of Change

Silicon Labs is pleased to announce the release of datasheet version 1.1 and errata version 0.3 for SiWG917 SoC devices.

SOFTWARE IMPACT DESCRIPTION:
Software changes for removing the 32-bit timer option are already part of 3.5.0 WiseConnect SDK release.

Links to data sheet and errata:
SiWG917 SoC data sheet: https://www.silabs.com/documents/public/data-sheets/siwg917-datasheet.pdf
SiWG917 SoC errata: https://www.silabs.com/documents/public/errata/siwg917-soc-ic-errata.pdf

Reason for Change

• Removed MPU (Memory protection Unit) section
• 32-bit configuration timer option is not supported & removed
• Modified SDIO from five functions to one functions
• Removed support for ADC start sync with PWM signal
• Updated ADC & DAC electrical specs in section 7.5.4 and 7.5.4
• Updated reference schematics for harmonics filtering and BLE Rx improvement
• MVP & AI/ML feature available as part of SDK 3.5.0 or above release and OPN support table 2.2 updated
• Updates to Reference schematics, BOM and layout guidelines section 8

Impact on Form, Fit, Function, Quality, Reliability

• Form – No change
• Fit – No change
• Quality – No change
• Reliability – No Change
• Function: There has been a change in ADC electrical specifications. Min/Max specs have been changed to Typical specs and ADC performance limitations below -20C have been added. For DAC, performance limitations below -20C have been added. Refer to sections 7.5.4 and 7.5.5 of Datasheet for details.

Minimum active current for MCU Sub-System Power Consumption in low-power and high performance mode has been updated in section 1.

Product Identification

```
Existing Part #
SIWG917M100MGTBA
SIWG917M100MGTBAR
SIWG917M110LGTBA
SIWG917M110LGTBAR
SIWG917M111MGTBA
SIWG917M111MGTBAR
SIWG917M111XGTBA
SIWG917M111XGTBAR
SIWG917M121XGTBA
SIWG917M121XGTBAR
```

```
SIWG917M141XGTBA
SIWG917M141XGTBAR
```

Last Date of Unchanged Product: Oct 13, 2025

Qualification Samples

N/A

Customer Response

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at https://www.silabs.com.
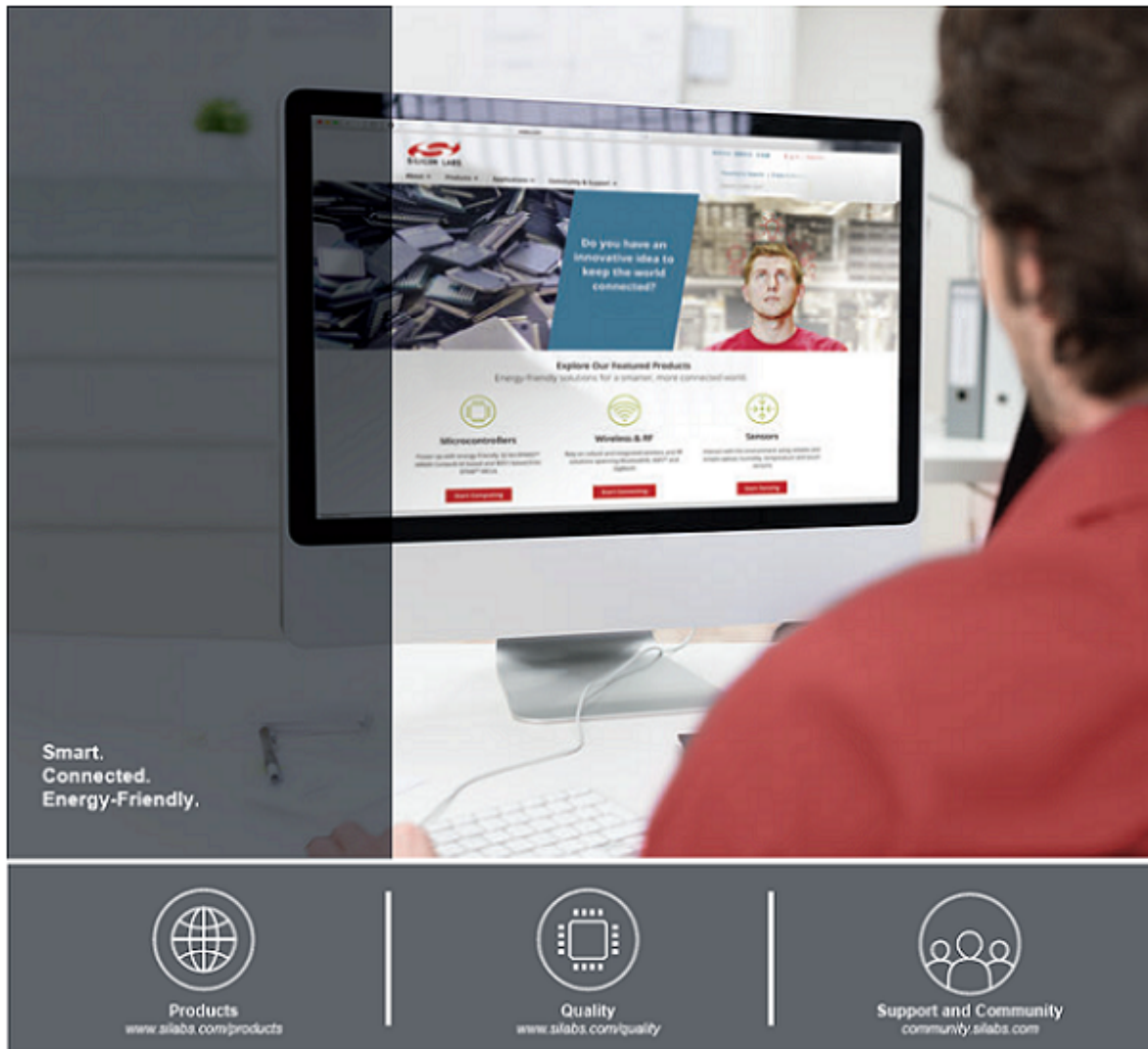
Customers may approve early PCN acceptance by emailing approval, along with PCN # to PCN@silabs.com

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