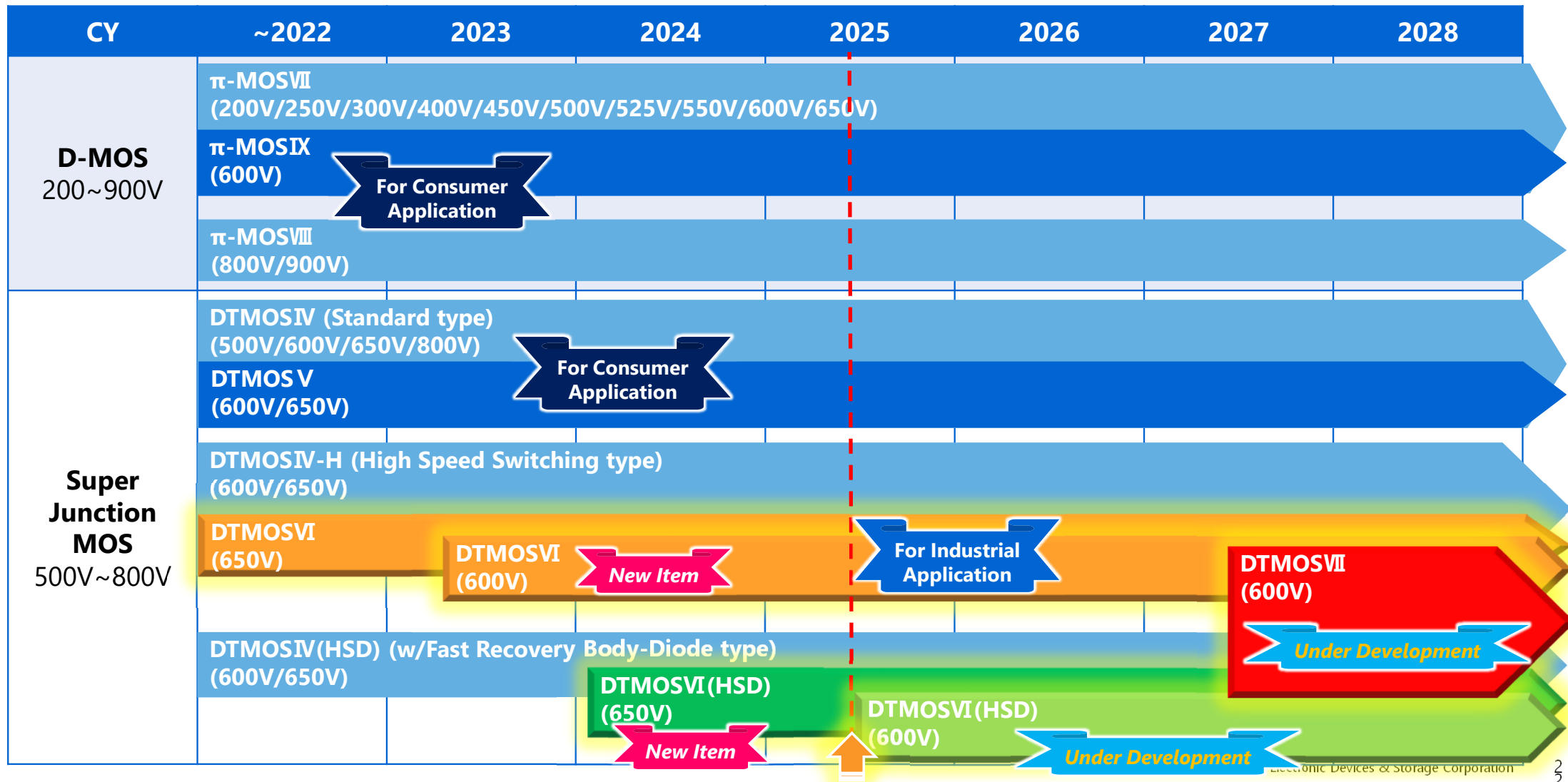


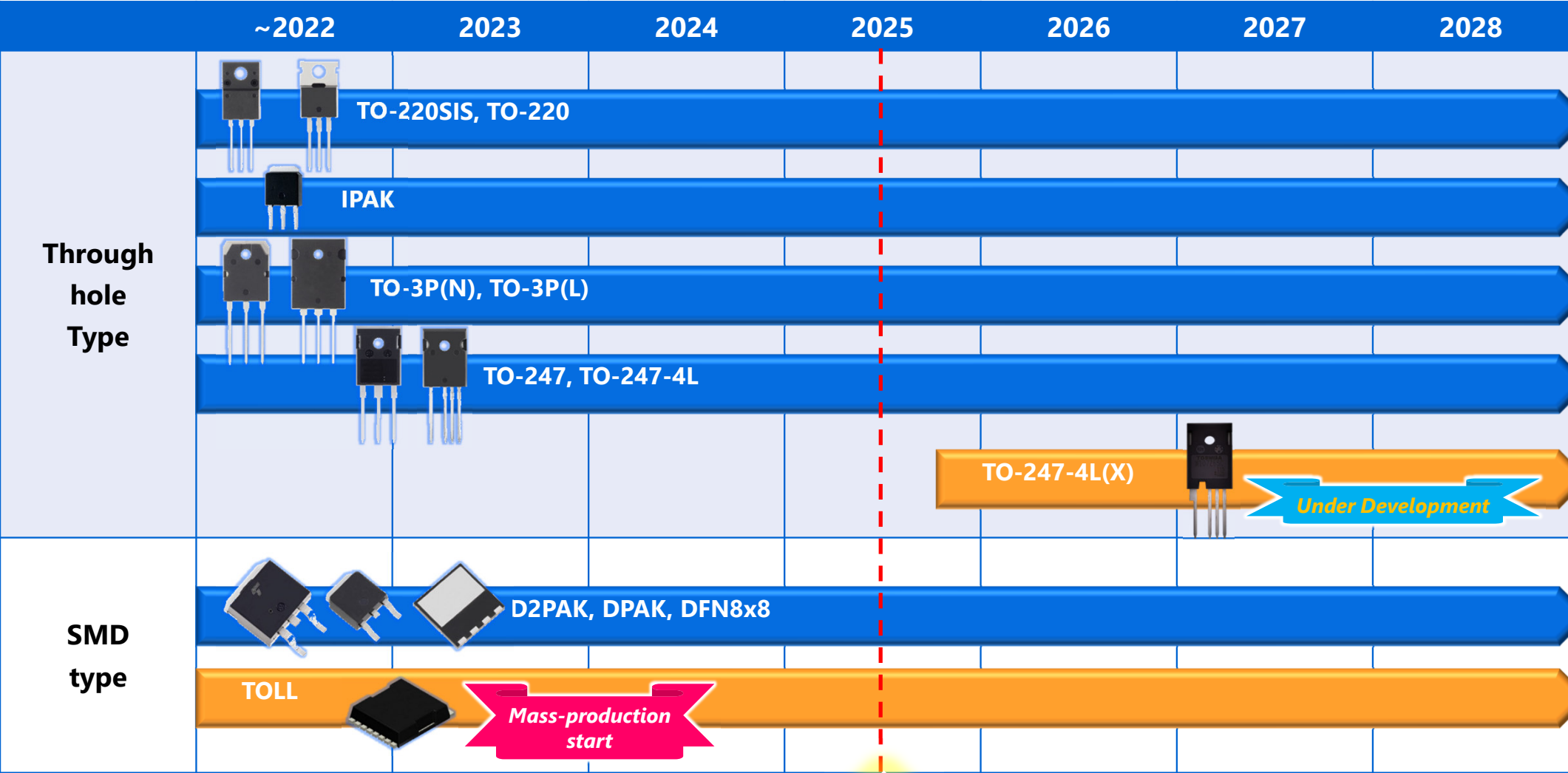
TOSHIBA 600V & 650V Mosfet.



Design Road Map for HVMOS ($V_{DSS}=400\sim 900V$)

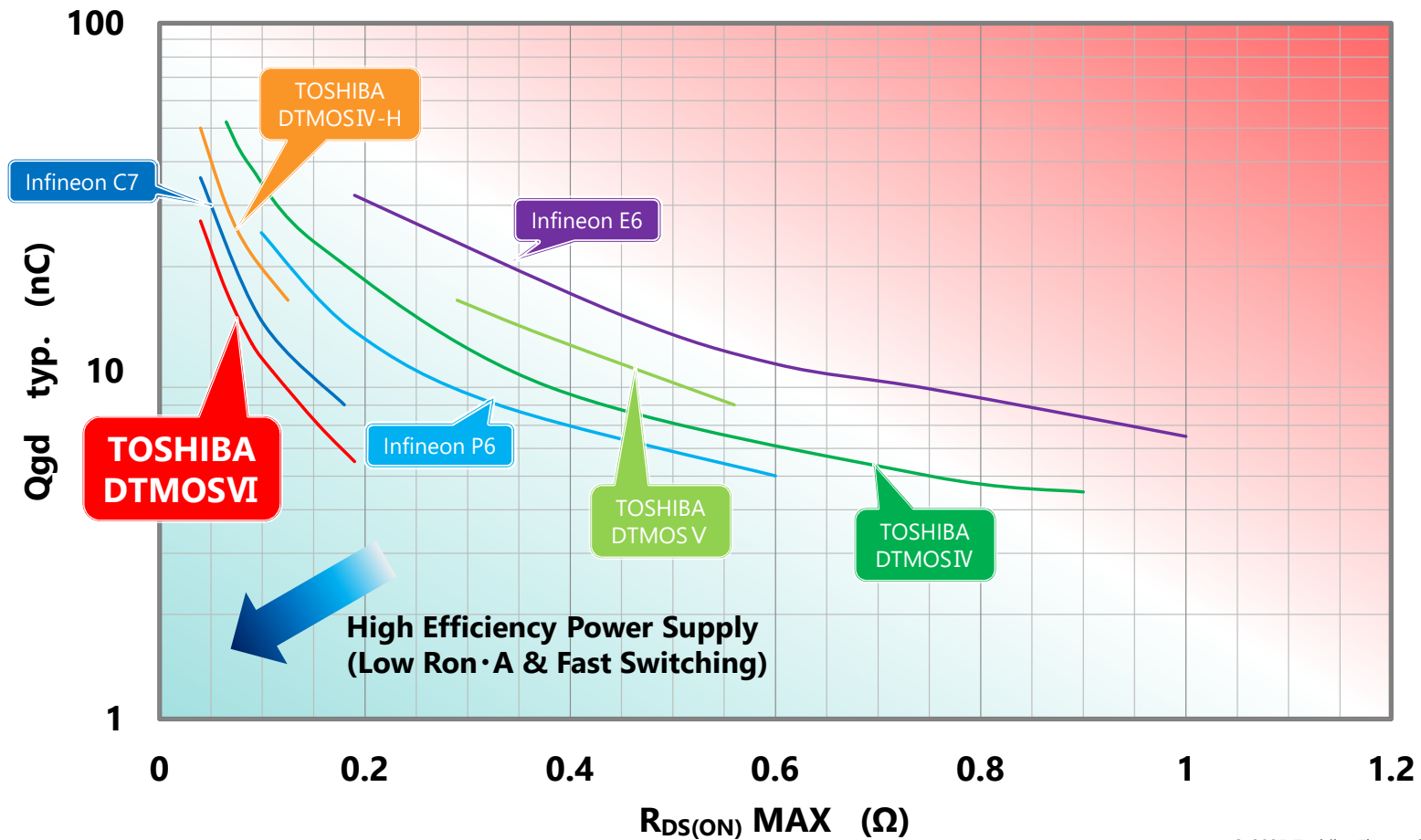


Package Road Map for HVMOS ($V_{DSS}=400\sim 900V$)



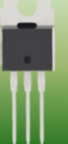
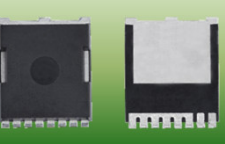
Development Concept for DTMOSVI series

DTMOSVI is targeting to catch up $R_{on} \times Q_{gd}$ of Infineon C7 series



DTMOSVI ($V_{DS}=650V$) series Lineup

* Schedule in CY

I_D (A)	$R_{DS(ON)}$ Max (Ω)	DFN8x8 	TO-220 	TO-220SIS 	TO-247 	TO-247-4L 	TOLL 	Q_g Typ. (nC)	C_{iss} Typ. ($V_{DS}=300V$) (pF)
15	0.19 / 0.21	TK210V65Z MP : OK	TK190E65Z MP : OK	TK190A65Z MP : OK			TK190U65Z MP : OK	25	1370
18	0.155 / 0.17	TK170V65Z MP : OK	TK155E65Z MP : OK	TK155A65Z MP : OK			TK155U65Z MP : OK	29	1635
24	0.11 / 0.125	TK125V65Z MP : OK	TK110E65Z MP : OK	TK110A65Z MP : OK	TK110N65Z MP : OK	TK110Z65Z MP : OK	TK110U65Z MP : OK	40	2250
30	0.090 / 0.099	TK099V65Z MP : OK	TK090E65Z MP : OK	TK090A65Z MP : OK	TK090N65Z MP : OK	TK090Z65Z MP : OK	TK090U65Z MP : OK	47	2780
38	0.065				TK065N65Z MP : OK	TK065Z65Z MP : OK	TK065U65Z MP : OK	62	3650
57	0.040				TK040N65Z MP : OK	TK040Z65Z MP : OK		105	6250



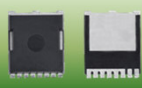
*1 : Please ask available sample qty to engineering section because sample qty is limited.

*2 : Regarding to detail schedule, please contact to sales representative.

Note : Specifications and schedule of under development is just target and is subject to change without notice.

DTMOSVI ($V_{DS}=600V$) series Lineup & Schedule Idea

* Schedule in CY

I_D (A)	$R_{DS(ON)}$ Max (Ω)	DFN8x8 	TO-220 	TO-220SIS 	TO-247 	TO-247-4L(X) 	TOLL 	Q_g Typ. (nC)	C_{iss} Typ. ($V_{DS}=300V$) (pF)
14	0.19 / 0.2	TK200V60Z1 CS : OK MP : 3Q 2025	TK190E60Z1 CS : OK MP : 3Q 2025	TK190A60Z1 CS : OK MP : 3Q 2025			TK190U60Z1 CS : OK MP : 3Q 2025	(20)	(1060)
17 / 16 ^{*1}	0.155 / 0.165	TK165V60Z1 MP : OK	TK155E60Z1 MP : OK	TK155A60Z1 MP : OK			TK155U60Z1 MP : OK	24	1350
20 / 18 ^{*1}	0.125 / 0.13	TK130V60Z1 MP : OK	TK125E60Z1 MP : OK	TK125A60Z1 MP : OK	TK125N60Z1 MP : OK	TK125Z60Z1 CS : OK MP : 3Q 2025	TK125U60Z1 MP : OK	28	1620
25 / 24 ^{*1}	0.099 / 0.105	TK105V60Z1 MP : OK	TK099E60Z1 MP : OK	TK099A60Z1 MP : OK	TK099N60Z1 MP : OK	TK099Z60Z1 CS : OK MP : 3Q 2025	TK099U60Z1 MP : OK	36	2050
30	0.080 / 0.085	TK085V60Z1 MP : OK	TK080E60Z1 MP : OK	TK080A60Z1 MP : OK	TK080N60Z1 MP : OK	TK080Z60Z1 CS : OK MP : 3Q 2025	TK080U60Z1 MP : OK	43	2510
37	0.063				TK063N60Z1 MP : OK	TK063Z60Z1 CS : OK MP : 3Q 2025		56	3200
40	0.055						TK055U60Z1 MP : OK	65	3680
52	0.040				TK040N60Z1 MP : OK	TK040Z60Z1 CS : OK MP : 3Q 2025		85	5200
80	0.024				TK024N60Z1 MP : OK	TK(024)Z60Z1 ^{(*)2} CS : OK MP : 3Q 2025		140	8420

Under
Investigation

Making
ES

ES
Available^{(*)2}

Order
Available^{(*)3}

Under
MP

^{*1} : I_D values differ only in the DFN8x8 package.

^{*2} : Please ask available sample qty to engineering section because sample qty is limited.

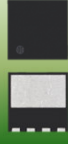
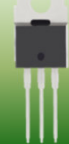
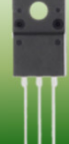
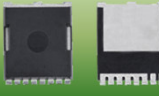
^{*3} : Regarding to detail schedule, please contact to sales representative.

Note : Specifications and schedule of under development is just target and is subject to change without notice.

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DTMOSVI(HSD) ($V_{DS}=650V$) series Lineup Idea

* Schedule in CY

I_D (A)	$R_{DS(ON)}$ max (Ω)	DFN8x8 	TO-220 	TO-220SIS 	TO-247 	TO-247-4L(X) 	TOLL 	Q_g Typ. (nC)	C_{iss} Typ. ($V_{DS}=300V$) (pF)	t_{rr} Typ. (ns)
15	0.200	TK200V65Z5 MP : OK	TK200E65Z5 MP : OK	TK200A65Z5 CS : OK MP : 2Q 2025			TK200U65Z5 CS : OK MP : 2Q 2025	26	1400	95
18	0.165	TK165V65Z5 MP : OK	TK165E65Z5 MP : OK	TK165A65Z5 CS : OK MP : 2Q 2025			TK165U65Z5 CS : OK MP : 2Q 2025	30	1660	100
24 / 23*1	0.115	TK115V65Z5 MP : OK	TK115E65Z5 MP : OK	TK115A65Z5 MP : OK	TK115N65Z5 MP : OK	TK115Z65Z5 CS : OK MP : 4Q 2025	TK115U65Z5 MP : OK	42	2280	110
29 / 28*1	0.095	TK095V65Z5 MP : OK	TK095E65Z5 MP : OK	TK095A65Z5 MP : OK	TK095N65Z5 MP : OK	TK095Z65Z5 CS : OK MP : 4Q 2025	TK095U65Z5 MP : OK	50	2880	115
37	0.068				TK068N65Z5 MP : OK	TK068Z65Z5 CS : OK MP : 4Q 2025	TK068U65Z5 MP : OK	68	3765	135
55	0.042				TK042N65Z5 MP : OK	TK042Z65Z5 CS : OK MP : 4Q 2025		105	6280	160

Under
Investigation

Making
ES

ES
Available(*2)

Order
Available(*3)

Under
MP

*1 : I_D values differ only in the DFN8x8 package.

*2 : Please ask available sample qty to engineering section because sample qty is limited.

*3 : Regarding to detail schedule, please contact to sales representative.

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